Basic Components

1. DAC (Larry)
2. OTA (Gary)
3. Analog Buffer (maybe)
DAC8a (8-bit DAC adjust)

10-12 bits if couple all out (using serial shift-in and registers would reduce requirements)
OTA for Fast performance (OTA)

4-pins (buffer DAC output)
4-pins (bond-pad), at least 1 separate with probe pads?
Needs simulation optimization (SCMOS)
Should pack into ~20um x 10um
Need to confirm simulation first