new bit/elf files for scrod/carrier

- there are several new versions of bit/elf firmware (some details below; more details on backup slide)
- it would be good to test the latest (especially items marked in blue) to see if there are problems before the start of phase3
  - future bugfixes and feature additions will build upon code that's already in the svn repo
  - testing the latest ASAP will mitigate delays that may be incurred if we have to fix old but undiscovered bugs

- scrod pl
  - 47 diff_term=true (helps with ttlost)
  - 48* ttySCROD (for debugging)

- carrier pl
  - 4e faster asic register interface (allows higher occupancy)
  - 4f no heap
  - 50 heapEnabled register (defaults to disabled)
  - 51 ttyCARRIER (for debugging)
  - 52* max_hits_per_asic register (defaults to 32 hits per asic)

- scrod ps
  - 59 no heap
  - 5a heapEnabled; CPU_FREQ increase (667 MHz -> 763 MHz; allows higher occupancy)
  - 5e* hard-coded DDR drive strengths for s16b [29,67]
  - 5f setting proper drive strengths for all boardstacks [29,67]; selectively alters it for a few

- carrier ps
  - 15 sets load=0/latch=26 on boot
  - 19 lowered timeouts on fast configuration
  - 1b* ttyCARRIER (for debugging)
detailed changes

- **scrod pl**
  - 47 diff_term=true (helps with ttlost)
  - 48 ttySCROD (for debugging)
- **carrier pl**
  - 4e faster asic register interface (allows higher occupancy)
  - 4f no heap
  - 50 heapEnabled register (defaults to disabled)
  - 51 ttyCARRIER (for debugging)
  - 52* max_hits_per_asic register (defaults to 32 hits per asic)

- **scrod ps**
  - 59 no heap
  - 5a heapEnabled; CPU_FREQ increase (667 MHz -> 763 MHz; allows higher occupancy); no longer needs entire ps7_init.tcl (just pll and clock)
  - 5c testRAM code built in; can run DDR tests in situ
  - 5d ttySCROD (for debugging)
  - 5e* hard-coded DDR drive strengths for s16b [29,67]
  - 5f setting proper drive strengths for all boardstacks [29,67]; selectively alters it for a few

- **carrier ps**
  - 15 sets load=0/latch=26 on boot
  - 16 no longer needs entire ps7_init.tcl (just pll and clock)
  - 19 lowered timeouts on fast configuration
  - 1b* ttyCARRIER (for debugging)