Belle II TOP firmware status

- immediate need is to combine three things:
  - feature extraction PS/ARM (Lynn/Ryan)
    - already in trunk (along with i2c/hum/temp code)
  - Region of Interest (RoI) (Luca) [plus trigger synchronizer (Eric)]
    - based off a branch with no svn history (the time-consuming part to undo)
  - StdLib upgrades
- made a new branch "CarrierE0E00021/experimental" starting from trunk@1313/1380, with StdLib snapshot and Eric's changes
- would like to ask a shifter to do a simple test: power cycle, program using 1D/21 bitfile, configure & take a short run, then power cycle, program using 26/28 bitfile (not yet in repo!), then configure & take a short run (will send email with details when this is ready)
- plan is to then add Luca's changes, do another test as above, then merge into trunk, then finally test again to confirm nothing broke