2011 cosmic ray test @ Nagoya electronics status

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PPNL
Lynn Wood
2011 cosmic ray test @ Nagoya
mechanical enclosure status
### 2011 cosmic ray test @ Nagoya

#### PCB status

<table>
<thead>
<tr>
<th>board name</th>
<th>quantity required for cosmic ray test</th>
<th>quantity in hand right now</th>
<th>quantity we expect to bring to Nagoya at the end of this week</th>
</tr>
</thead>
<tbody>
<tr>
<td>SCROD</td>
<td>4</td>
<td>5</td>
<td>5</td>
</tr>
<tr>
<td>interconnect</td>
<td>4</td>
<td>5</td>
<td>5</td>
</tr>
<tr>
<td>carrier0/1/2/3</td>
<td>16</td>
<td>20</td>
<td>20</td>
</tr>
<tr>
<td>IRS2_DC revB</td>
<td>64</td>
<td>1</td>
<td>40</td>
</tr>
<tr>
<td>front-front (painted)</td>
<td>4</td>
<td>0</td>
<td>5</td>
</tr>
<tr>
<td>front-back</td>
<td>4</td>
<td>2</td>
<td>5</td>
</tr>
<tr>
<td>SL10_HV revA2 (painted)</td>
<td>4</td>
<td>0</td>
<td>5</td>
</tr>
</tbody>
</table>

If all goes as planned, we will only be missing half of the IRS2_DC boards that we need; the rest will follow us via FedEx to Nagoya early next week.
2011 cosmic ray test @ Nagoya firmware block-diagram
2011 cosmic ray test @ Nagoya
firmware tasks/personnel mapping

- Lynn Wood (PNNL)
  - feedback loops for ASIC Wilkinson conversion, sampling rate and trigger width
- Gary Varner (Hawaii)
  - trigger bit stream collection and per-channel scaler accumulation
- Matt Andrew (Hawaii)
  - fiber optic data transfer (raw waveform and sideband data)
  - command decoding and implementation via fiber optic
- Xiaowen Shi (Hawaii)
  - $i^2c$ e$^2$prom SCROD identity reading/writing
  - $i^2c$ temperature sensor
- Kurtis Nishimura (Hawaii)
  - ASIC sampling control
  - ASIC data readout
  - overall synchronization (with FTSW as clock source)
  - overall firmware integration