Belle II bPID electronics status for cosmic test / beam-test

Instrumentation Development Lab
University of Hawai'i
bPID back-end electronics status

firmware tasks (Xin Gao, Serge Negrashov, Kurtis Nishimura, Matt Andrew):
  • Aurora data in from fiber optics
  • FPGA-FPGA interface
  • PCI interface
  • DSP interface (skipping this for now)

software tasks (Serge Negrashov & Andrew Wong):
  • linux kernel driver
  • “userland” library
  • “userland” test program (85%-92%)
    • “userland” program to raw data dump-to-disk for cosmic run / actual beam-test
bPID front-end electronics board stack status

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bPID front-end electronics board stack status

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these 4 boards are being fabricated now

1 of 2 board is in hand; 2 of 2 is being fabricated now

these 4 boards are in hand & assembled; at least partially tested and working