Belle II bPID electronics status

Instrumentation Development Lab
University of Hawai'i
bPID back-end electronics
status

COPPER2 with 3 DSPFIN boards

DSP_cPCI

Matt Andrew / University of Hawai'i

2011-06-03 US Belle II Firmware Meeting
bPID front-end electronics board stack development

2010 November:
- concept mechanical design

2010 November:
- mechanical mockup completed
bPID front-end electronics board stack development
SCROD timeline / status

2010 November:
• concept

2010 December to 2011 January:
• detailed design / schematic entry / layout

2011 February:
• fabrication & assembly

2011 March/April:
• FPGA programs and runs

2011 May:
• USB works!
• logic analyzer debug port works!
• remote programming (JTAG/LVDS) works!
• fiber-optic link works!
• I²C testing in progress...

Matt Andrew / University of Hawai‘i