



all banks 3.3V

+5V
GND
-5V
(only required for some toupees)

I2C0
(gpio0 and gpio1)
connected to EEPROM

althea revBL

Instrumentation Development Lab

mza 2020-10-07

High Energy Physics Group

University of Hawaii

toupee connector

toupee connector

+RAW

IIC

ROT

CB2

GND +50_05_ID1

PB [4]

[5] p33

BBR

FPGA

RESET

[0] p105

[1] p95 (GCLK)

[2] p85 (GCLK)

[3] p75

CLOCK100
p126 (GCLK)
p127

X p12

W p10

V p8

U p2

Y p15

B p124

X p12

W p10

V p8

U p2

A p117

B p124

X p12

W p10

V p8

U p2

C p132

D p140

E p134

F p142

G p40

H p43

[4] p6

pb p1

[4] p6

[5] p33

43

[5] p33

M p57

L p47

J p45

G p40

H p43

I p41

J p45

K p55

L p47

N p30

M p57

L p47

J p45

H p43

I p41

J p45

K p55

L p47

P p35

N p30

M p57

L p47

J p45

H p43

I p41

J p45

K p55

[4] p6

[5] p33

Q p33

P p35

N p30

M p57

L p47

J p45

H p43

I p41

J p45

[4] p6

[5] p33

R p27

Q p33

P p35

N p30

M p57

L p47

J p45

H p43

I p41

[4] p6

[5] p33

S p24 (GCLK)

R p27

Q p33

P p35

N p30

M p57

L p47

J p45

K p55

[4] p6

[5] p33

T p22 (GCLK)

S p24 (GCLK)

R p27

Q p33

P p35

N p30

M p57

L p47

K p55

[4] p6

[5] p33