

E

D

C

B

A

E

D

C

B

A

6

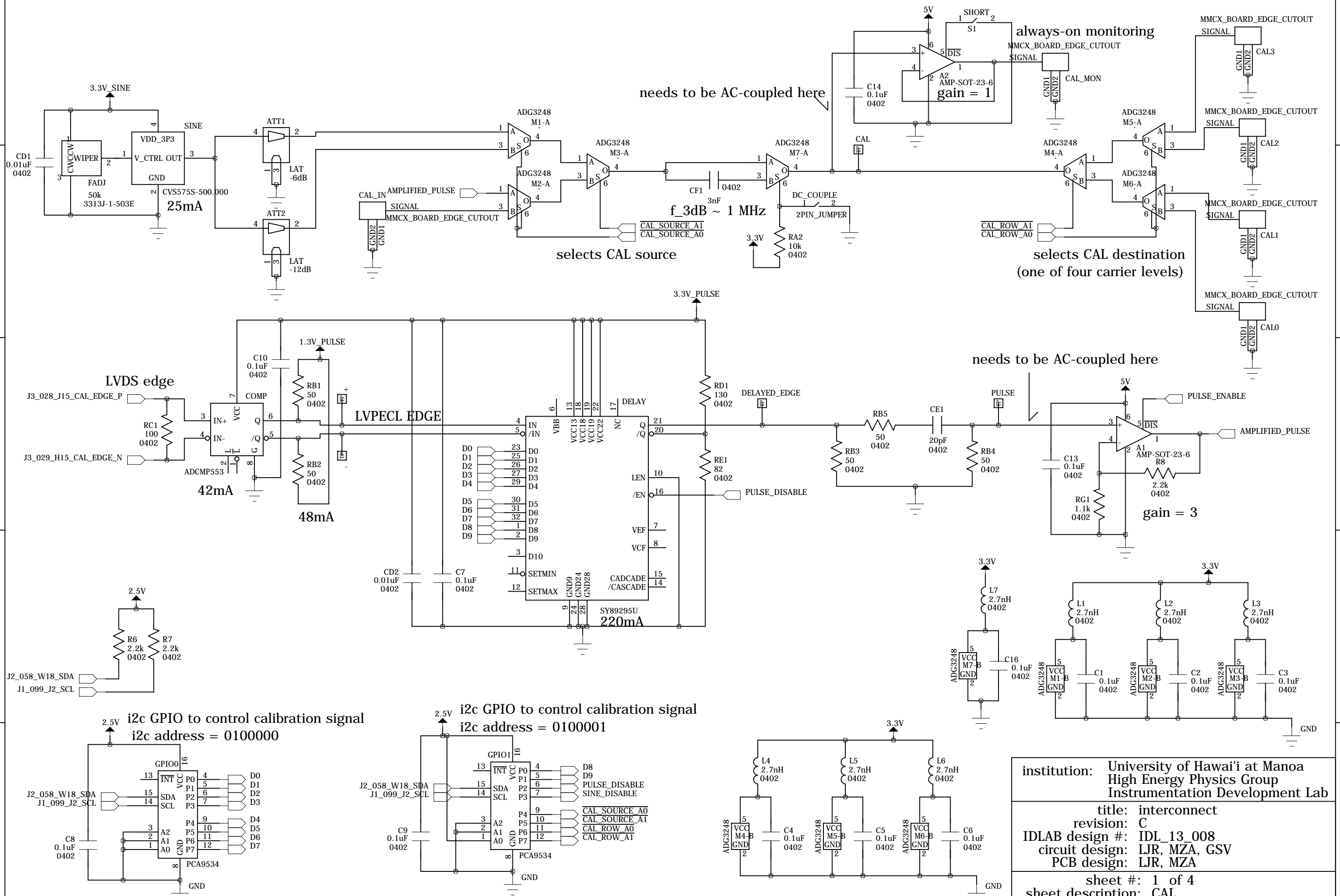
5

4

3

2

1



institution: University of Hawai'i at Manoa  
 High Energy Physics Group  
 Instrumentation Development Lab

title: interconnect  
 revision: C  
 IDLAB design #: IDL\_13\_008  
 circuit design: LJR, MZA, GSV  
 PCB design: LJR, MZA

sheet #: 1 of 4  
 sheet description: CAL  
 date last modified: 2015-02-26

6

5

4

3

2

1

E

E

D

D

C

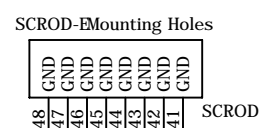
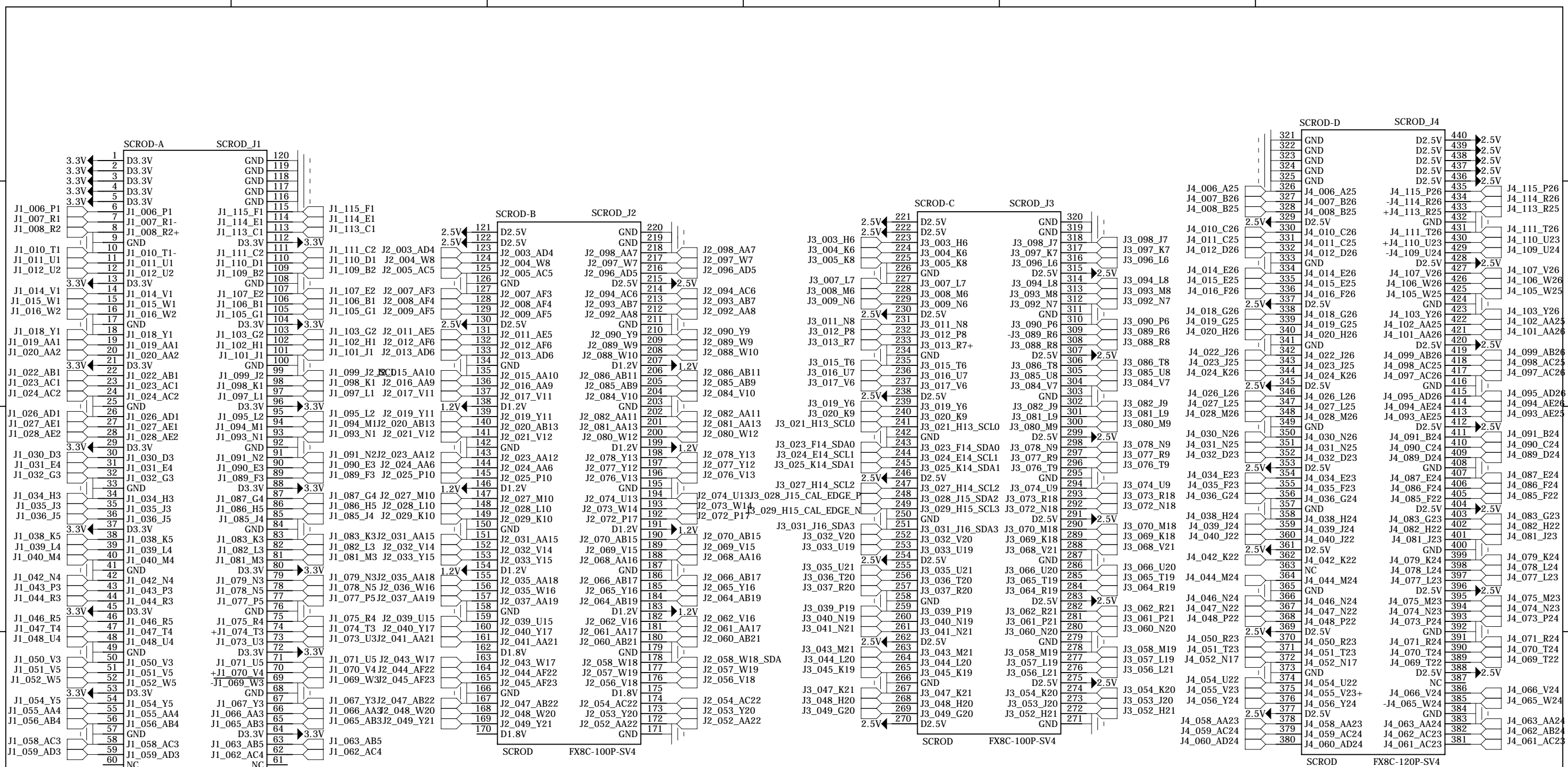
C

B

B

A

A



institution: University of Hawai'i at Manoa  
High Energy Physics Group  
Instrumentation Development Lab

title: interconnect  
revision: C  
IDLAB design #: IDL\_13\_008  
circuit design: LJR, MZA, GSV  
PCB design: LJR, MZA

sheet #: 2 of 4  
sheet description: BOTTOM-CONNECTORS  
date last modified: 2015-02-26

6

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E

E

D

D

C

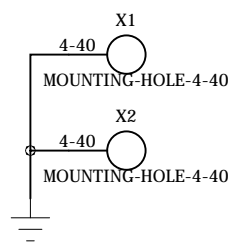
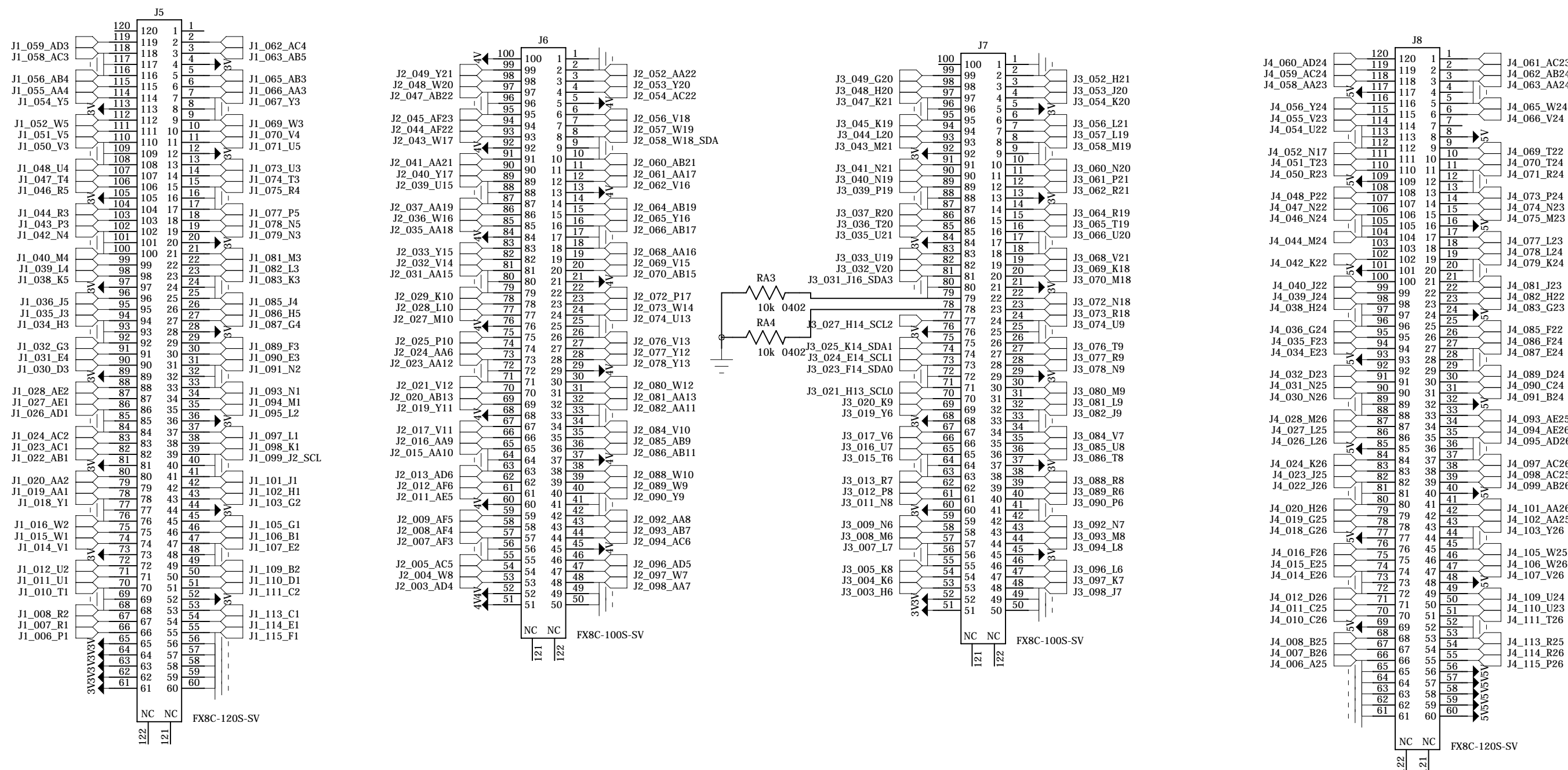
C

B

B

A

A



institution: University of Hawai'i at Manoa  
 High Energy Physics Group  
 Instrumentation Development Lab

title: interconnect  
 revision: C  
 IDLAB design #: IDL\_13\_008  
 circuit design: LJR, MZA, GSV  
 PCB design: LJR, MZA

sheet #: 3 of 4  
 sheet description: TOP-CONNECTORS  
 date last modified: 2015-02-26

6

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4

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2

1

E

E

D

D

C

C

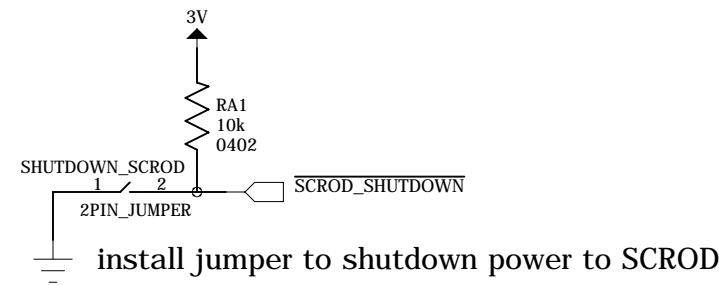
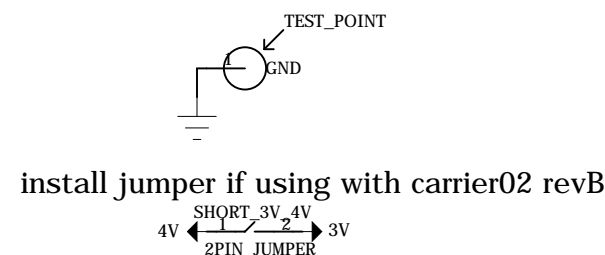
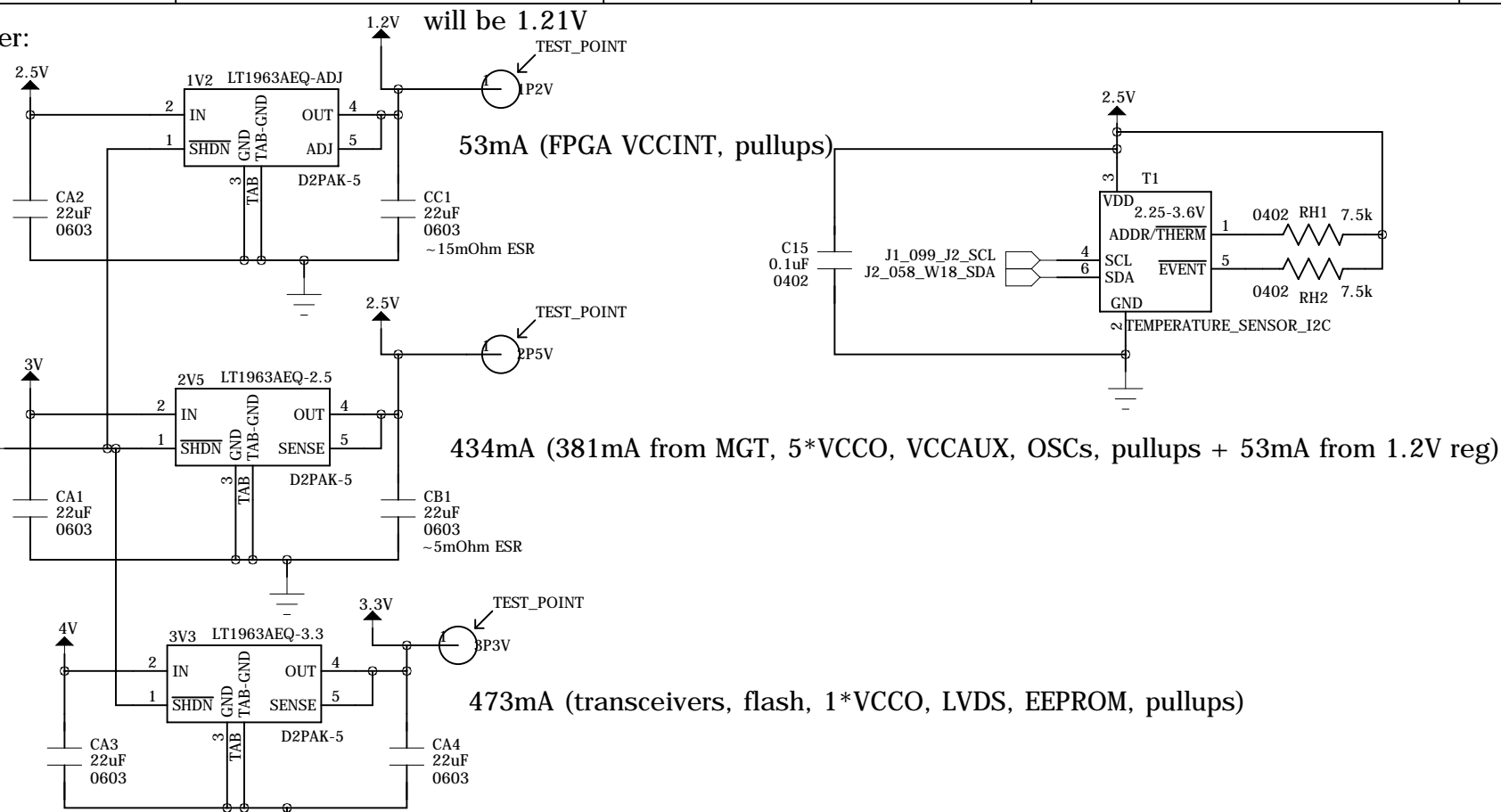
B

B

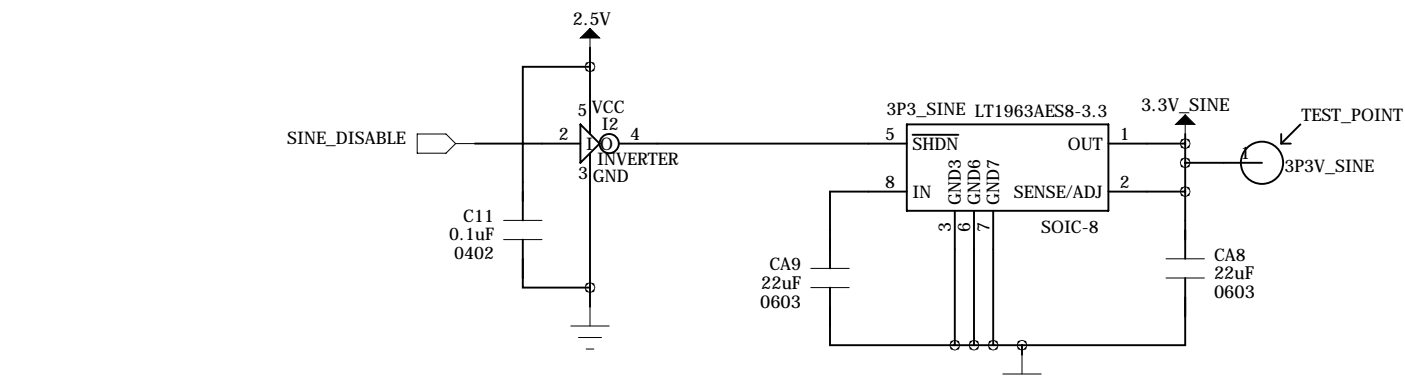
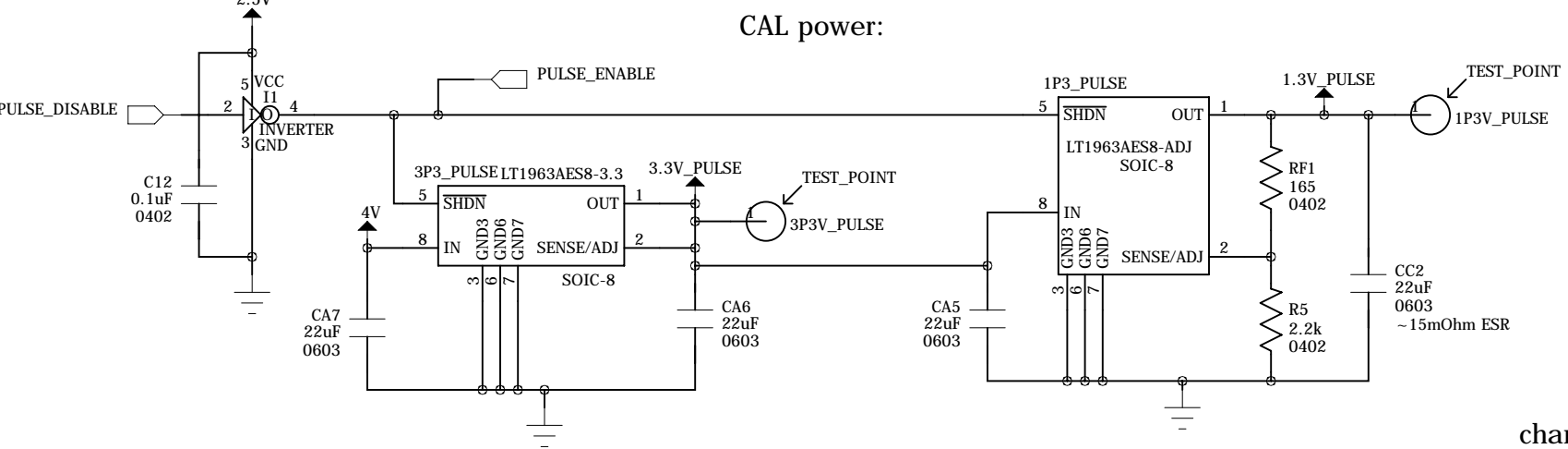
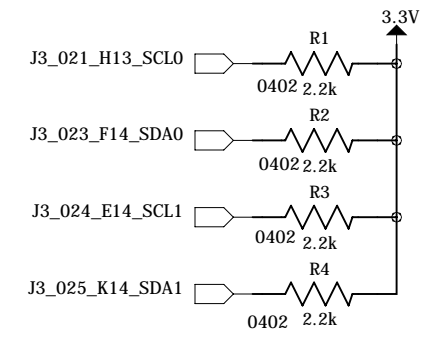
A

A

SCROD power:



I<sup>2</sup>C pull-ups



changes for revC:

added calibration circuit bringing a sine, a pulse or an external input to any of 4 of the carriers via MMCX connectors

this board takes power from the carrier0 above it (3V, 4V, 5V)

3.3V for SCROD still comes from 4V, but the other voltages come from the 3V raw net

institution:	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
title:	interconnect
revision:	C
IDLAB design #:	IDL_13_008
circuit design:	LJR, MZA, GSV
PCB design:	LJR, MZA
sheet #:	4 of 4
sheet description:	VOLTAGE-REGULATORS
date last modified:	2015-02-26