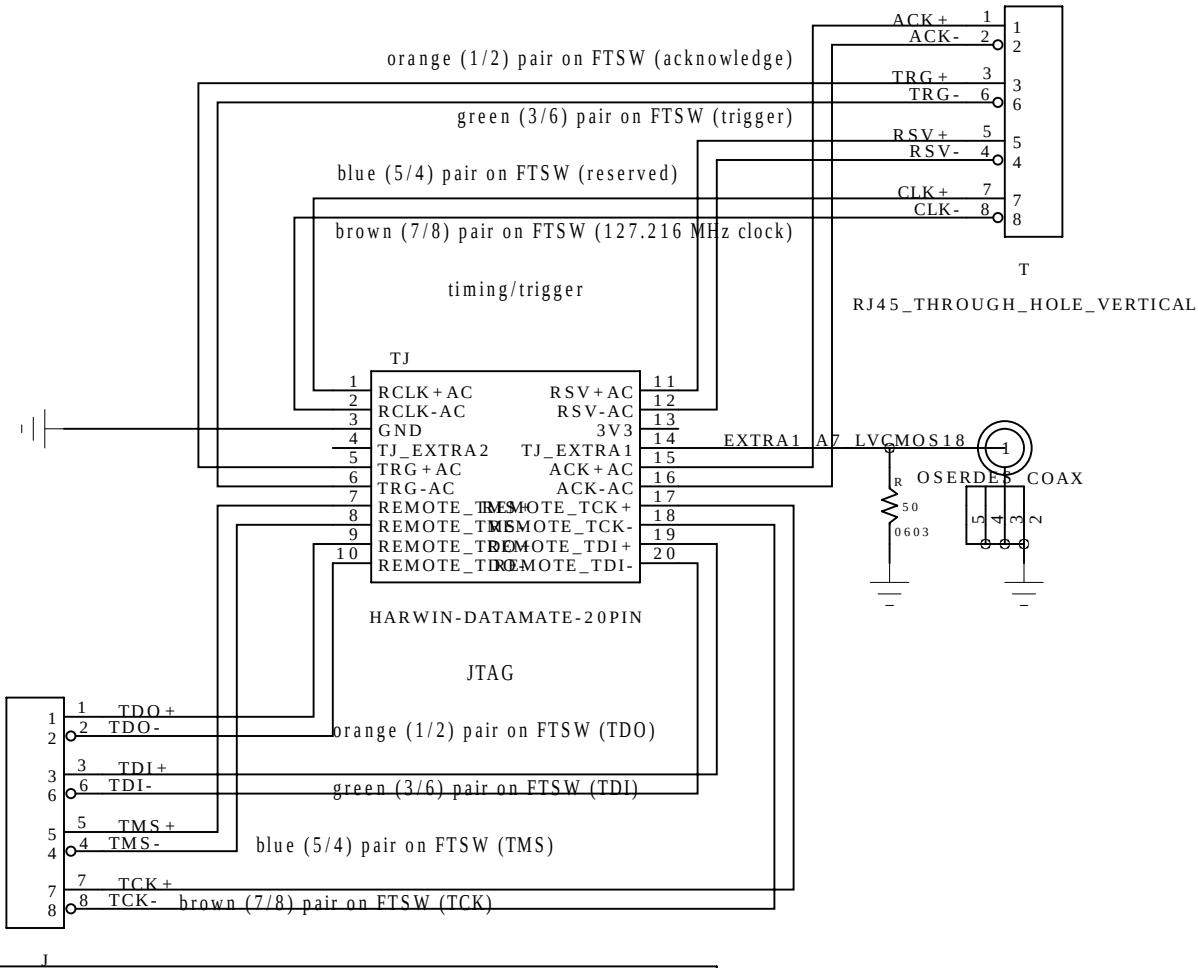


FTSW pair definitions from <https://belle2.cc.kek.jp/~twiki/bin/view/Detector/DAQ/TimingDistribution>
T568B / cat7 cable wire color definitions from <http://www.incentre.net/tech-support/other-support/ethernet-cable-color-coding-diagram/>



University of Hawaii at Manoa High Energy Physics Lab Instrumentation Development Lab
title: T.J. Hooker revision: D IDLAB design #: IDL_19_015 circuit design: GV, CK, mza PCB design: mza
sheet #: 1 of 1 sheet description: BOARD date last modified: 2019-12-18

notes for next revision:
add a spot for a board serial number