

Start	Interval	Talk Subject	Speaker	Confirmed?	Gate	Pass?		
8:30	0:15	Introductions & Charge	Gary D/V				Pass?	
8:45	0:20	Belle II and US role	J. Fast	GSV present	PNNL	n	Includes global schedule and deliverables	
9:05	0:45	Trigger/DAQ upgrade overview	G. Varner	y	Hawaii	y		
9:50	0:20	KLM Upgrade	L. Piilonen	y	VA Tech	n		
10:10	0:20	bKLM (RPC) readout	G. Visser	y	Indiana	y	Brandon will present?	
10:30	0:15	Coffee Break						
10:45	0:20	Scintillator upgrade readout	G. Varner	y	Hawaii	y		
11:05	0:15	Pre-amp production/test	N. Morgan	y	VA Tech	y		
11:20	0:20	TARGET6 ASIC, MB, production	G. Varner	y	Hawaii	y		
11:40	0:20	KLM Discussion/action items	all					
12:00	1:15	Lunch						
13:15	0:15	iTOP PID Upgrade	G. Varner	y	Hawaii	y		
13:30	0:20	iTOP Electronics Overview	K. Nishimura	y	Hawaii	n	Incl. bench/beam test results, issues	
13:50	0:15	Integrated board stack	M. Andrew	y	Hawaii	n		
14:05	0:15	High voltage prototypes	G. Visser	y	Indiana	y		
14:20	0:15	IRS3B ASIC	G. Varner	y	Hawaii	y		
14:35	0:20	Firmware/Calibration/timing issues	K. Nishimura	y	Hawaii	n		
14:55	0:20	Back-end/DSP FINESSE	L. Wood	y	PNNL	n		
15:15	0:15	Coffee Break						
15:30	0:30	iTOP Discussion/action items	all		ANL			
16:00	0:30	Large procurement/support lessons	G. Drake		ANL		CDF, ATLAS...	
16:30	0:15	Summary of action items	Gary D/V					
16:45	0:15	Contingency						
17:00		Adjourn						

Gate Pass list	US Citizen?
David Cinabro	y
Norman Morgan	y
Gary Varner	y
Gerard Visser	y
Michael Wright	?