UNIVERSITY OF HAWAII, INSTRUMENTATION DEVELOPMENT LABORATORY
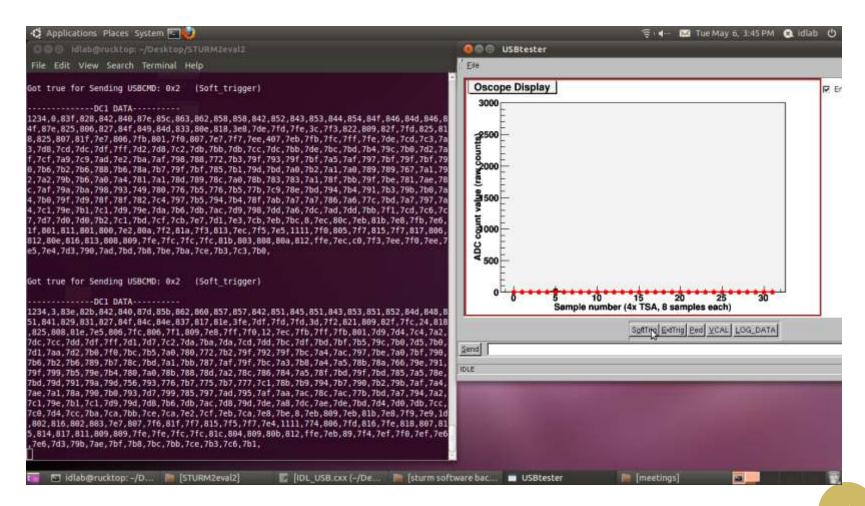
STURM2 FIRMWARE & SOFTWARE

Jussi Malin

FIRMWARE PROGRESS

- Firmware updated to readout samples from all 8 daughter cards

- New timing delay setting for TSA0 – TSA3 signals

- Cleaned unused signals

READOUT SOFTWARE PROGRESS

- Pedestal value reduced from the plot to display only incoming signal

- ADC linearity test function working

- Working on to display sample values from each daughter card separately in the command window

- Next step to make a own display window for each daughter card & channels and display the plot

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READOUT EXAMPLE, DAUGHTER CARD 1

ADC LINEARITY TEST EXAMPLE