

institution:	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
title:	DC_STURM2
revision:	C
IDLAB design #:	IDL_13_006
circuit design:	JM/ZX
PCB design:	JM/ZX
sheet #:	1 of 4
sheet description:	TOP Block
date last modified:	5-FEB-2013

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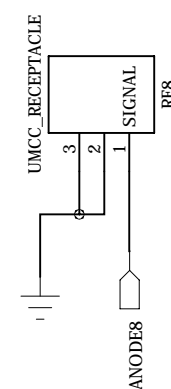
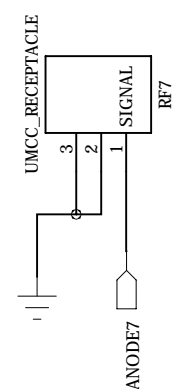
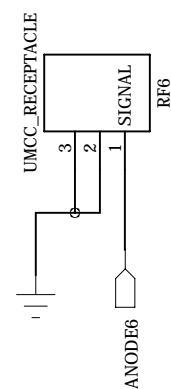
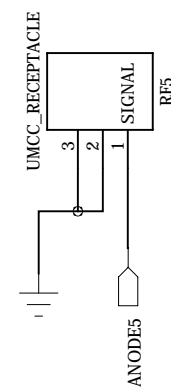
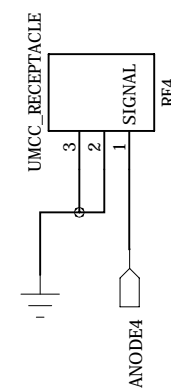
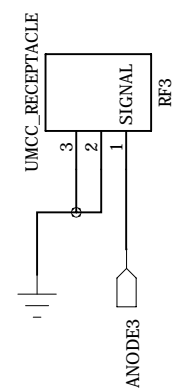
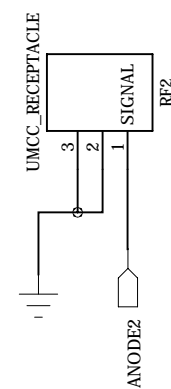
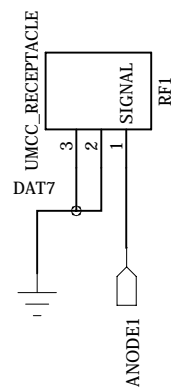
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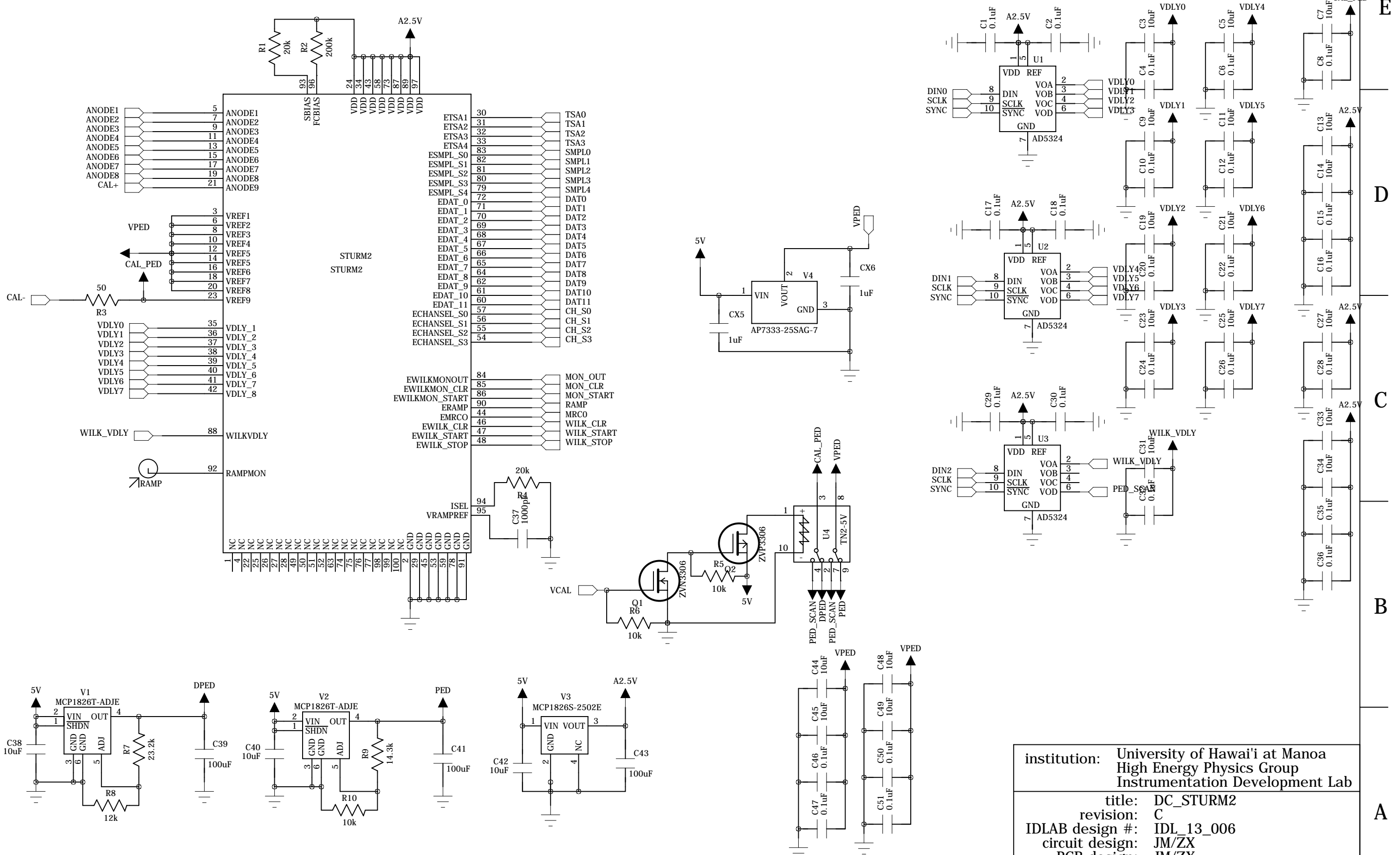
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title:	DC_STURM2
revision:	C
IDLAB design #:	IDL_13_006
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sheet #:	2 of 4
sheet description:	Signal input
date last modified:	5-FEB-2013

AD5324 output capacitors are useless, get rid of those



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PCB design:	JM/ZX
sheet #:	3 of 4
sheet description:	STURM2
date last modified:	5-FEB-2013

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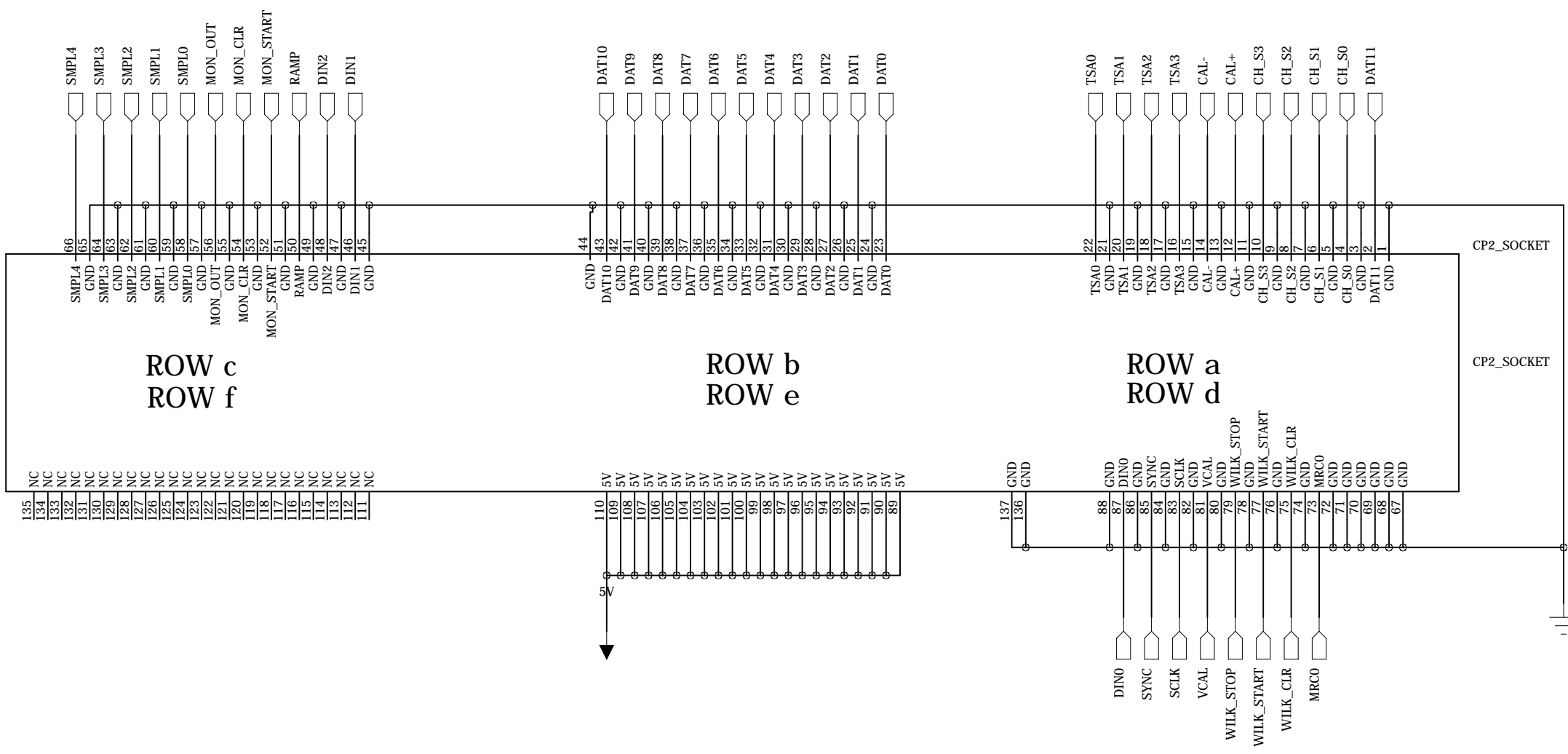
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title:	DC_STURM2
revision:	C
IDLAB design #:	IDL_13_006
circuit design:	JM/ZX
PCB design:	JM/ZX
sheet #:	4 of 4
sheet description:	SCROD connection
date last modified:	5-FEB-2013