

Samtec Connector types:

IRSX / CARRIER

IRSX: ERF8-025-01-L-D-EM2-TR (board edge connector)

CARRIER: ERM8-025-05.0-L-DV-TR

<http://www.samtec.com/technical-specifications/Default.aspx?SeriesMaster=ERF8-EM>

CARRIER / XRM MOTHERBOARD

CARRIER: SEAF-40-01-X-10-X-A (Samtec ASP-134604-01)

MOTHERBOARD: SEAM-40-10-A (Samtec ASP_134603_01)

Power Consumption

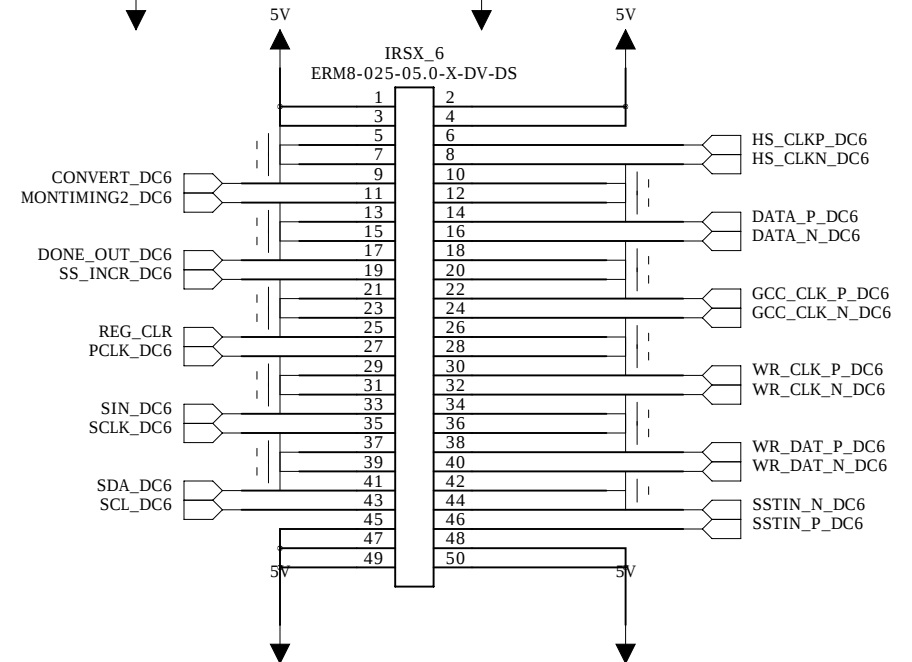
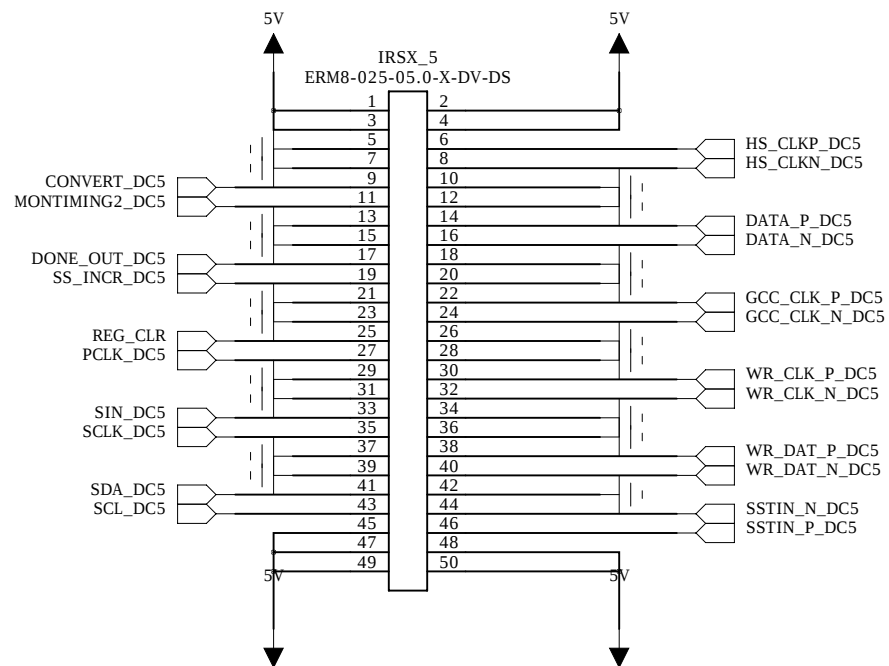
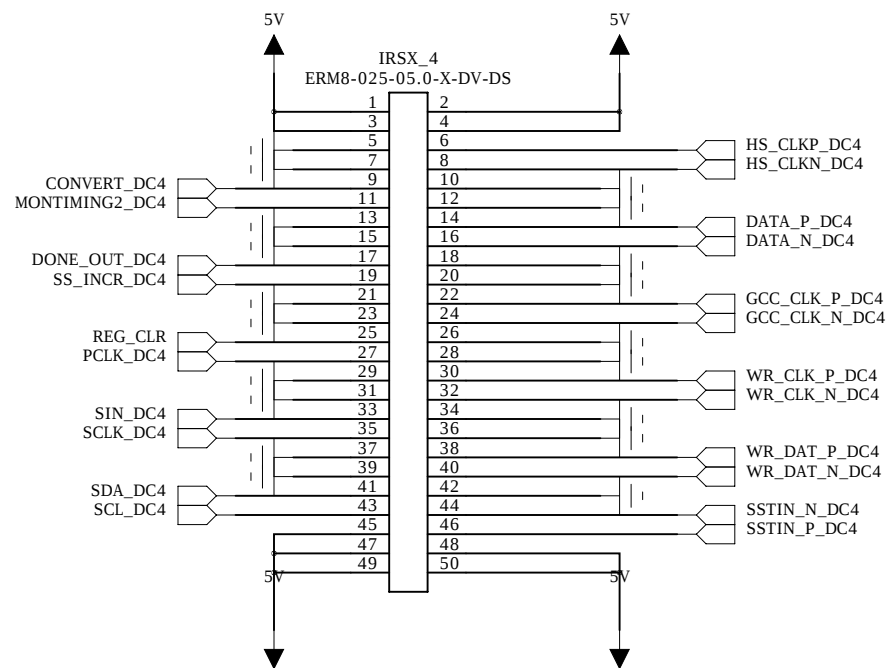
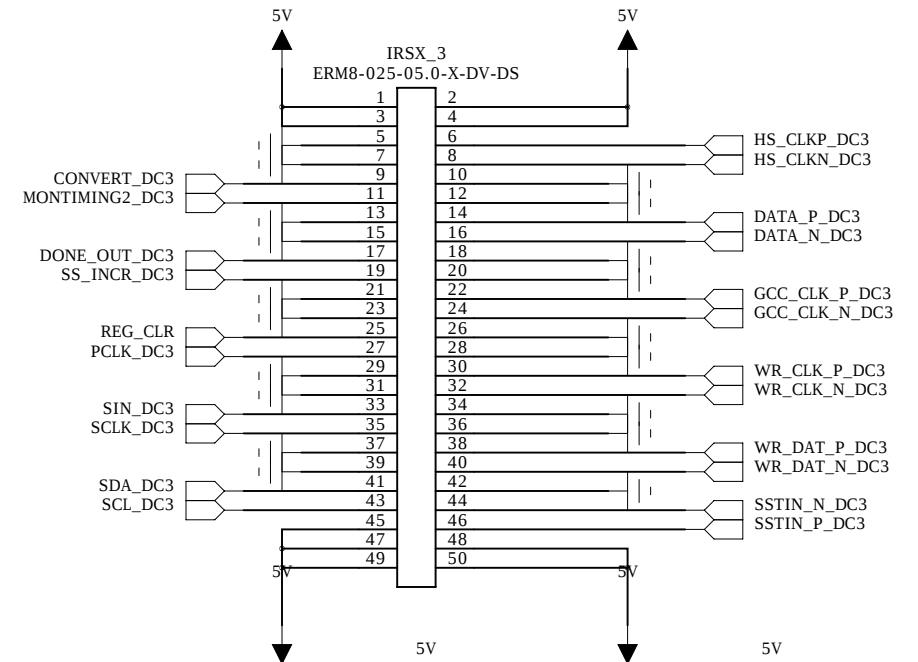
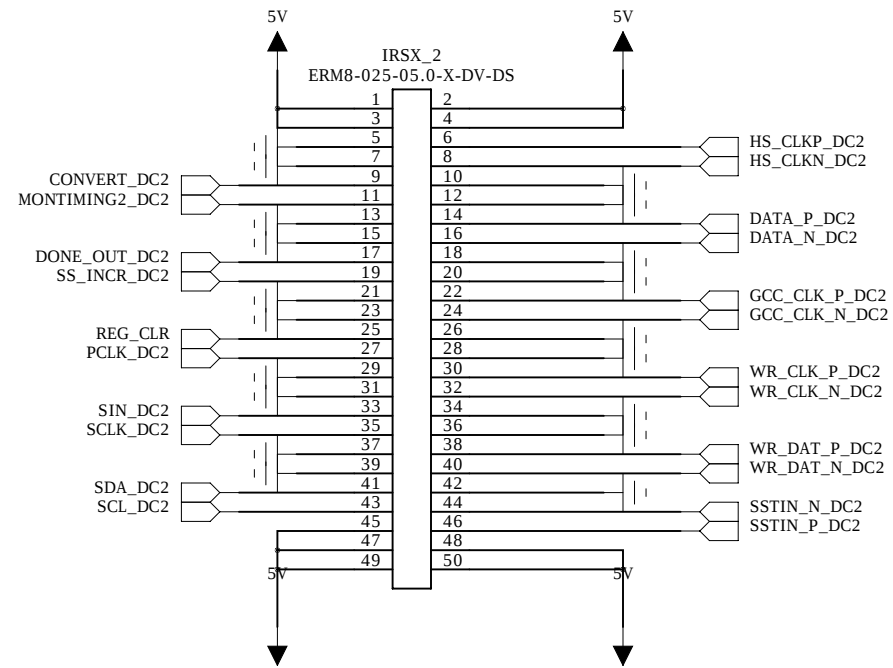
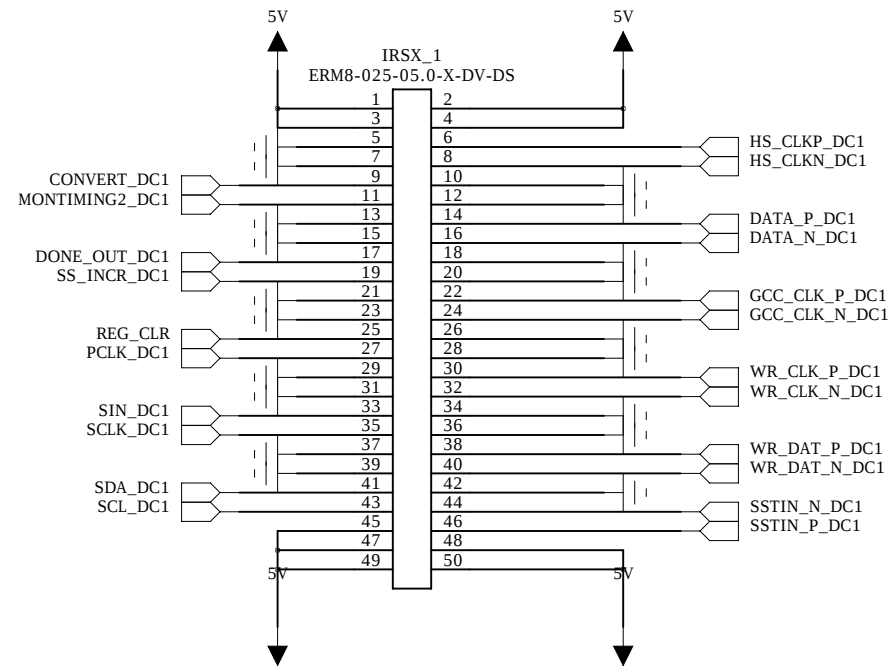
CLK_Fanout: 2 x165 mW = 330 mW

IRSX DC: 16 x 1 W = 16 W

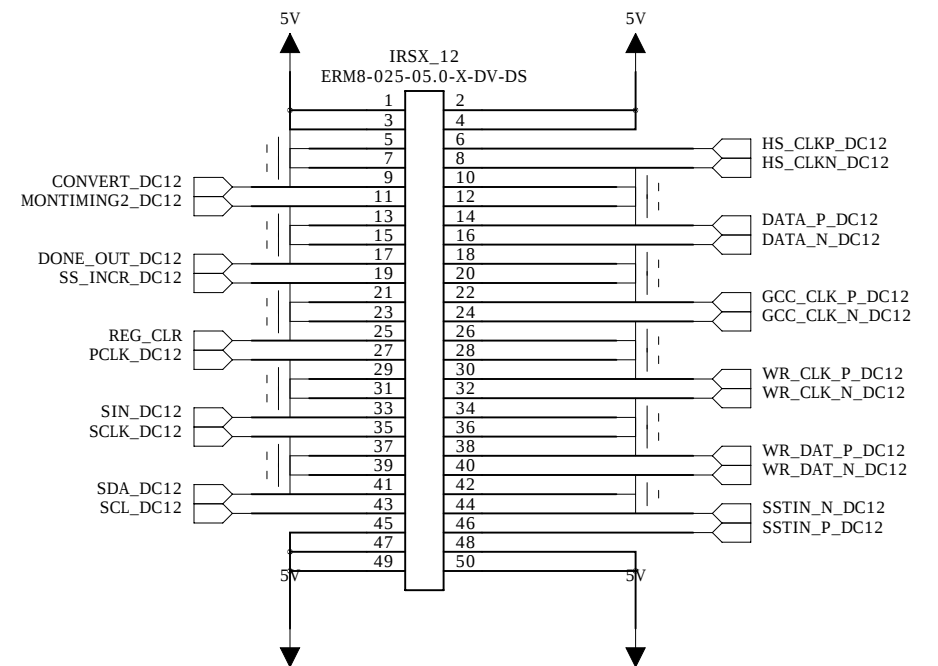
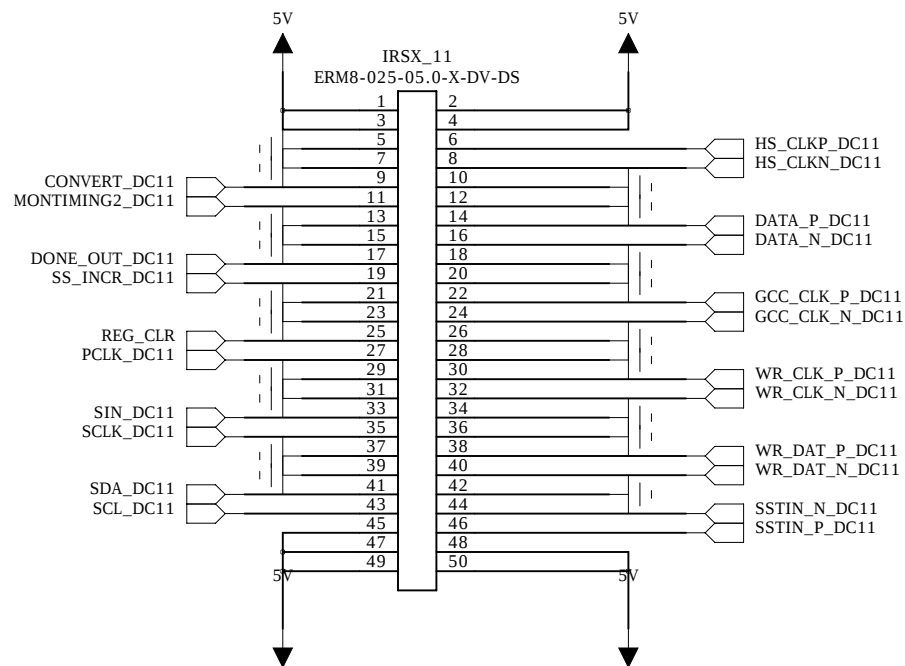
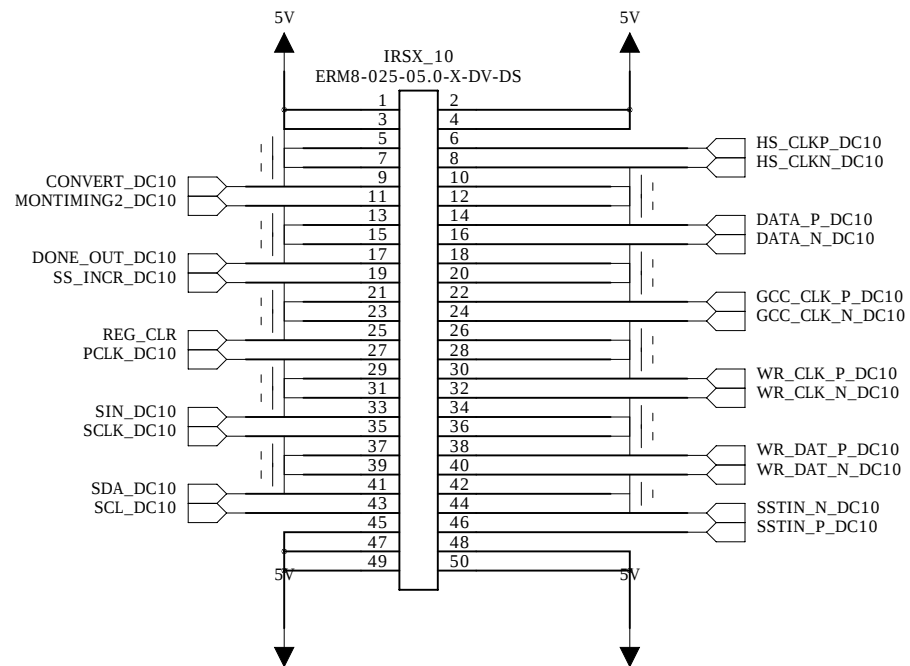
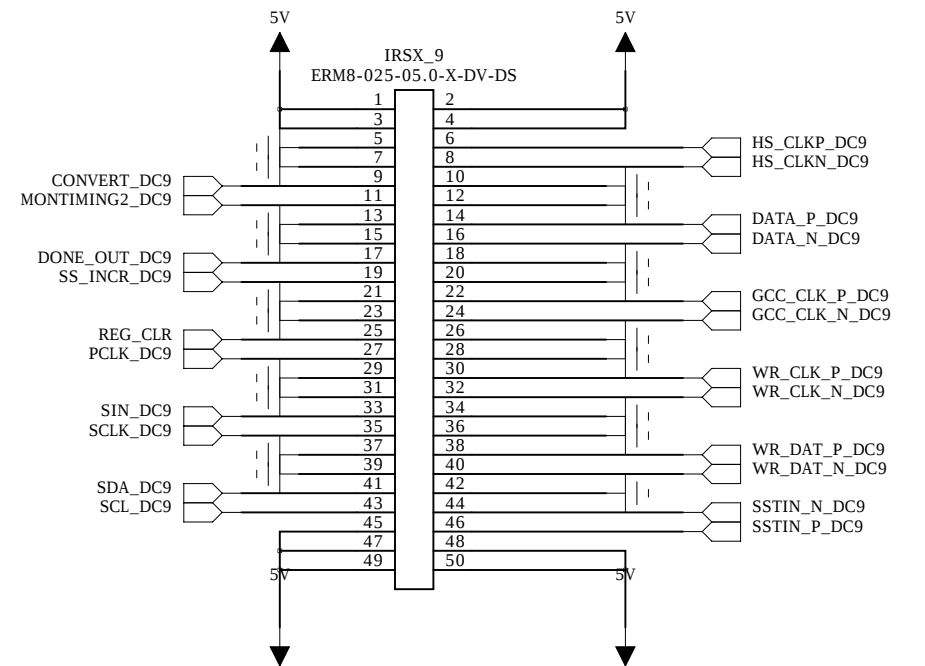
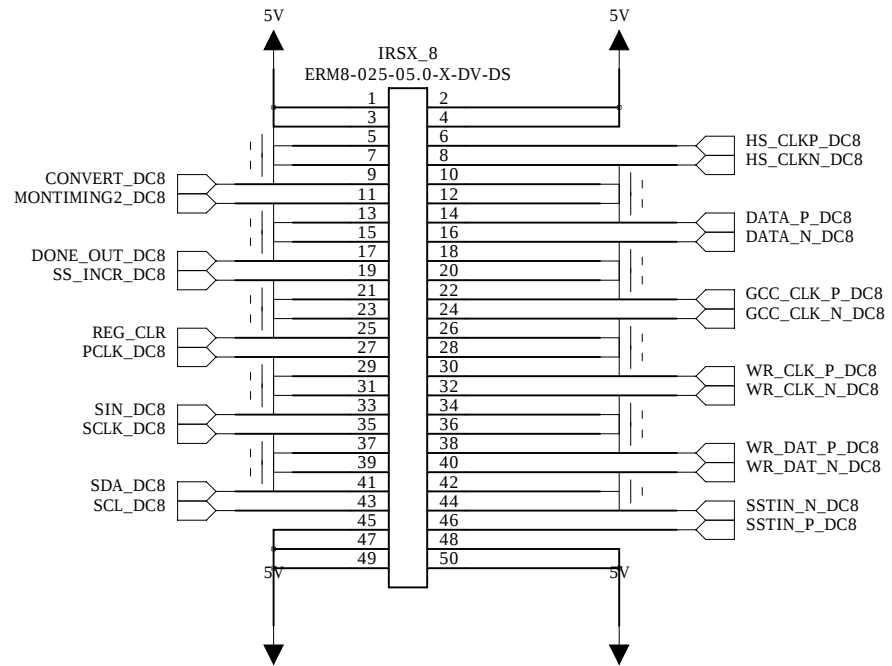
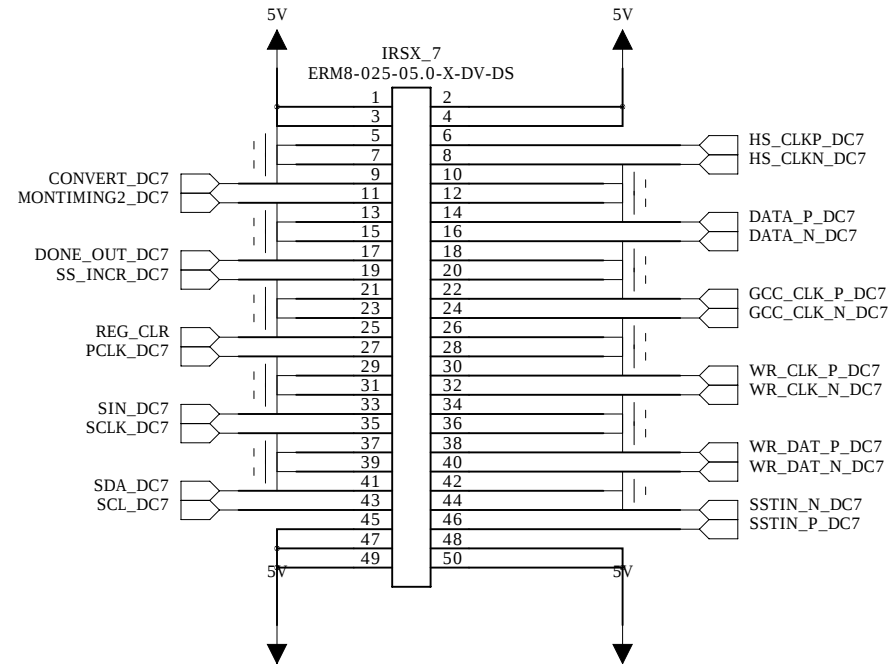
ARTIX-7 FPGA: 1 W

TOTAL : 17.330 W / 3.46 A

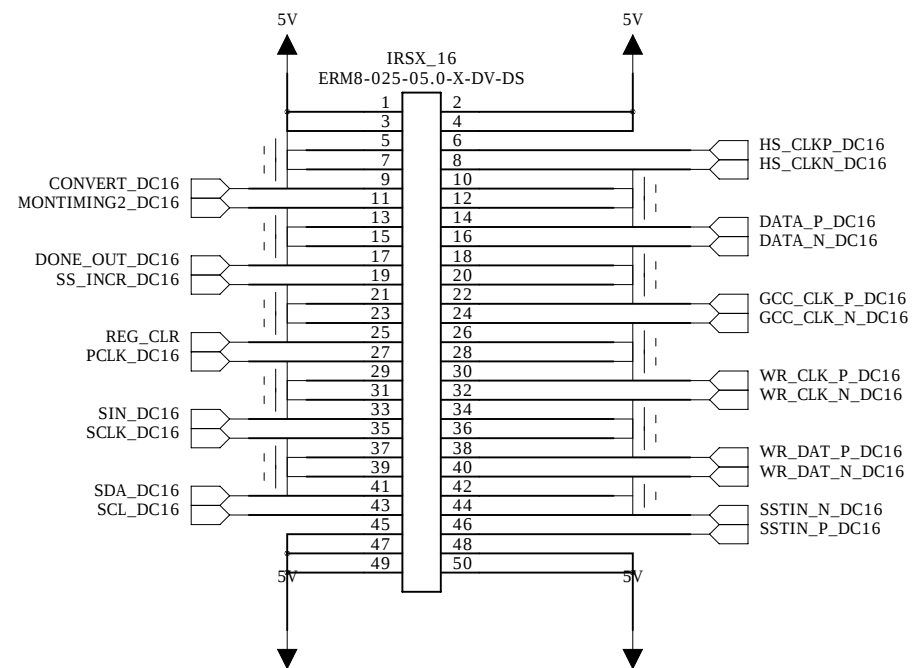
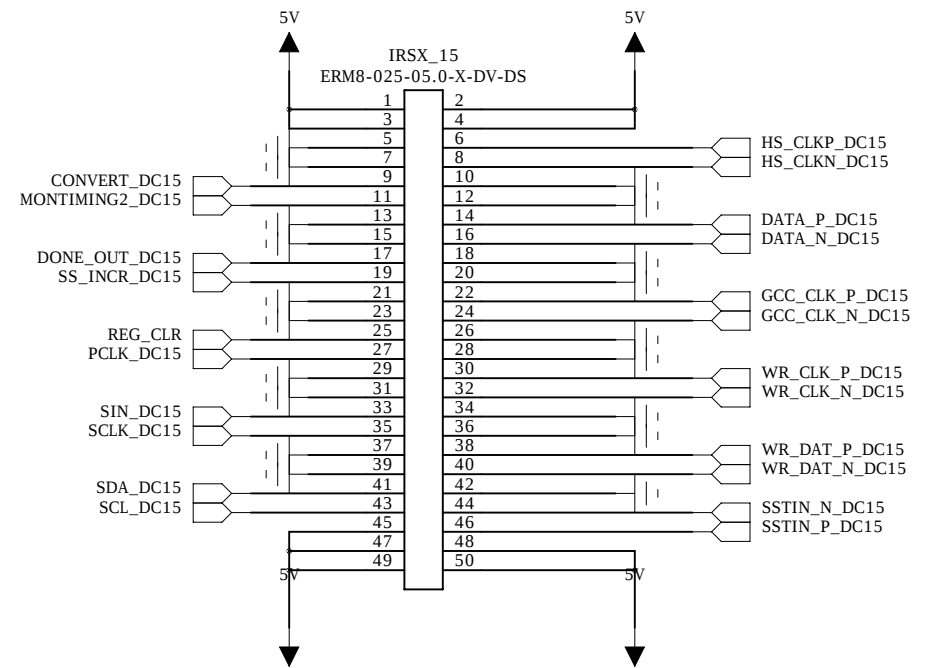
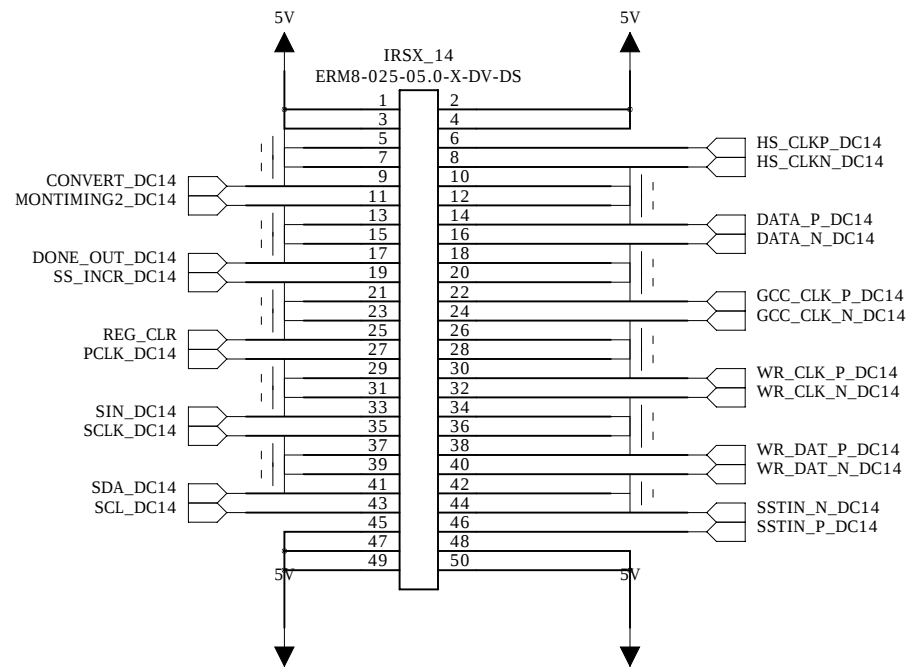
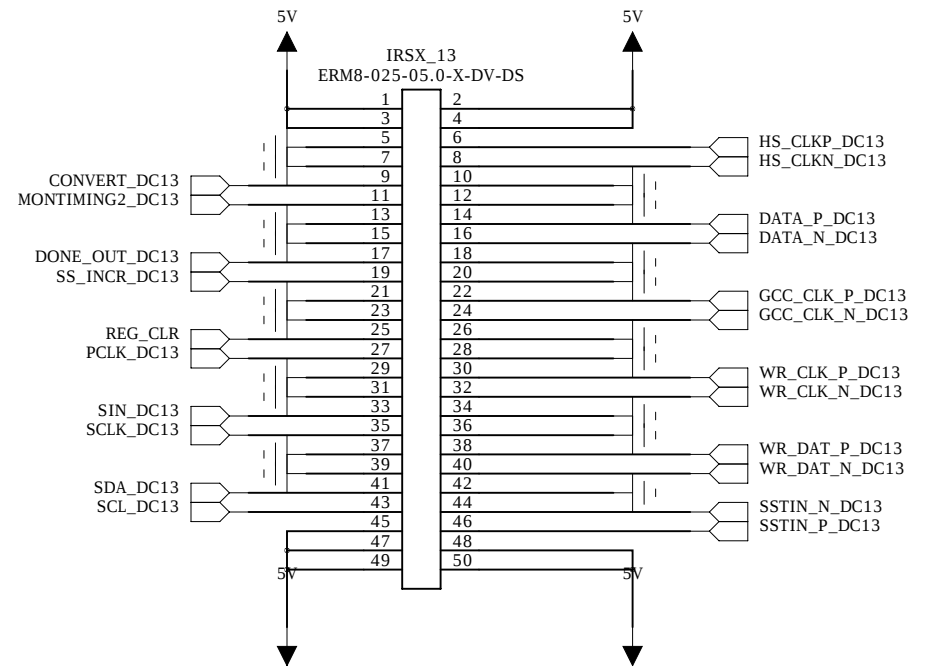
Institution	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Laboratory
Title	IRSX_Carrier
Revision	A
IDLAB Design #	IDL_14_025
Circuit Design	JM
PCB Design	JM
Sheet #	1 of 7
Description	NOTES
Last Modified	22-September-2014



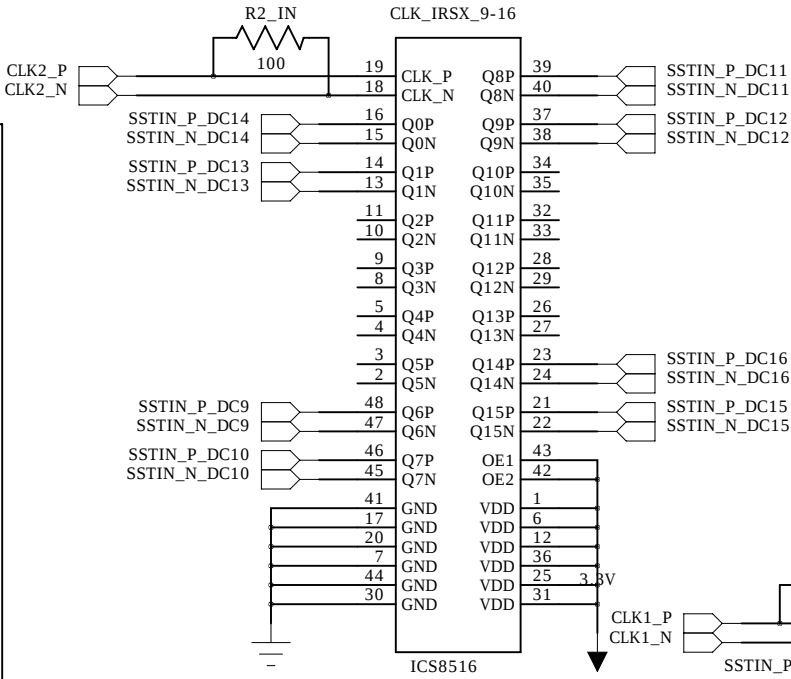
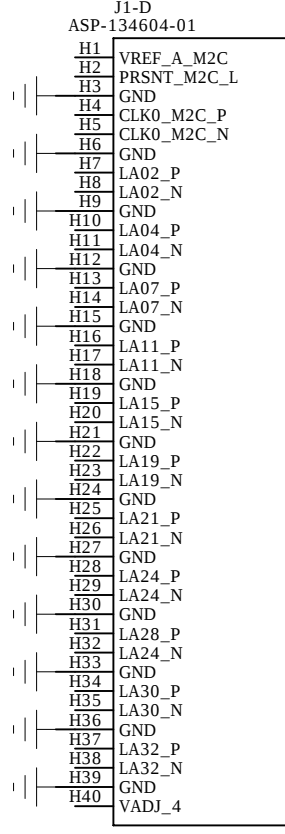
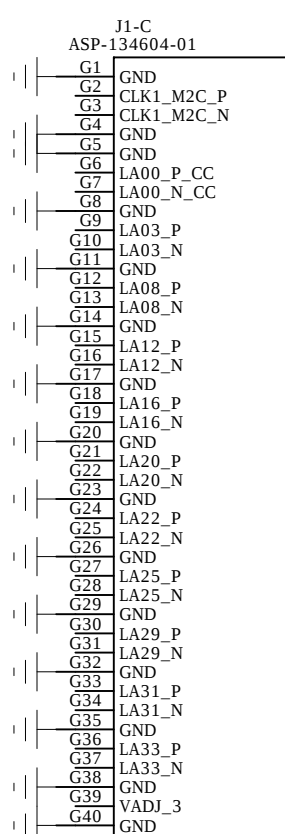
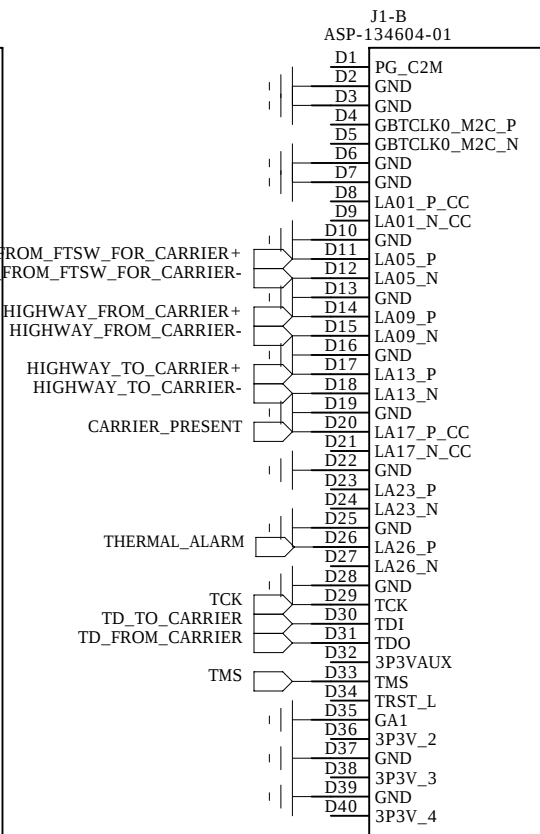
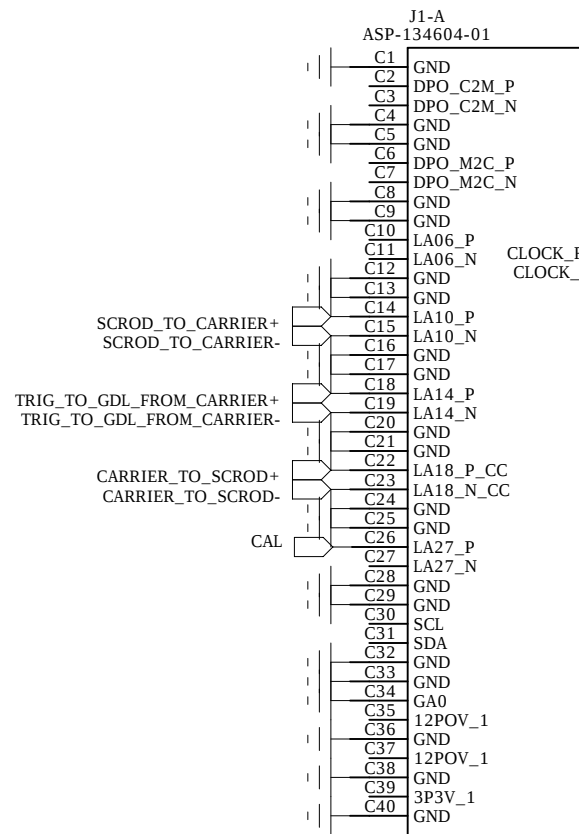
Institution	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Laboratory
Title	IRSX_Carrier
Revision	A
IDLAB Design #	IDL_14_025
Circuit Design	JM
PCB Design	JM
Sheet #	2 of 7
Description	IRSX Connectors 1-6
Last Modified	22-September-2014



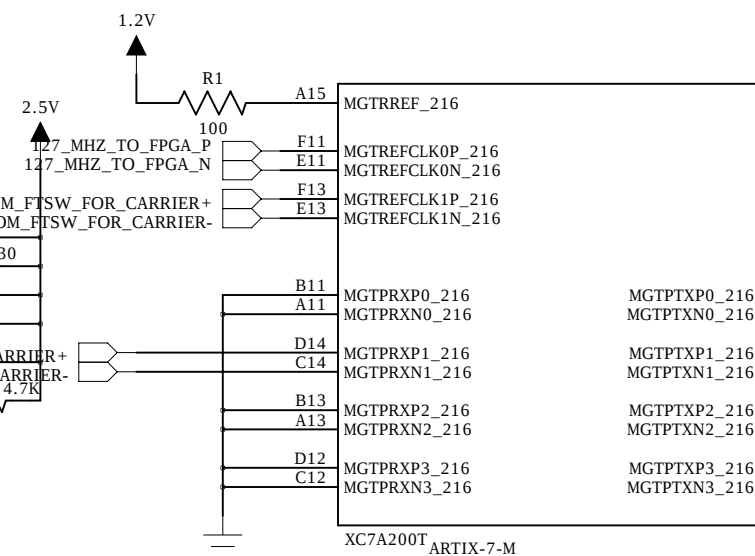
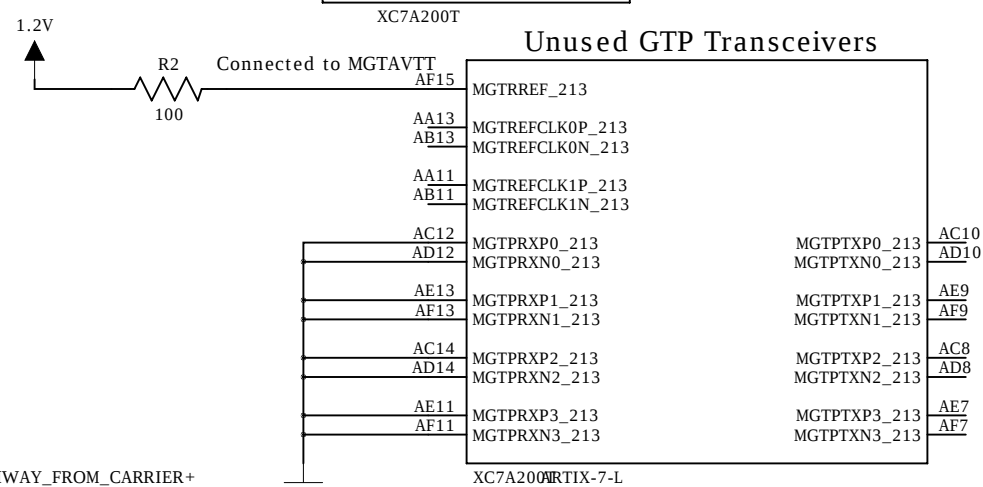
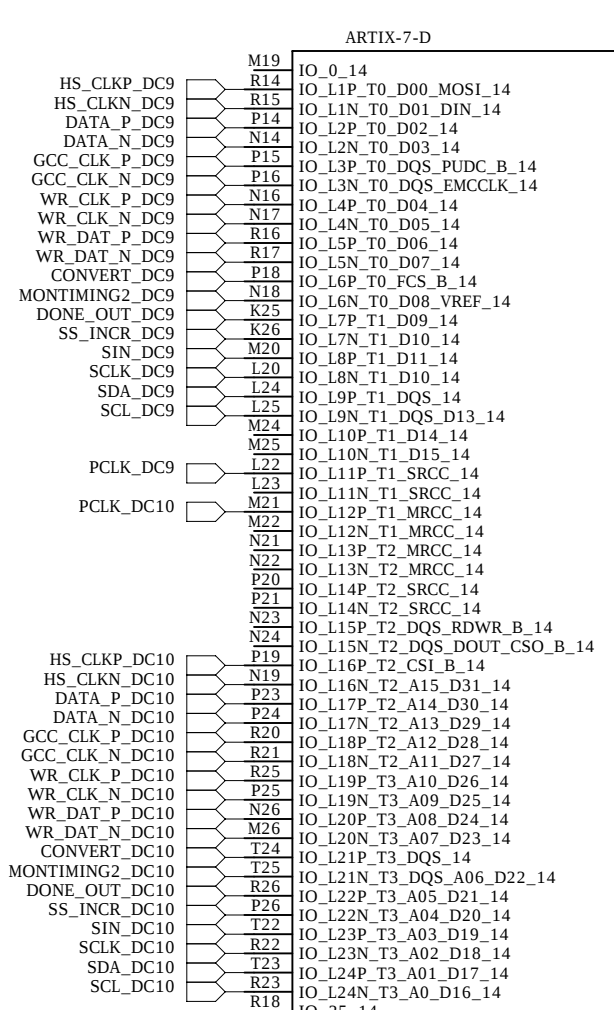
Institution	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Laboratory
Title	IRSX_Carrier
Revision	A
IDLAB Design #	IDL_14_025
Circuit Design	JM
PCB Design	JM
Sheet #	3 of 7
Description	IRSX Connectors 7-12
Last Modified	22-September-2014



Institution	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Laboratory
Title	IRSX_Carrier
Revision	A
IDLAB Design #	IDL_14_025
Circuit Design	JM
PCB Design	JM
Sheet #	4 of 7
Description	IRSX Connectors 13-16
Last Modified	22-September-2014



FPGA specification: http://www.xilinx.com/support/documentation/user_guides/ug475_7Series_Pkg_Pinout.pdf



GTP Transceivers powered trough MGTAVCC/MGTAVTT G11

Institution	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Laboratory
Title	IRSX_Carrier
Revision	A
IDLAB Design #	IDL_14_025
Circuit Design	JM
PCB Design	JM
Sheet #	6 of 7
Description	FPGA Banks 0,12,13,14,15,213,216
Last Modified	22-September-2014
