

# IDL\_14\_023\_XRM\_MOTHERBOARD\_REVA

Changed to SCROD REVB

Power levels for SCROD:

RAW1 : 1.87 Volts

RAW2: 3.15 Volts

RAW3: 4.33 Volts

Estimated Power Consumption (max)

CLK Fanout: 165 mA

Fans: 4 x 130mA = 520 mA

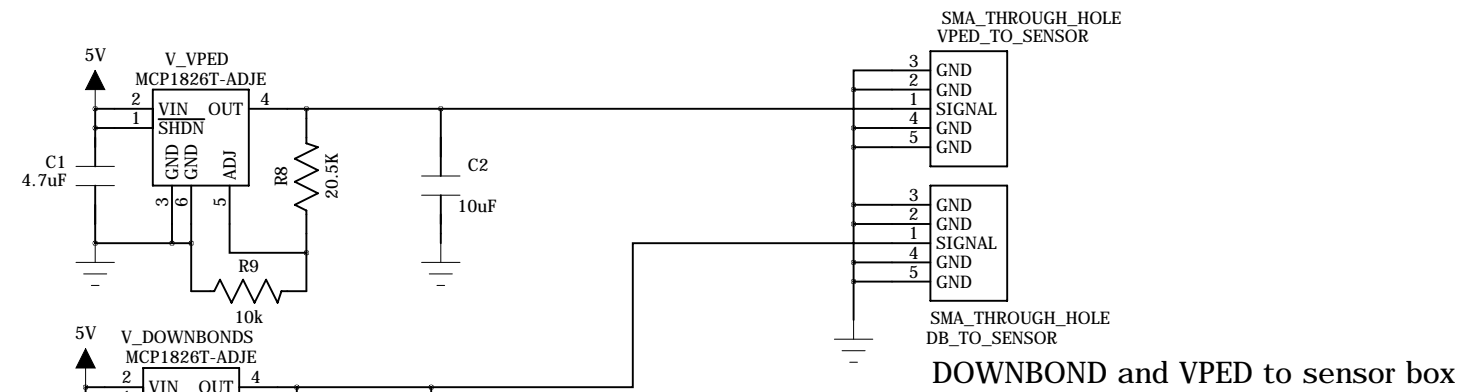
Carriers : 4 x 17.330 W = 69.32 W = 18.864 A

Amplifiers: 4 x 7.5 W = 30 W = 6 A

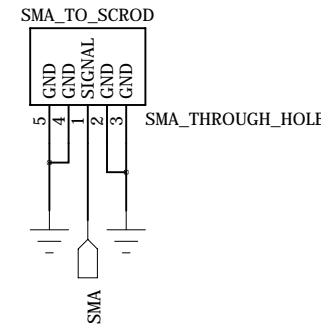
SCROD RevB :

System total power consumption = 25.549 A = 128 W

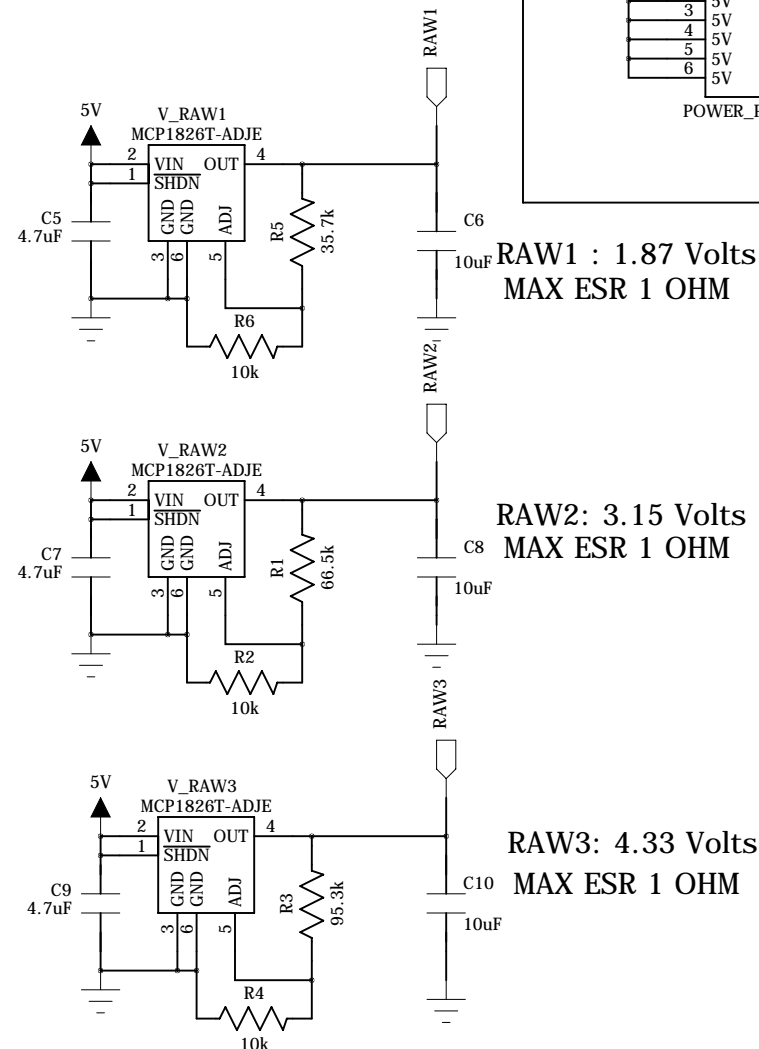
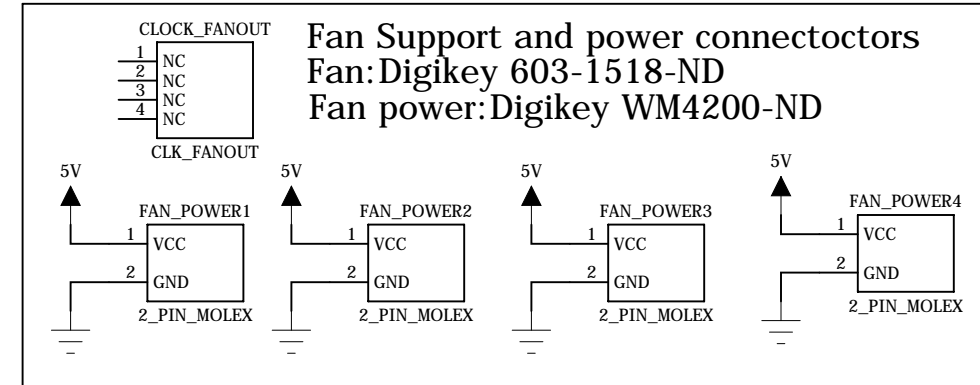
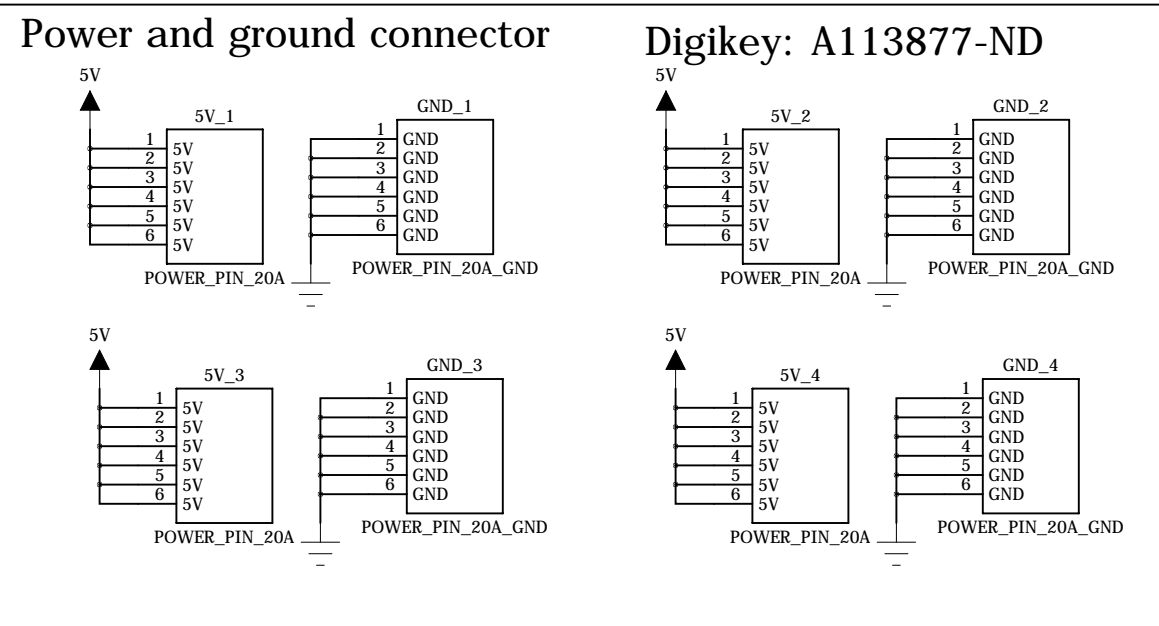
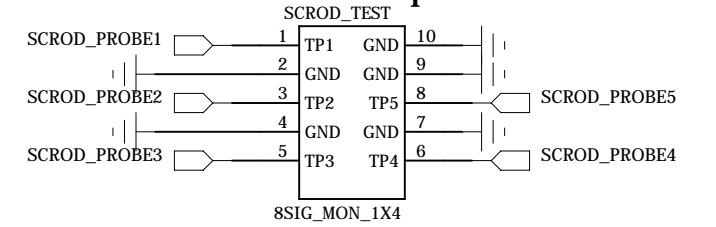
Institution	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
Title	XRM_Motherboard
Revision	A
IDLAB Design #	IDL_14_023
Circuit Design	JM
PCB Design	JM
Sheet #	1 of 5
Description	NOTES
Last Modified	22-SEPTEMBER-2014



**Vertical SMA connector to SCROD**



**SCROD Testpoints**



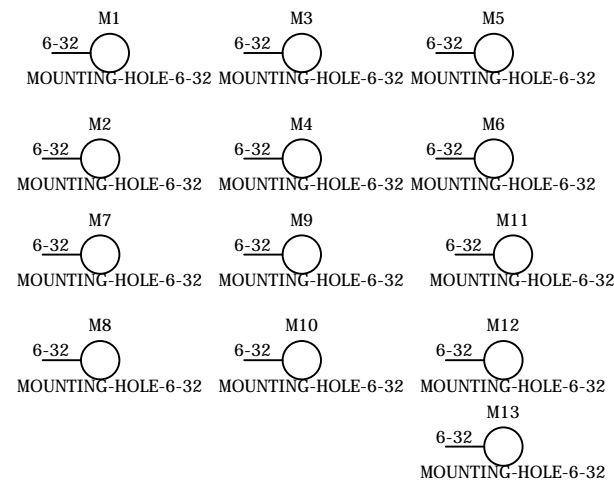
**RAW1 : 1.87 Volts  
MAX ESR 1 OHM**

**RAW2: 3.15 Volts  
MAX ESR 1 OHM**

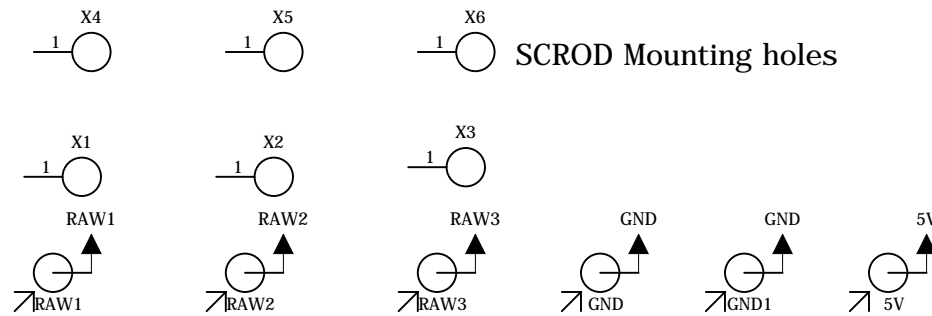
**RAW3: 4.33 Volts  
MAX ESR 1 OHM**

$V_{out} = 0.41 * ((R3 + R4) / R4)$   
Keep R4 between 10k - 200k

**Board mounting holes**

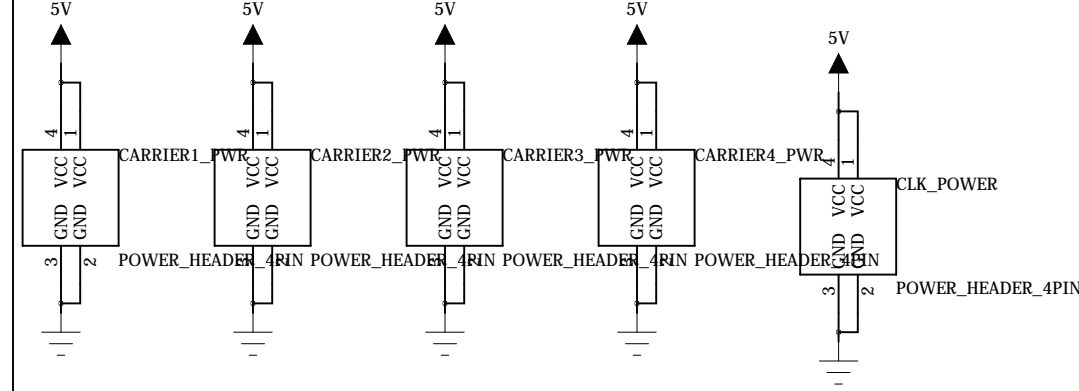


**SCROD Mounting holes**

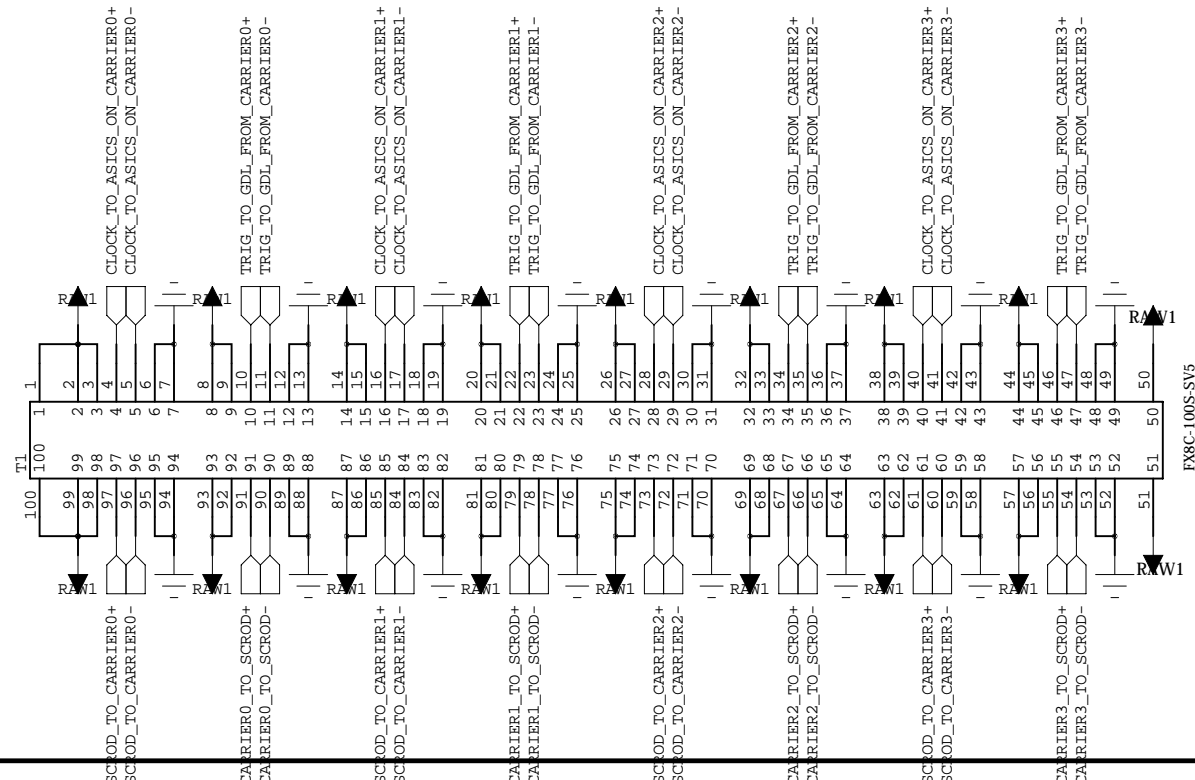
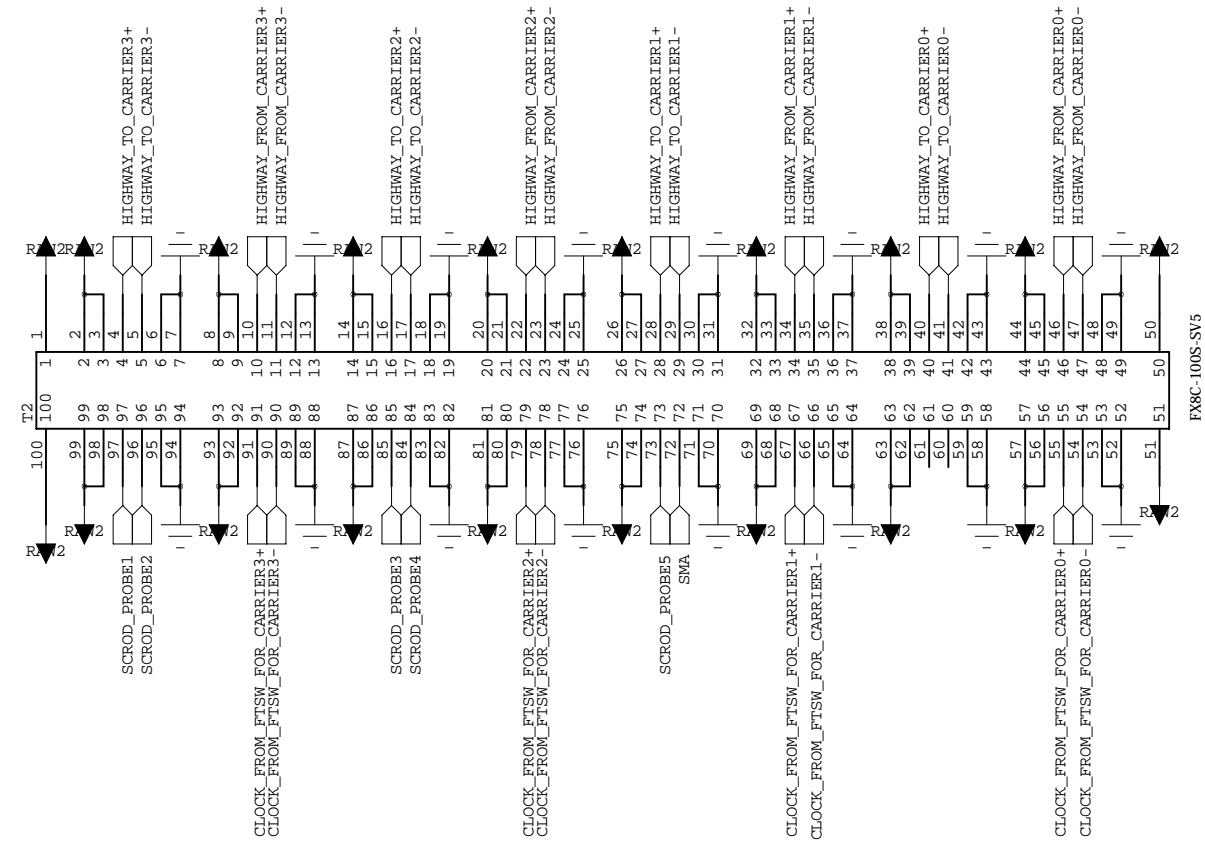
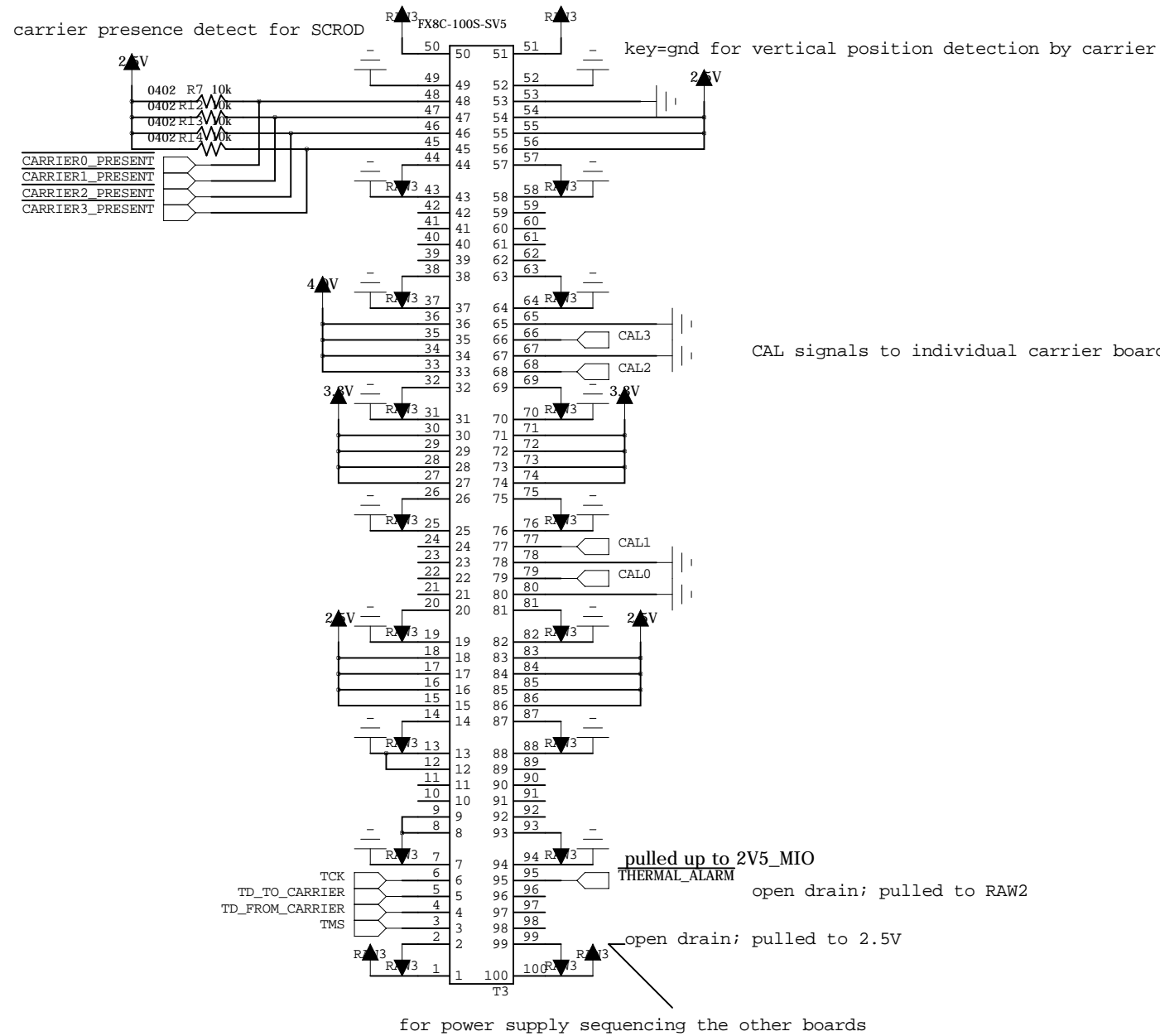


**Voltage testpoints**

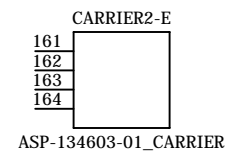
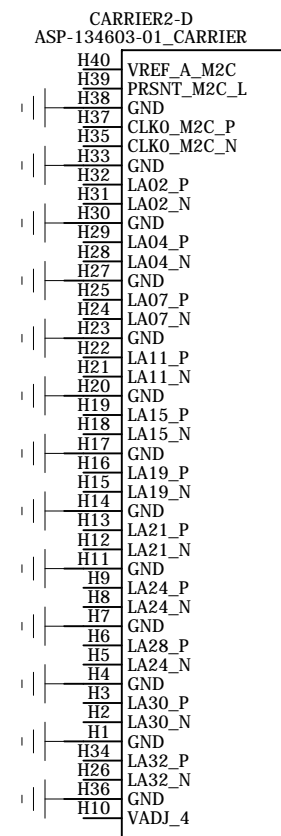
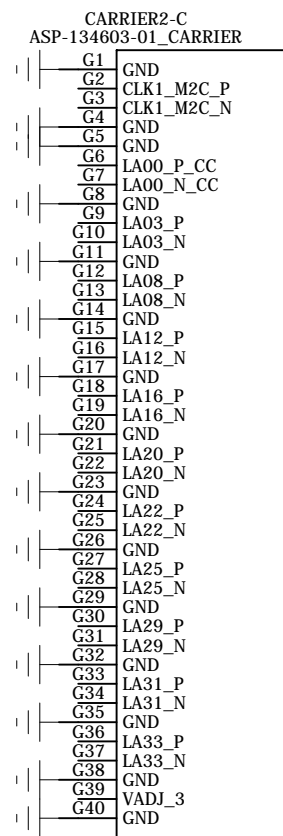
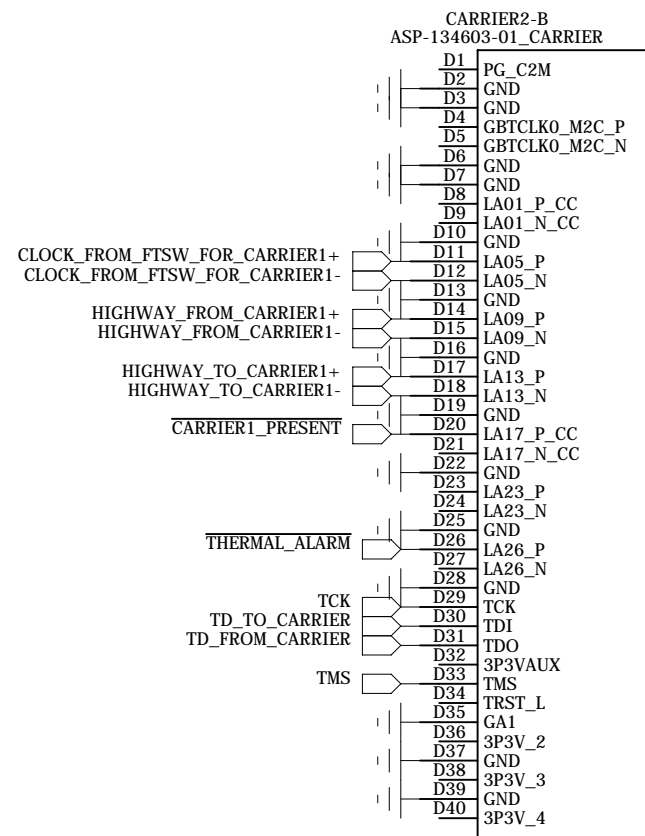
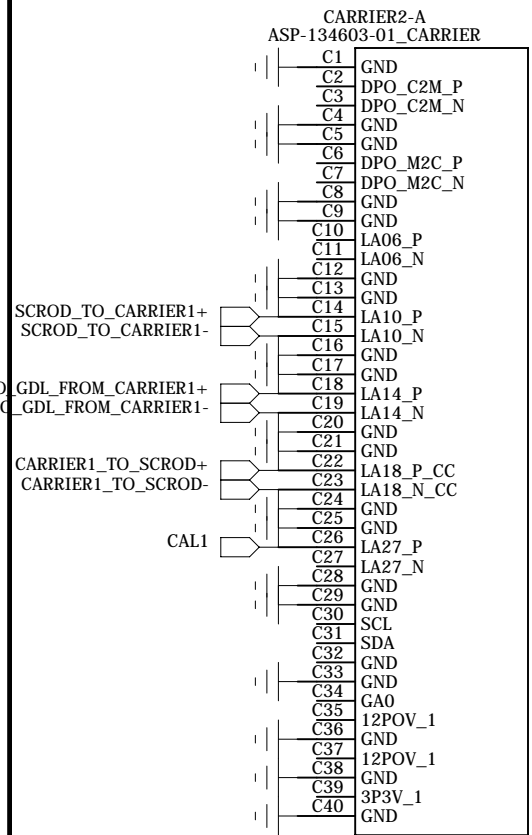
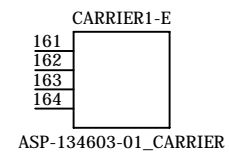
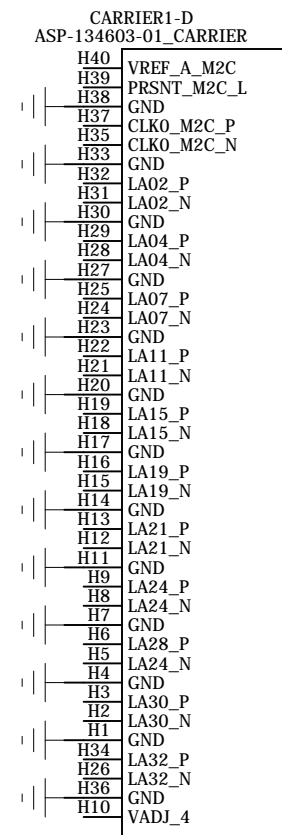
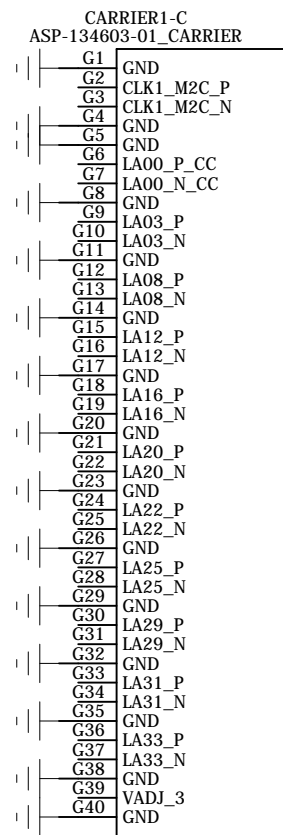
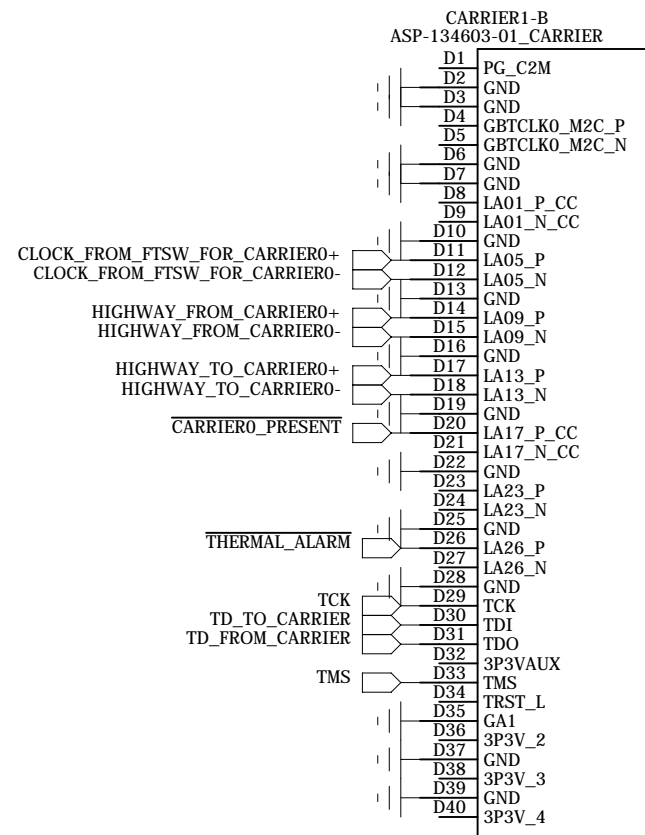
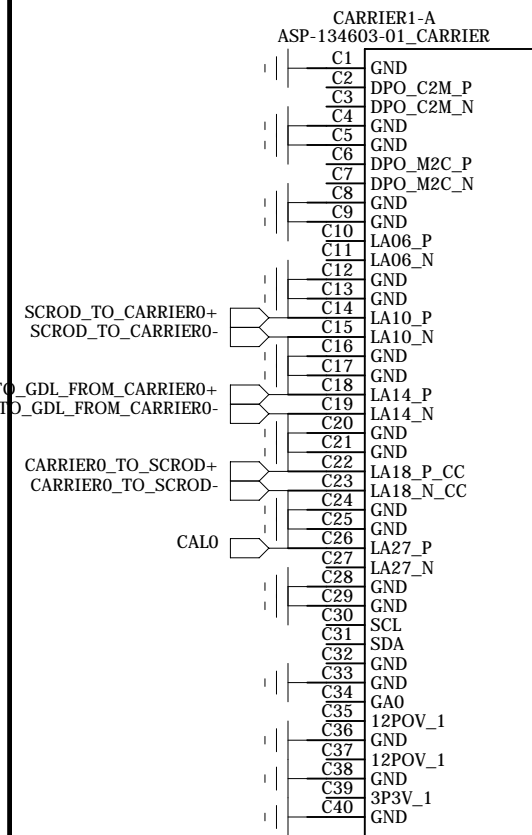
**Carrier and CLK Fanout power connectors**  
Digikey: CONN HEADER VERT 4POS .156 TIN



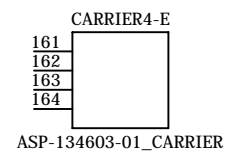
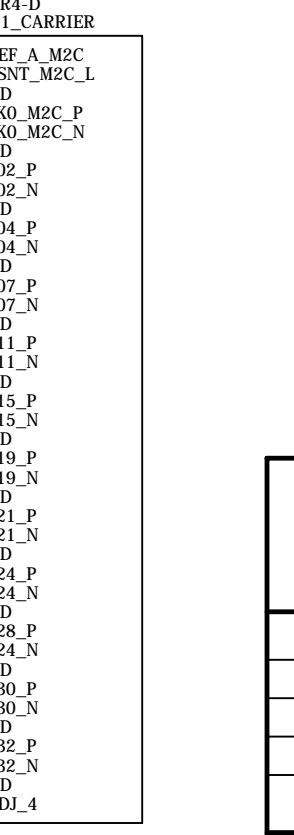
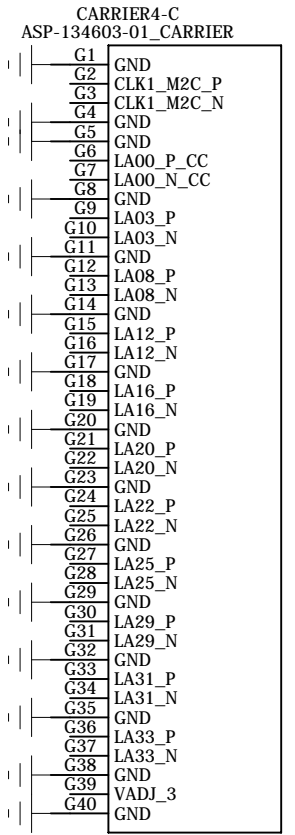
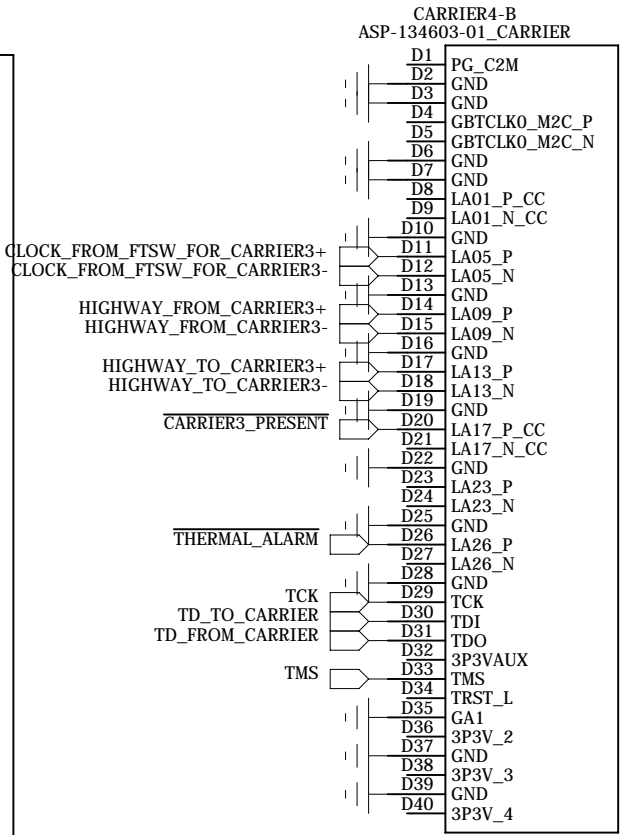
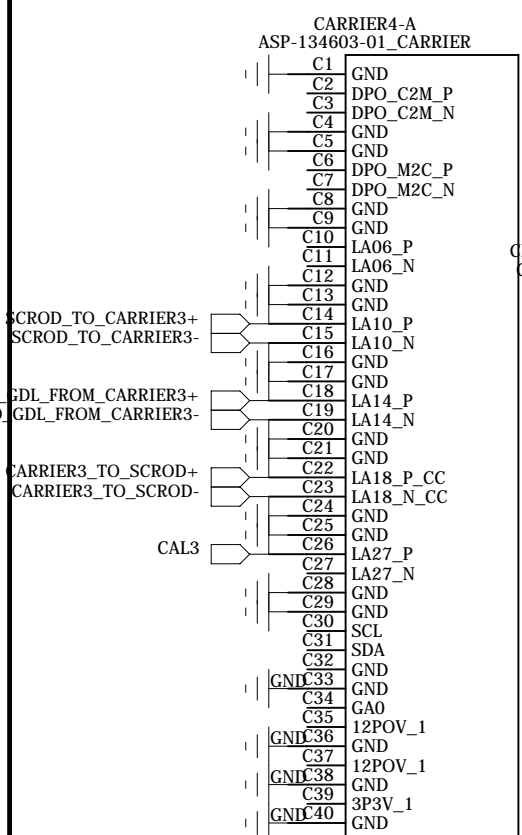
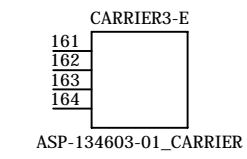
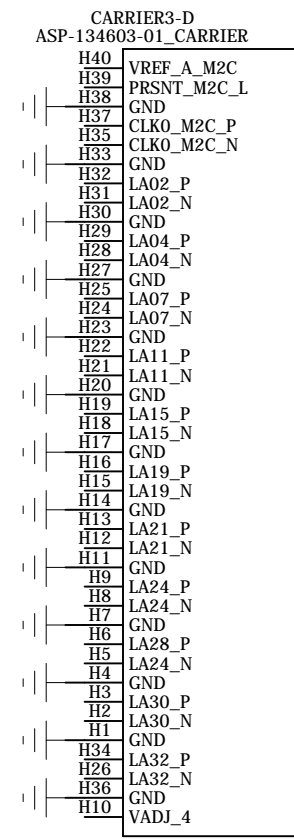
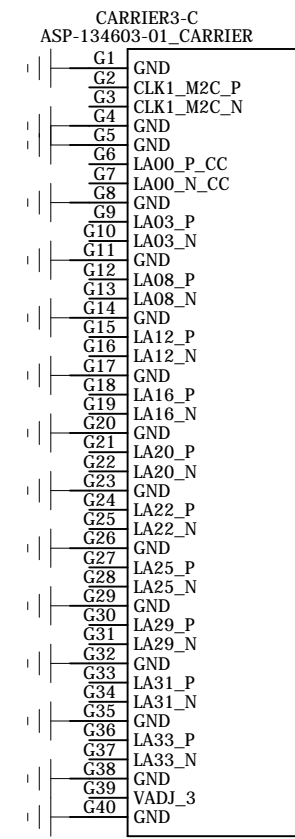
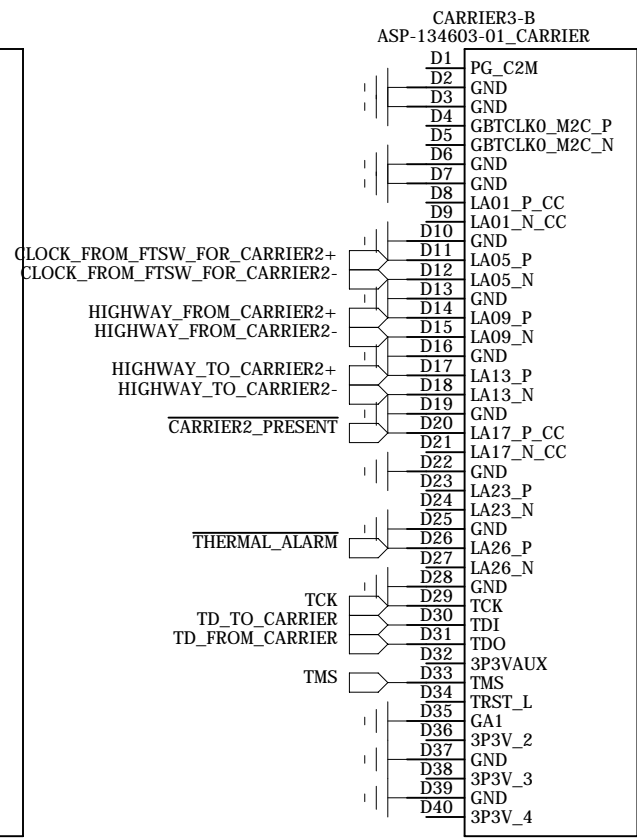
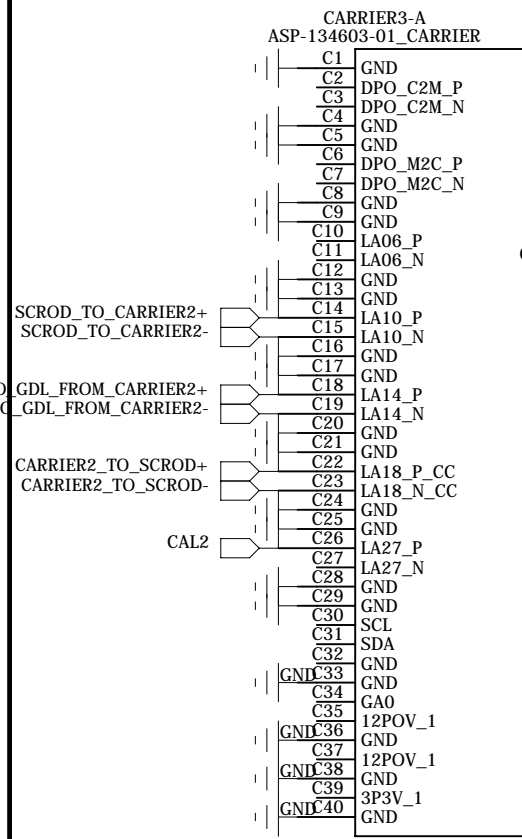
Institution	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
Title	XRM_Motherboard
Revision	A
IDLAB Design #	<IDLAB Desing #>
Circuit Design	JM
PCB Design	JM
Sheet #	2 of 5
Description	Power & testpoints
Last Modified	22-SEPTEMBER-2014



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Title	XRM_Motherboard
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IDLAB Design #	<IDLAB Desing #>
Circuit Design	JM
PCB Design	JM
Sheet #	3 of 5
Description	SCROD Connectors
Last Modified	22-SEPTEMBER-2014



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Title	XRM_Motherboard
Revision	A
IDLAB Design #	<IDLAB Desing #>
Circuit Design	JM
PCB Design	JM
Sheet #	4 of 5
Description	Carrier 1 and 2 connectors
Last Modified	22-SEPTEMBER-2014



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Title	XRM_Motherboard
Revision	A
IDLAB Design #	<IDLAB Desing #>
Circuit Design	JM
PCB Design	JM
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Description	Carrier 3 and 4 connectors
Last Modified	22-SEPTEMBER-2014