

E

D

C

B

A

E

D

C

B

A

6

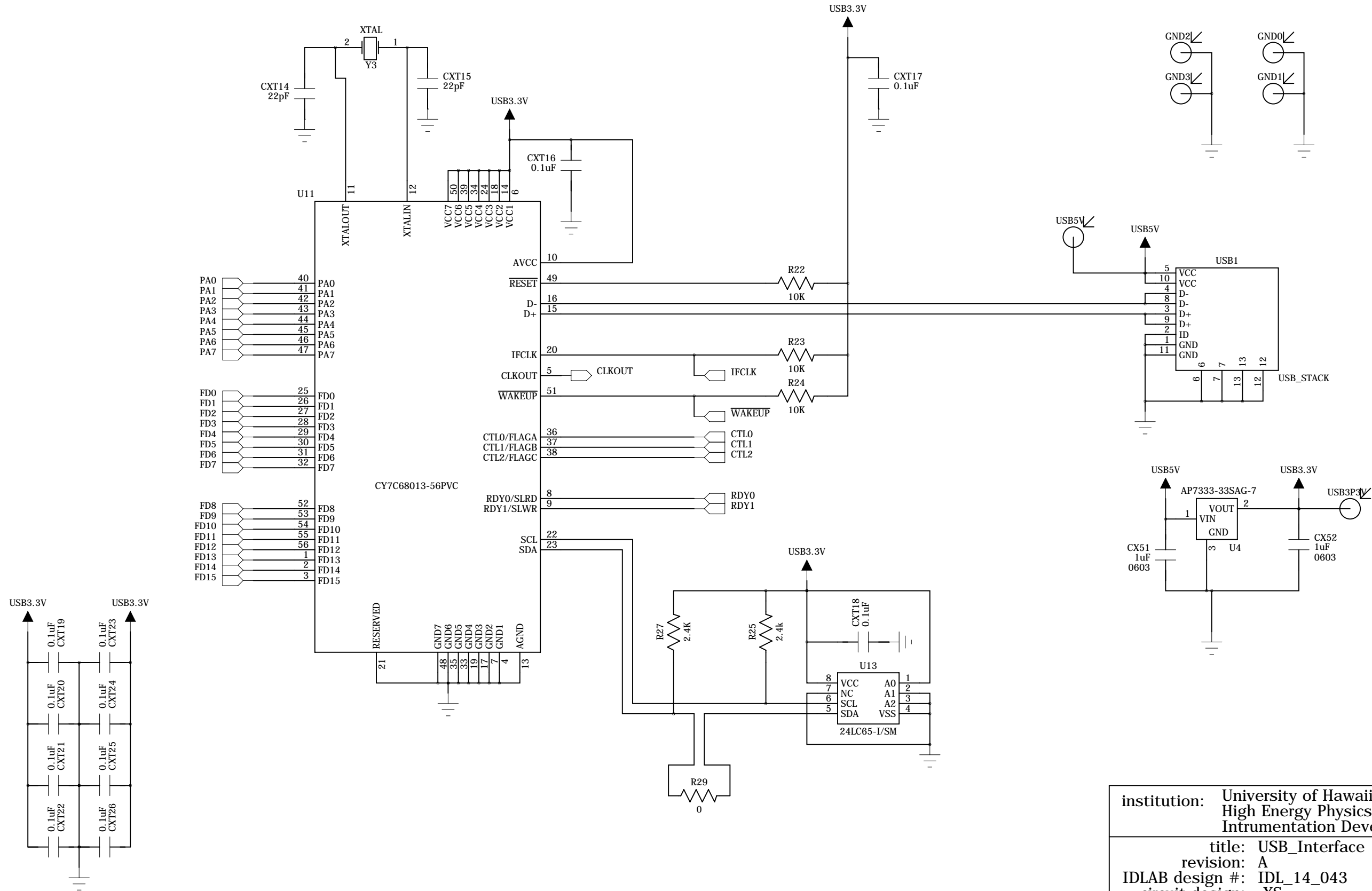
5

4

3

2

1



institution:	University of Hawaii at Manoa High Energy Physics Lab Instrumentation Development Lab
title:	USB_Interface
revision:	A
IDLAB design #:	IDL_14_043
circuit design:	XS
PCB design:	XS
sheet #:	1 of 2
sheet description:	USB 2.0 Interface
date last modified:	2014-10-18

6

5

4

3

2

1

E

E

D

D

C

C

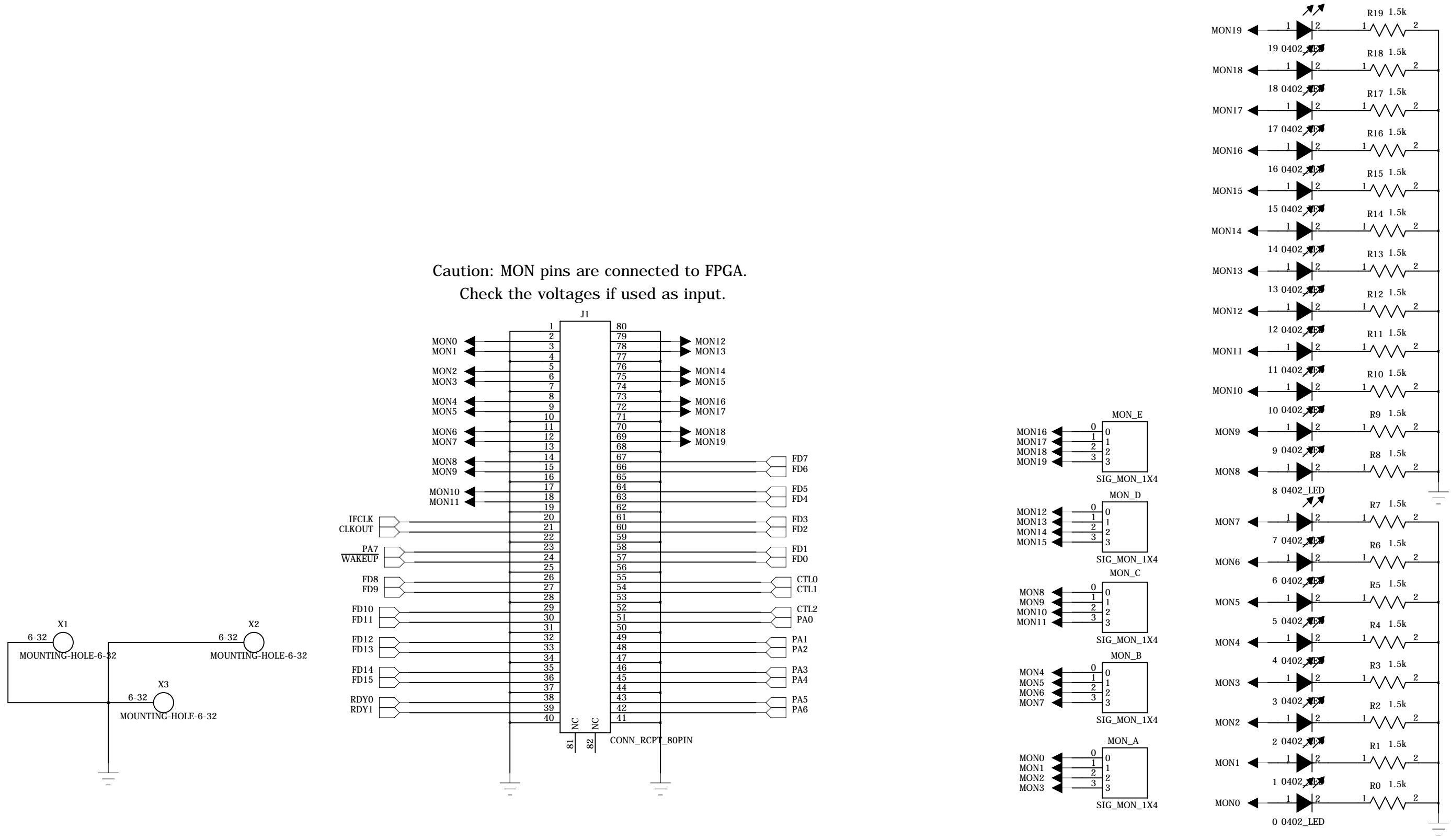
B

B

A

A

Caution: MON pins are connected to FPGA.
Check the voltages if used as input.



institution:	University of Hawaii at Manoa High Energy Physics Lab Instrumentation Development Lab
title:	USB_Interface
revision:	A
IDLAB design #:	IDL_14_043
circuit design:	XS
PCB design:	XS
sheet #:	2 of 2
sheet description:	LEDs and In/Out
date last modified:	2014-10-18