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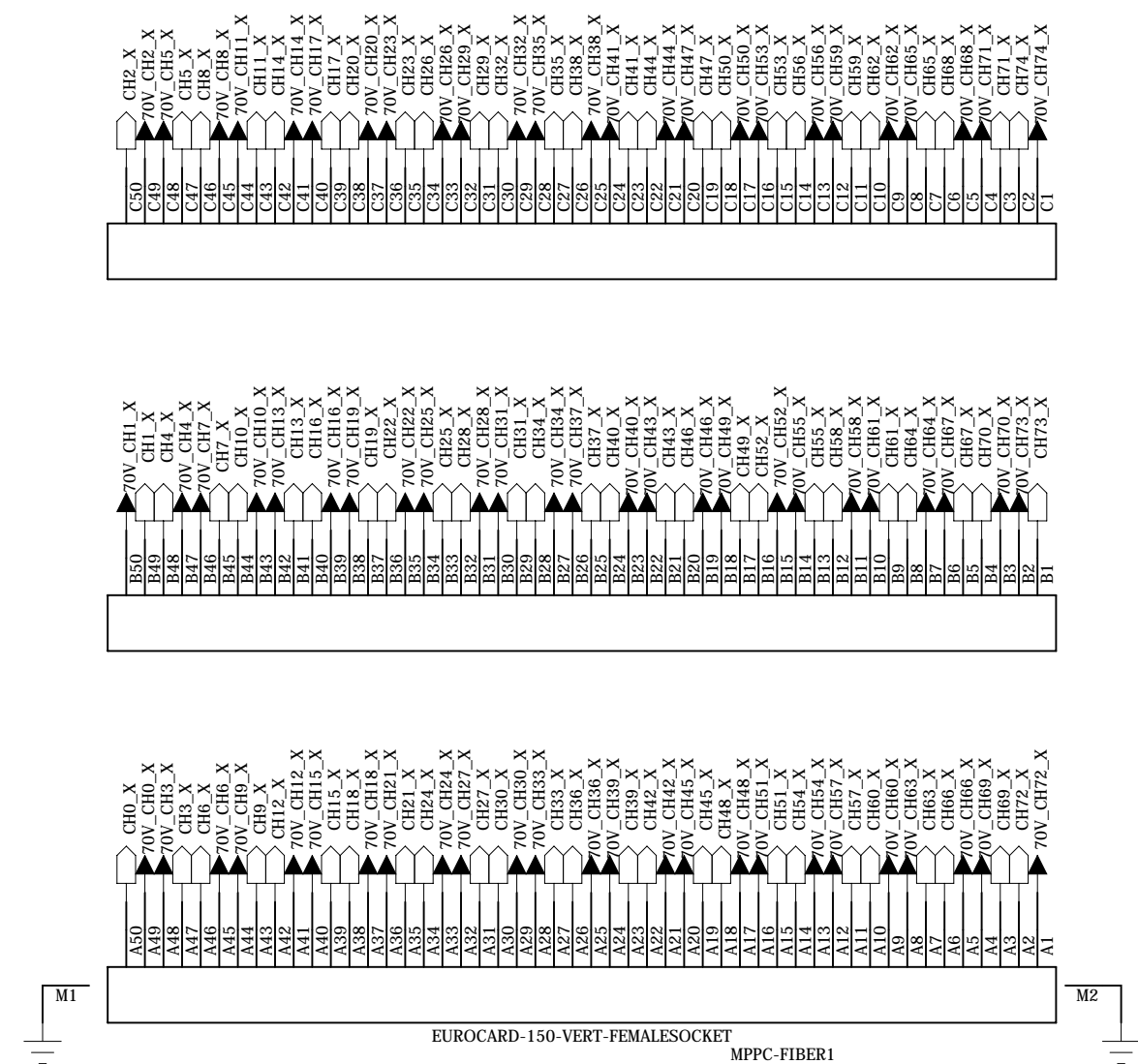
C

B

B

A

A



institution:	University of Hawaii at Manoa High Energy Physics Lab Instrumentation Development Lab
title:	MB_SciFi_Tracker
revision:	C
IDLAB design #:	IDL_14_017
circuit design:	GSV XS
PCB design:	XS
sheet #:	
sheet description:	
date last modified:	May-27-2014

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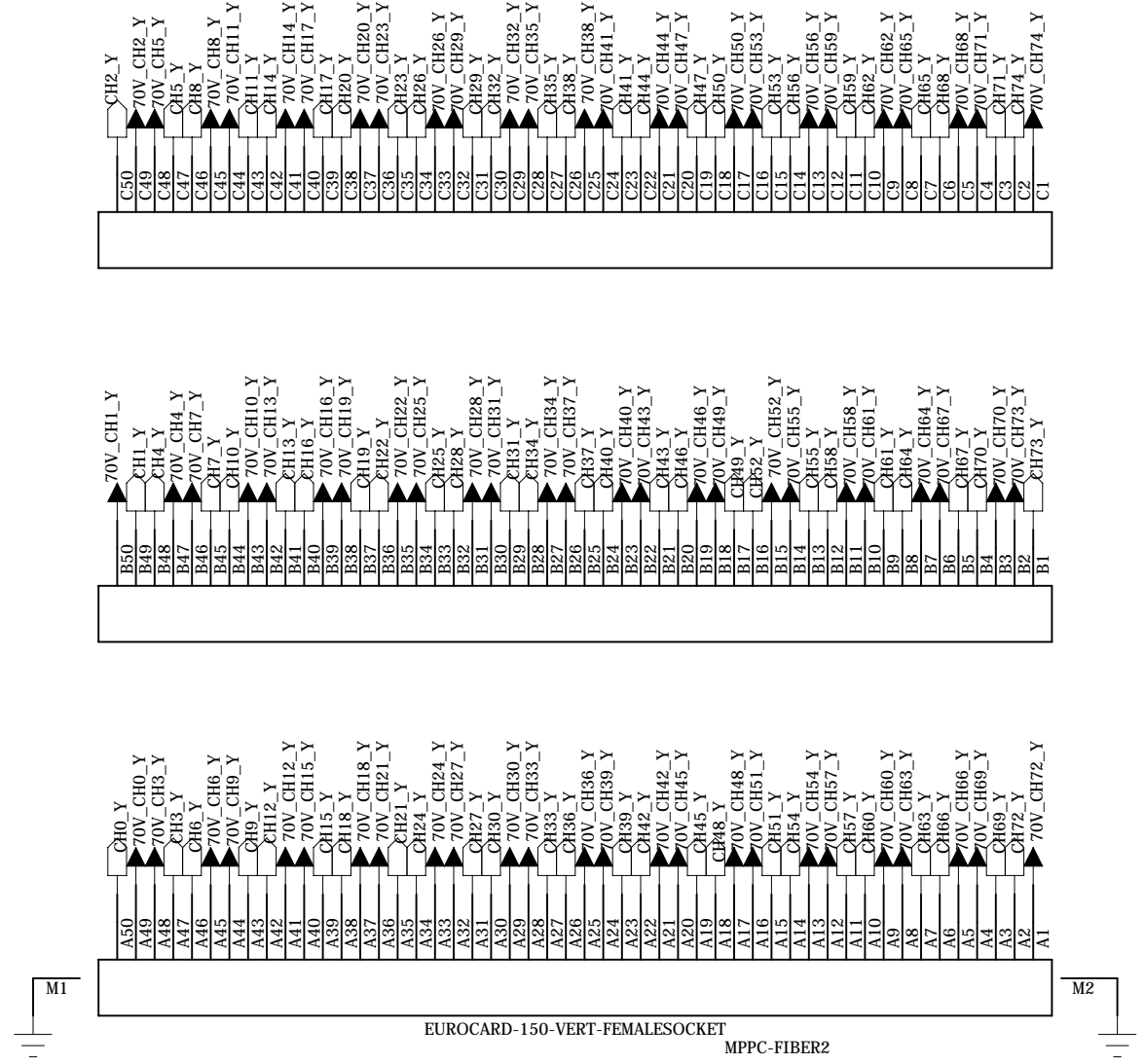
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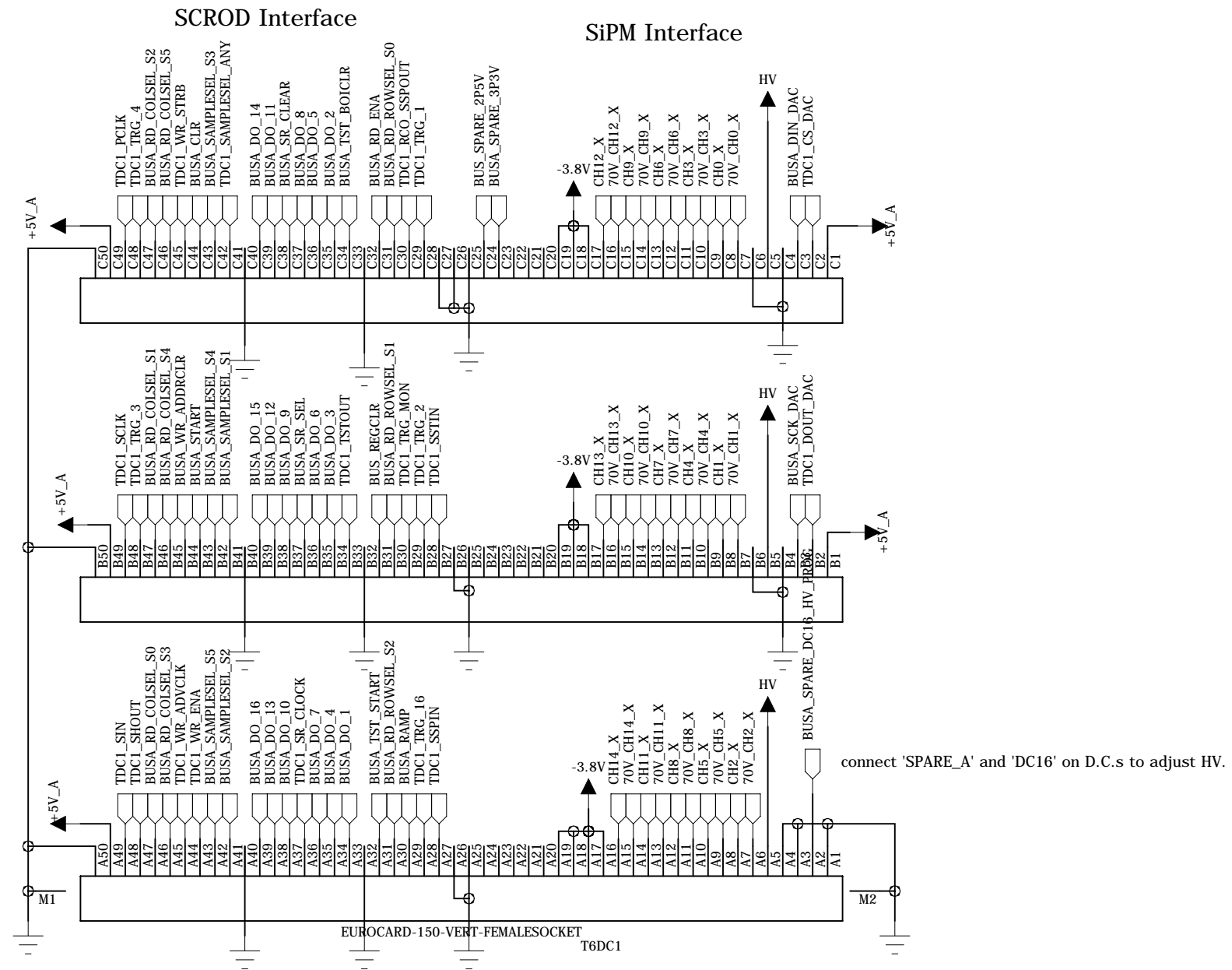
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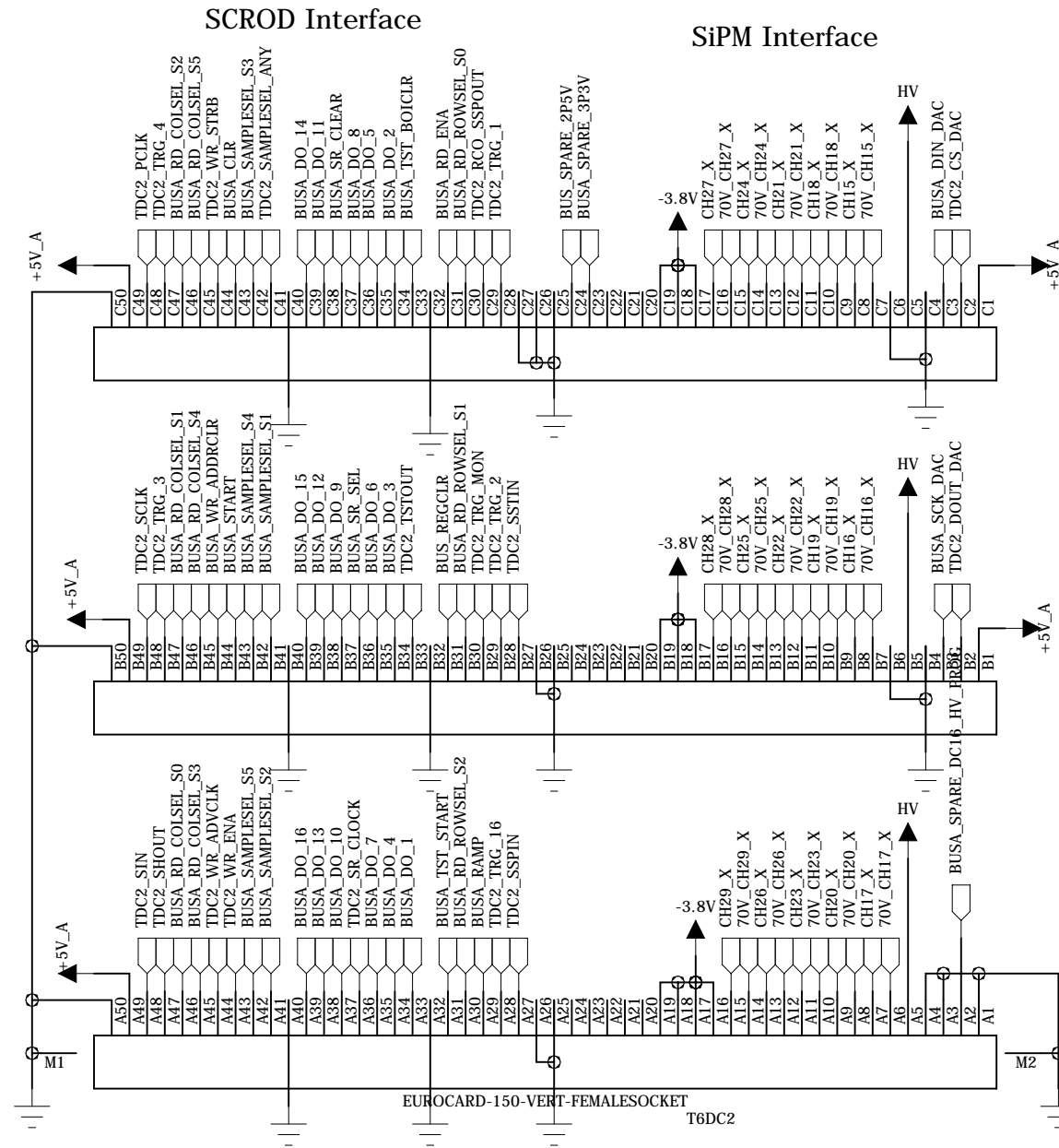
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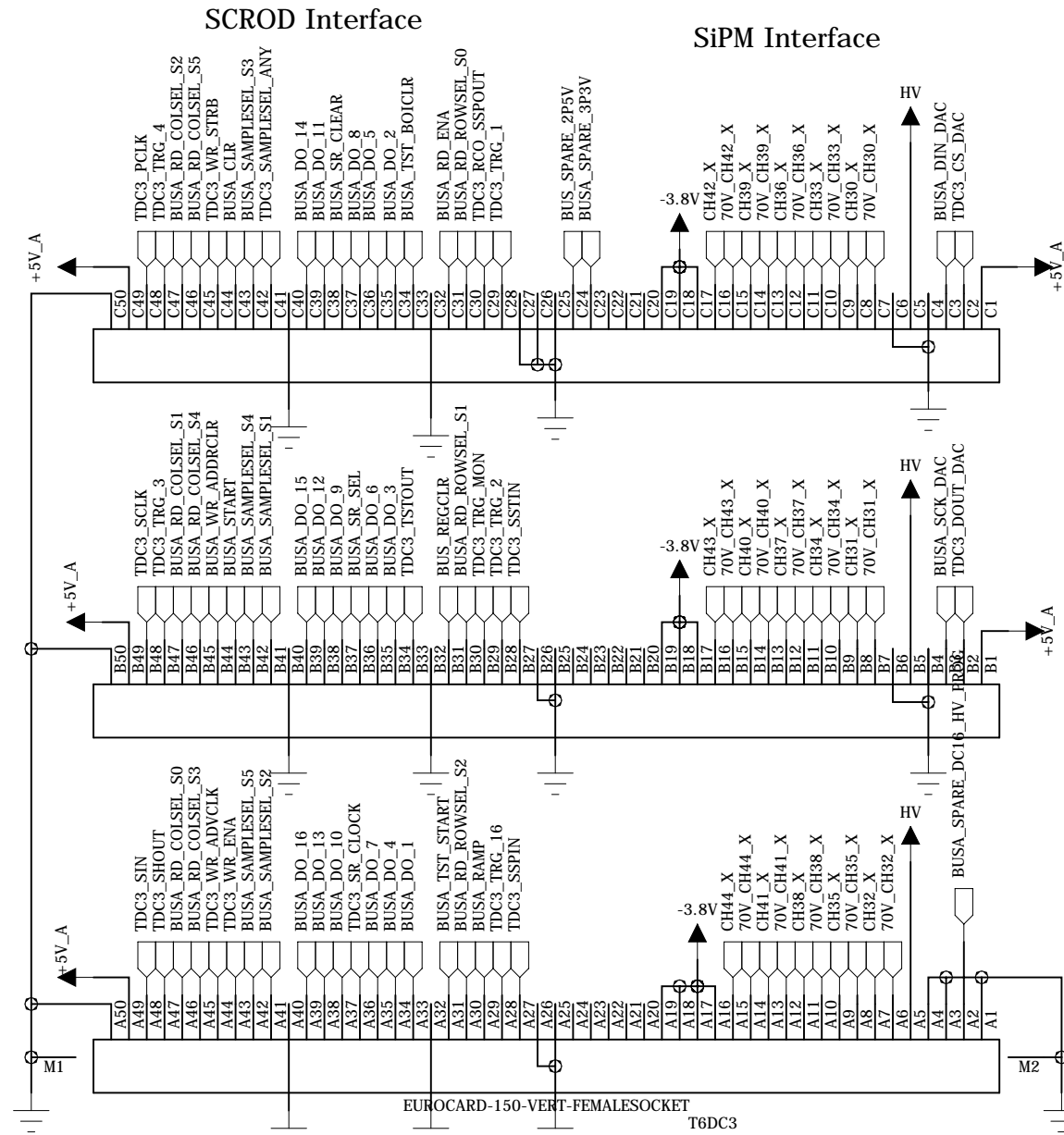
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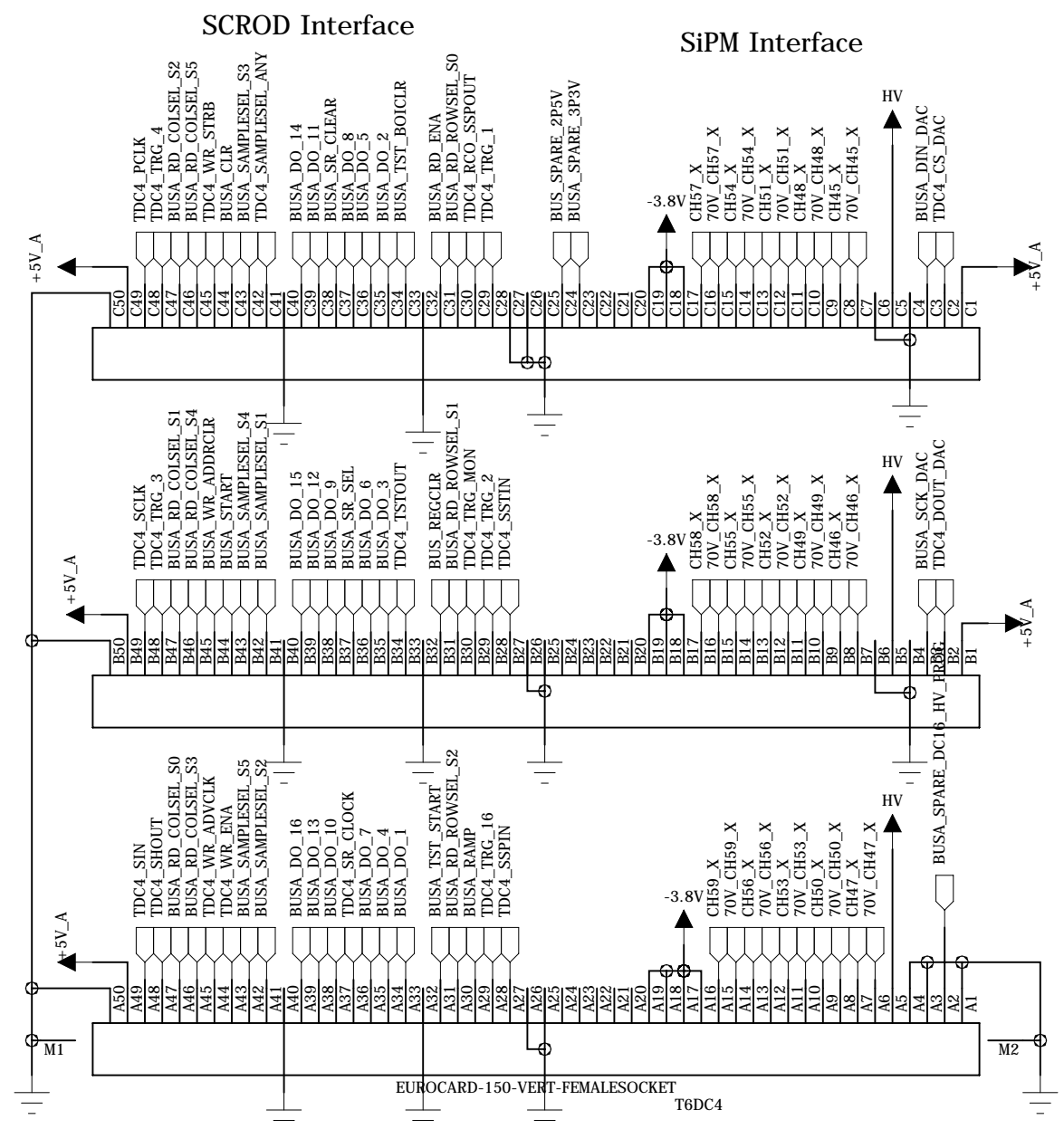
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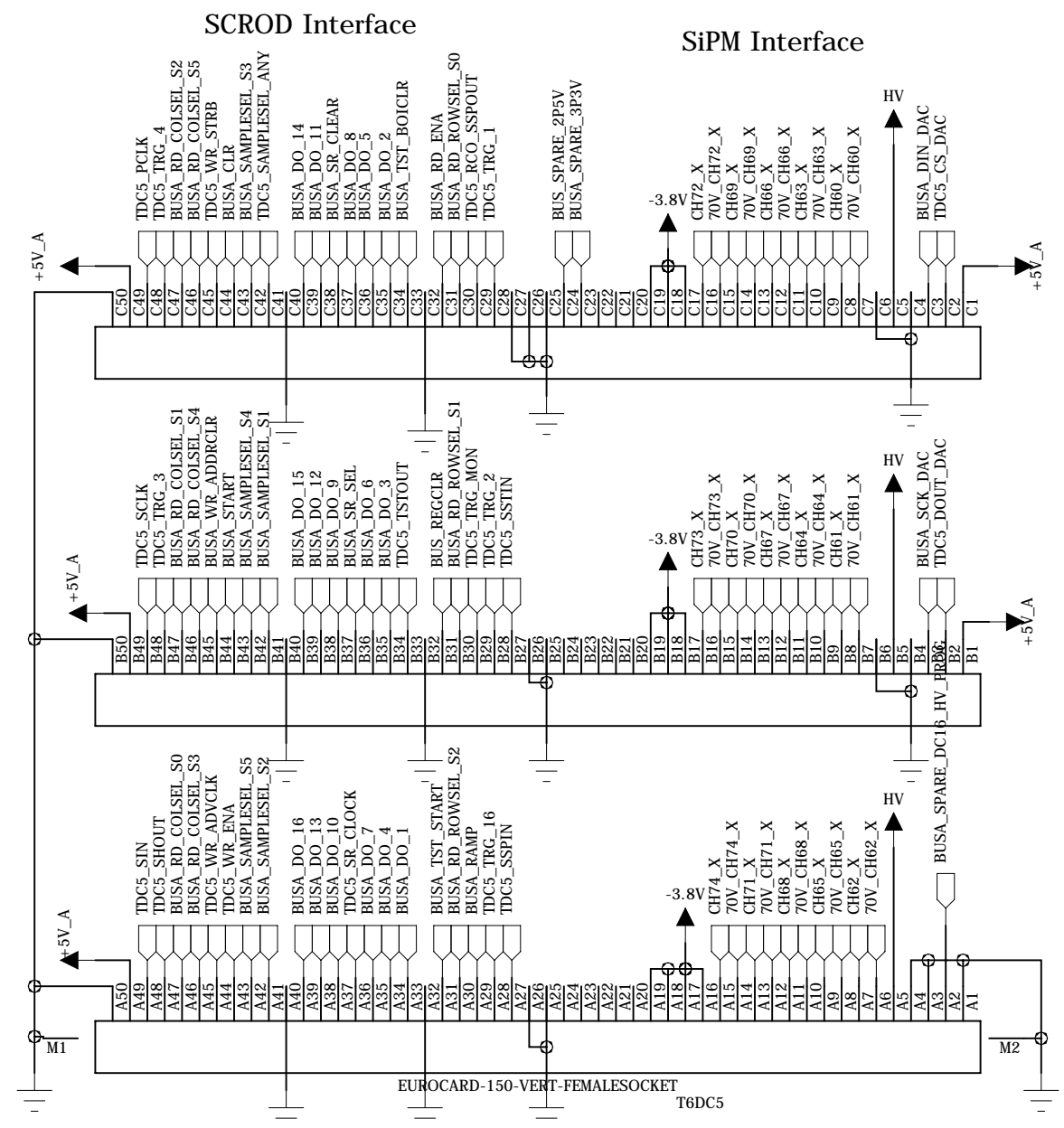
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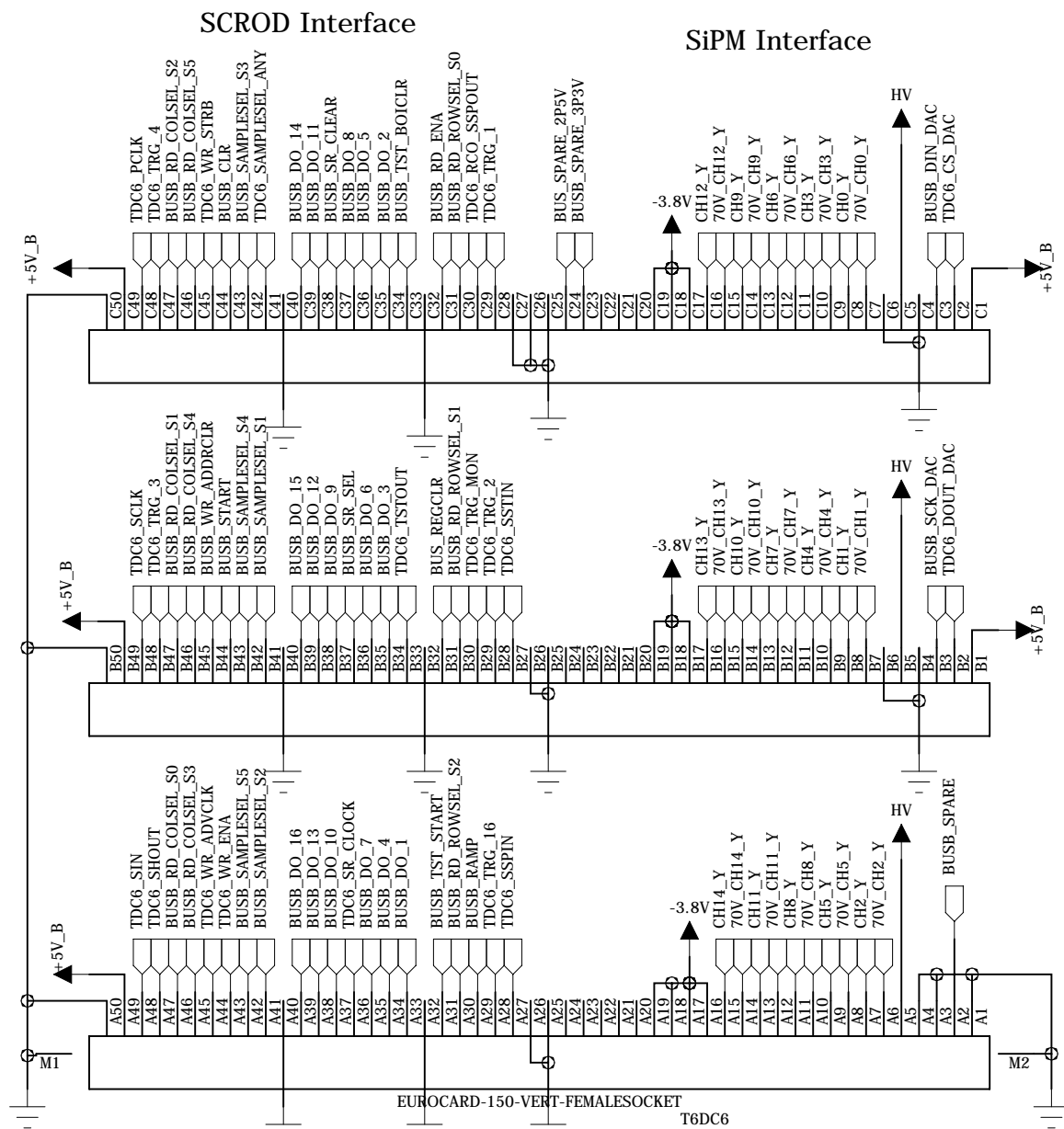
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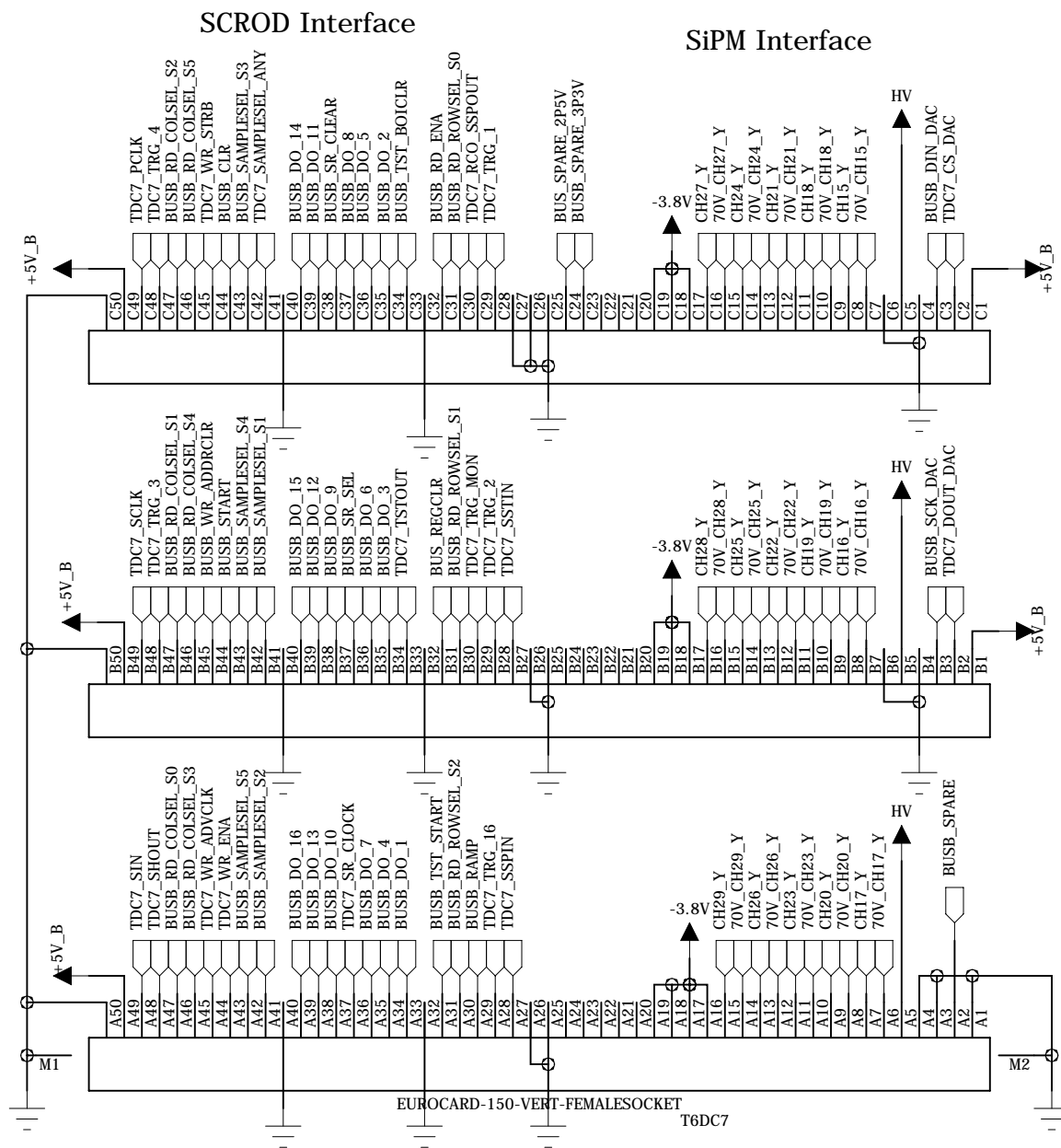
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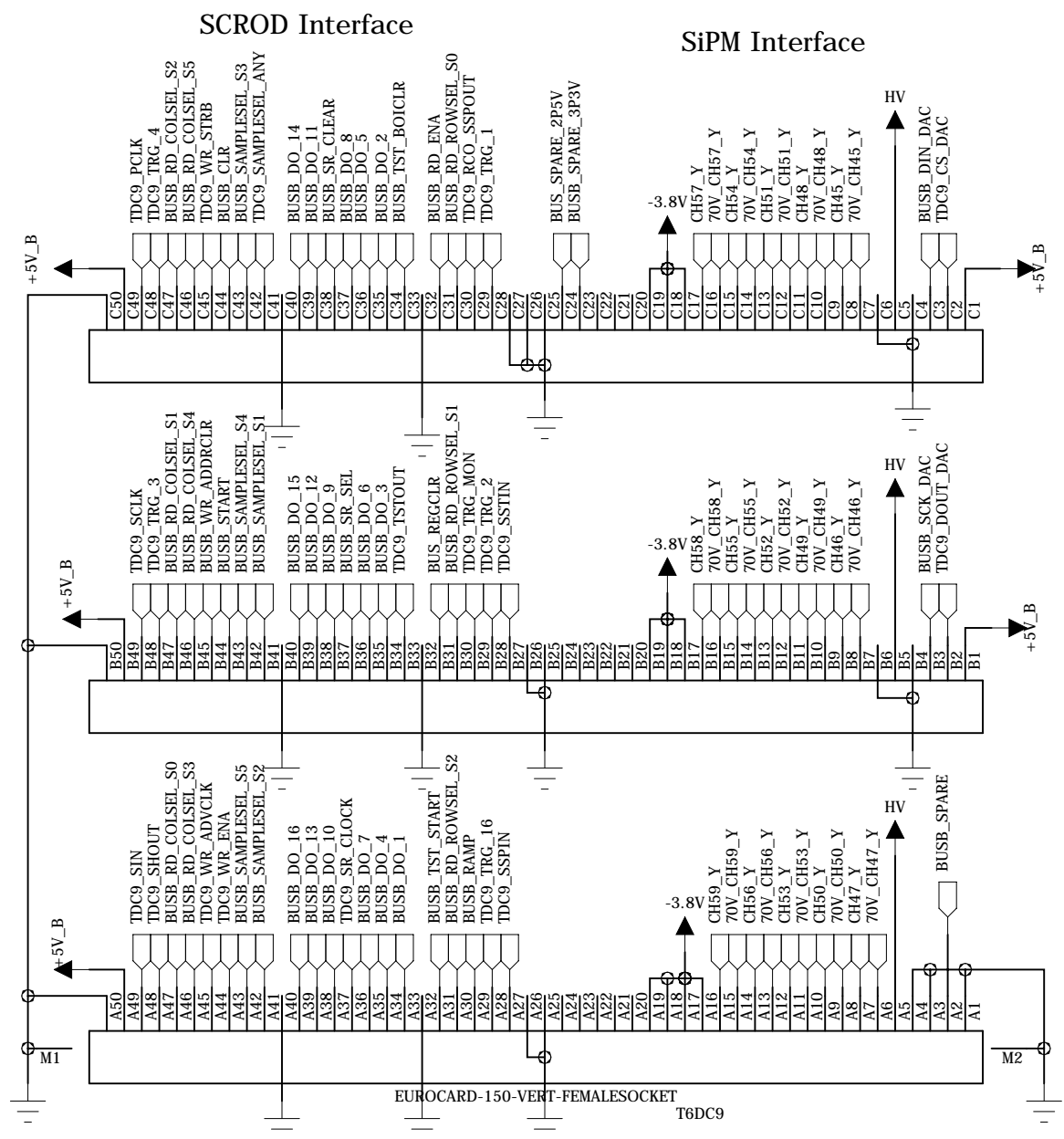
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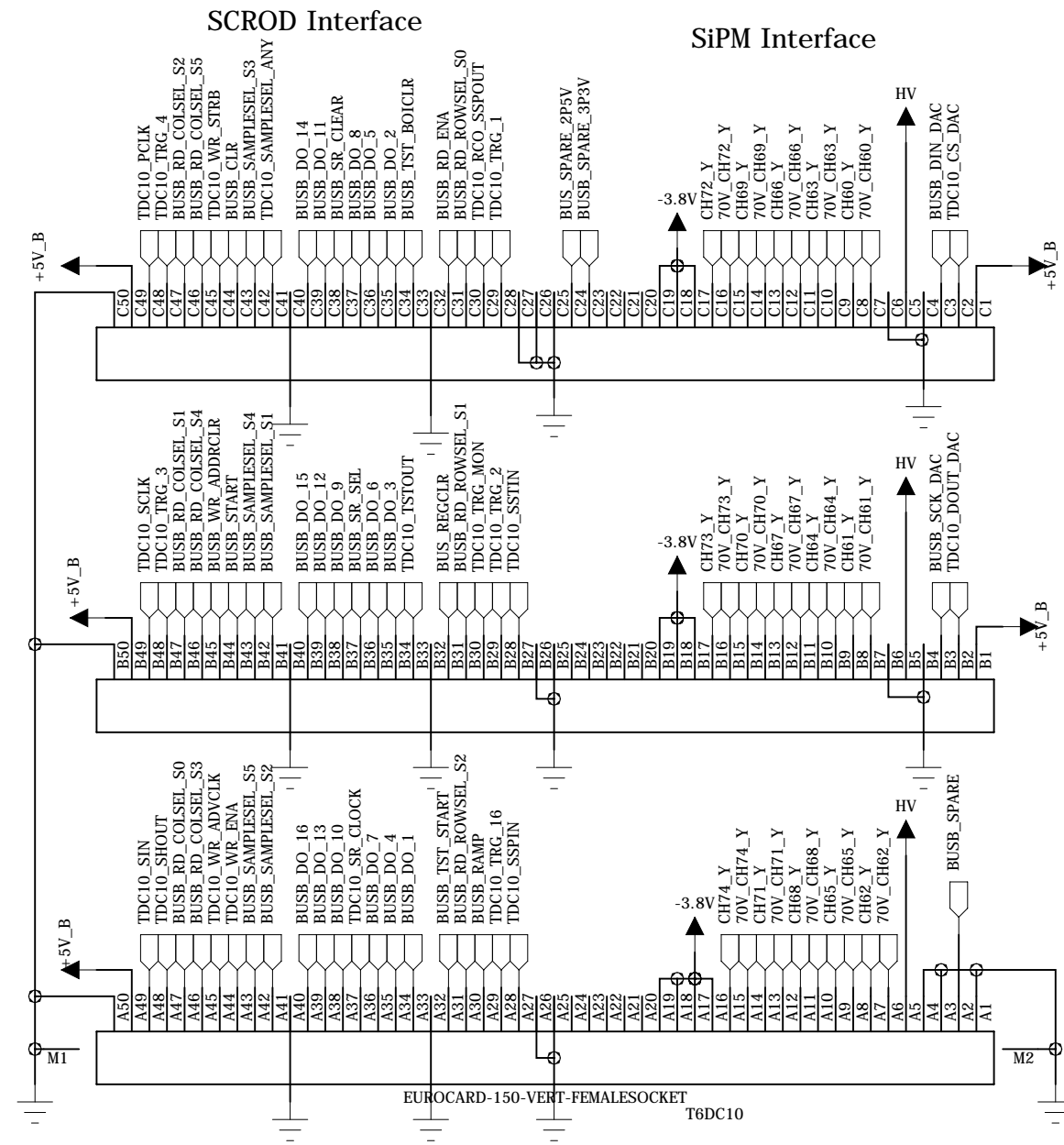
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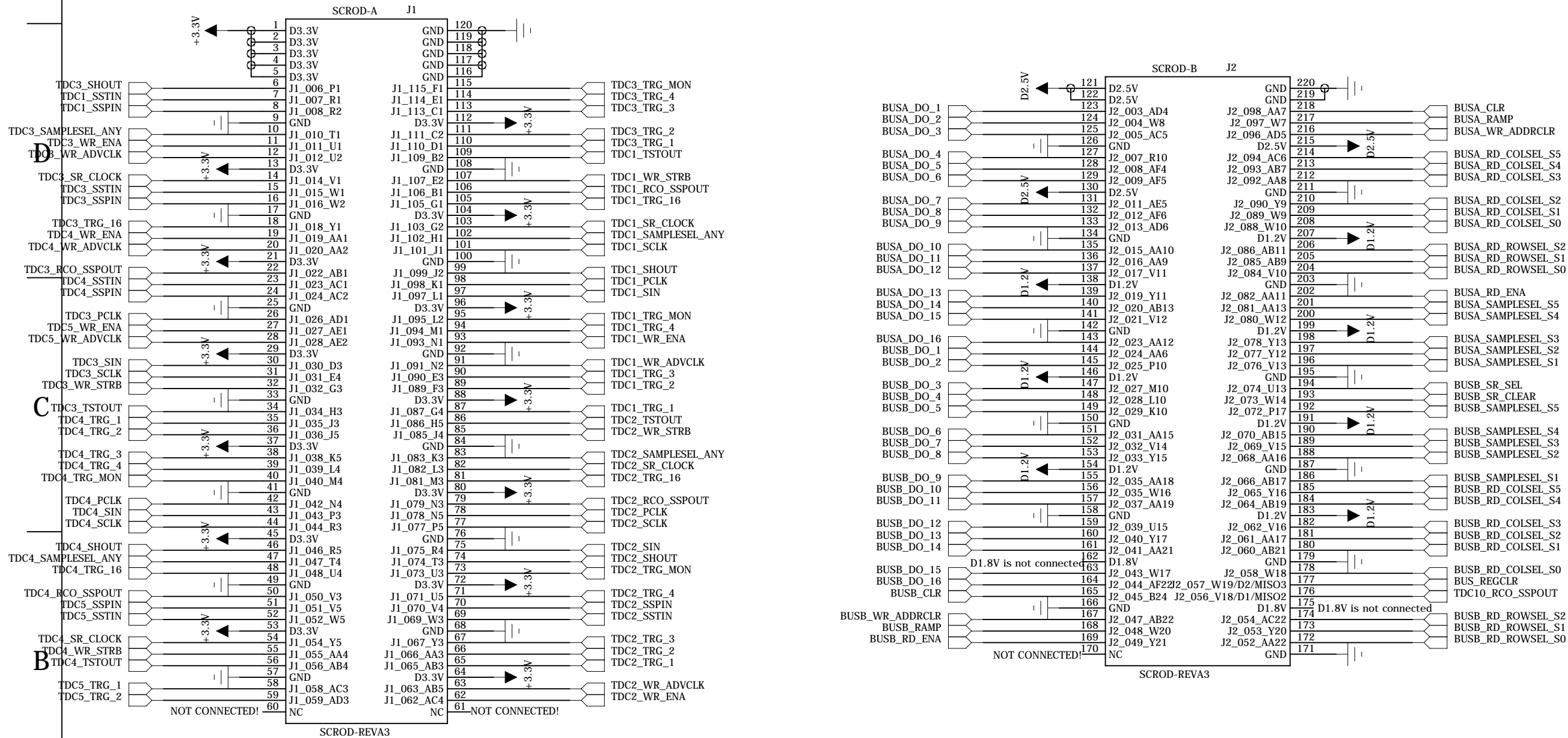
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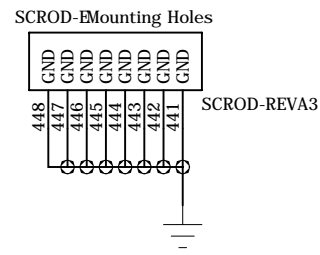
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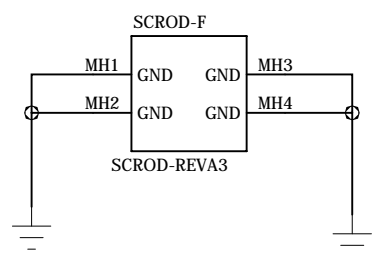
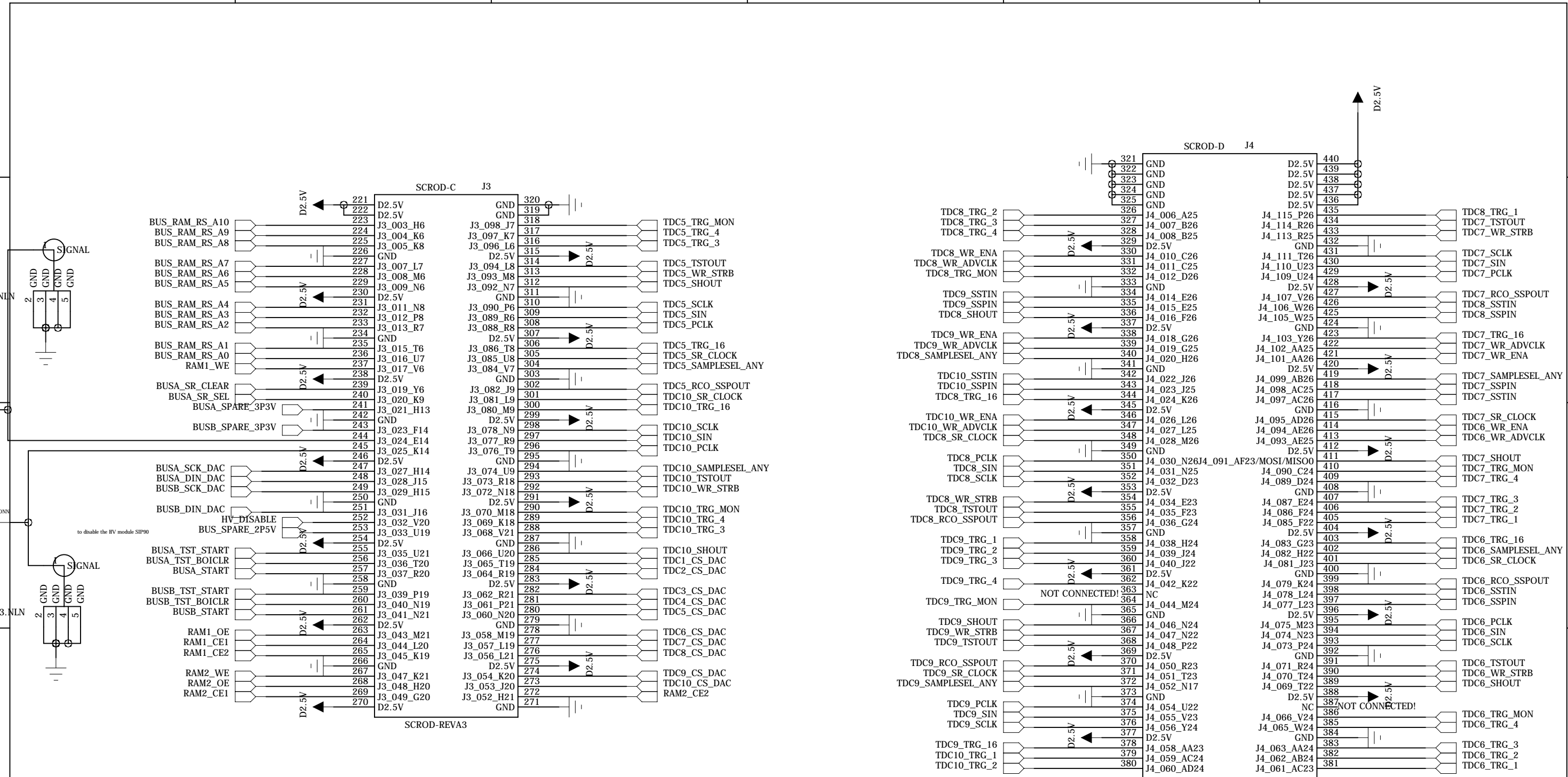
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LEMO2
LEMO_EPL.00.113.NLN

RIGGER1

EX_TRIGGER2
LEMO2

LEMO1
LEMO_EPL.00.113.NLN



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'SPARE_A' pins on D.C. 6, 7, 8, 9, 10

E

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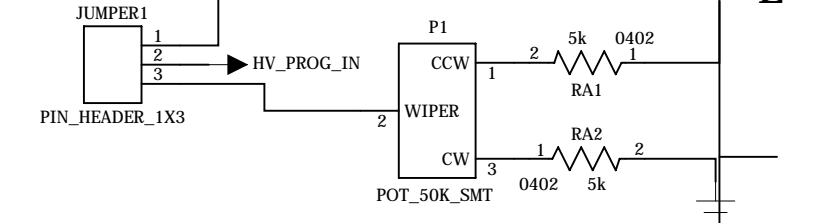
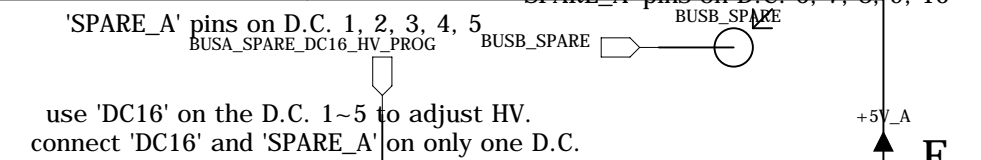
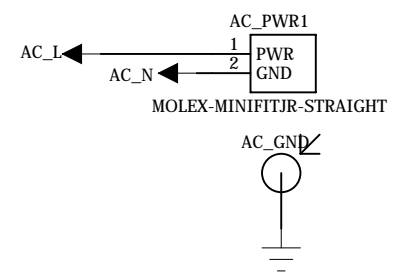
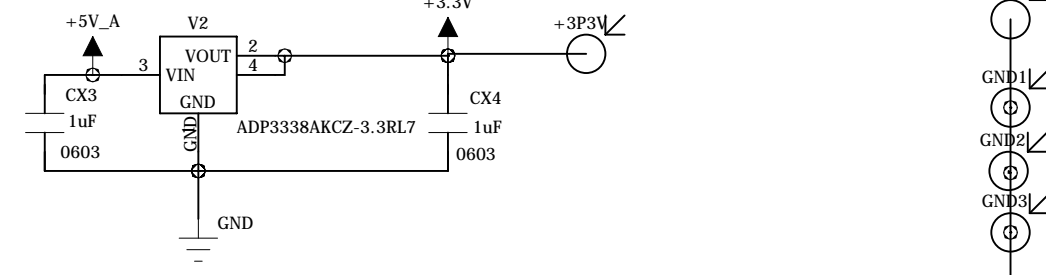
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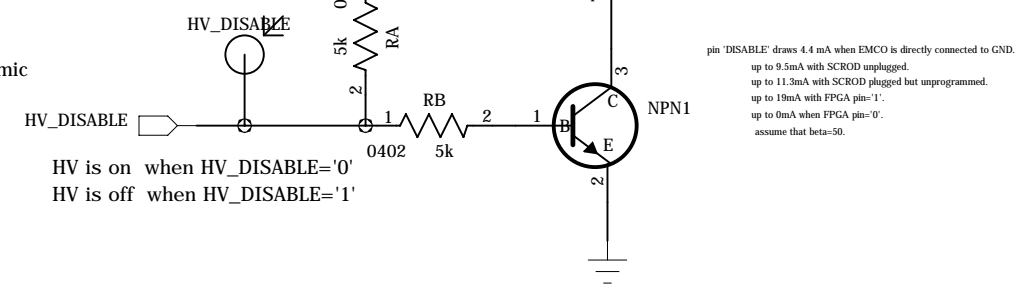
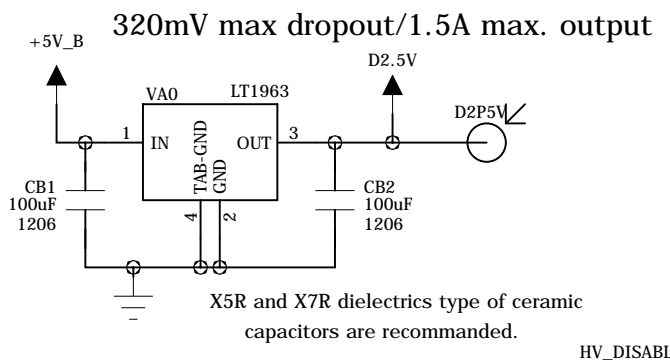
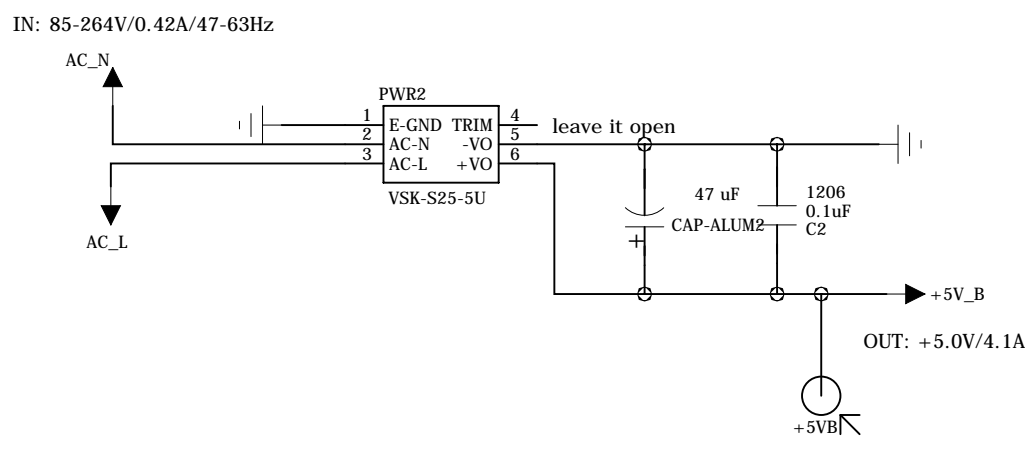
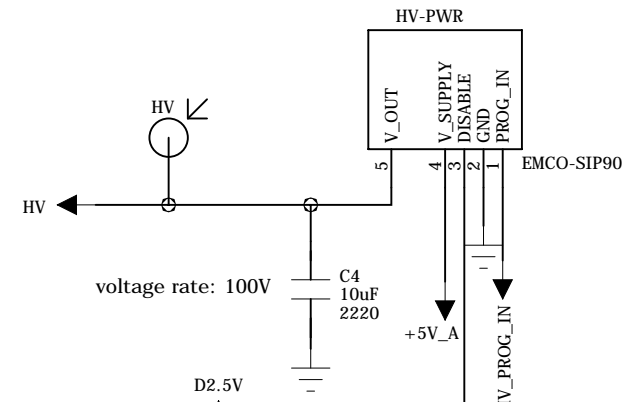
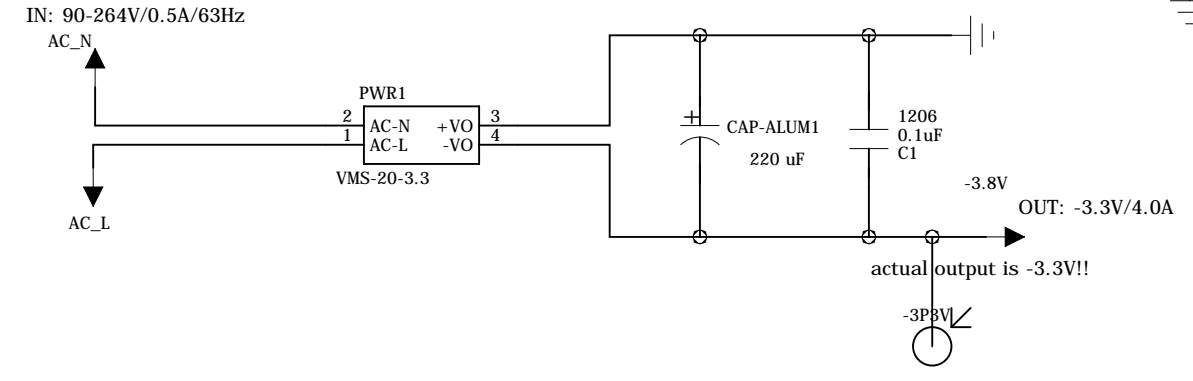
A

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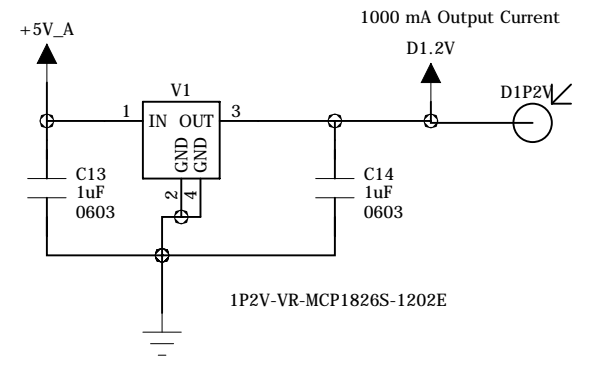
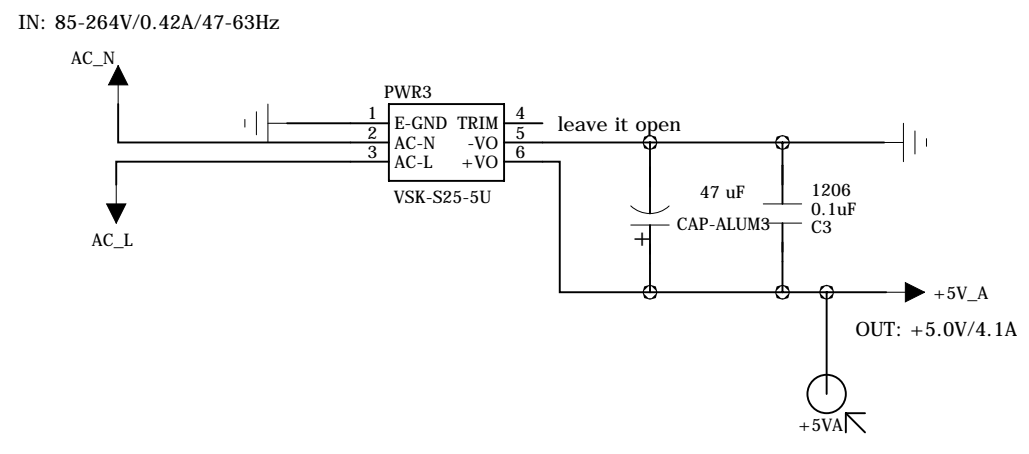


Vprog	Vout
0.5	83.45
1.4	70.76
3.3	43.97

Vout=90.5-14.1*Vprog



pin 'DISABLE' draws 4.4 mA when EMCO is directly connected to GND.
 up to 9.5mA with SCROD unplugged.
 up to 11.3mA with SCROD plugged but unprogrammed.
 up to 19mA with FPGA pins='1'.
 up to 0mA when FPGA pins='0'.
 assume that beta=50.



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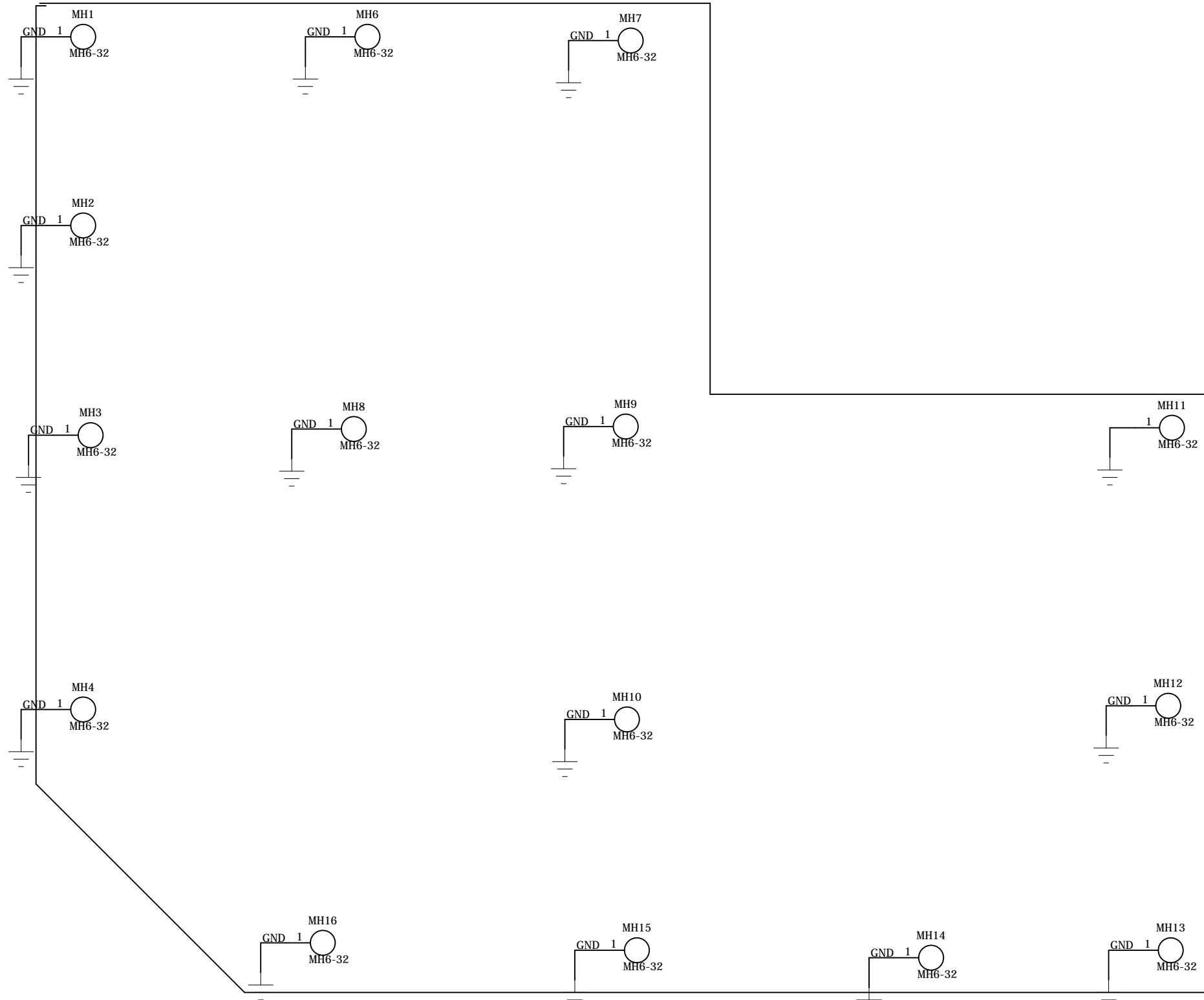
C

B

B

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E

E

D

D

C

C

B

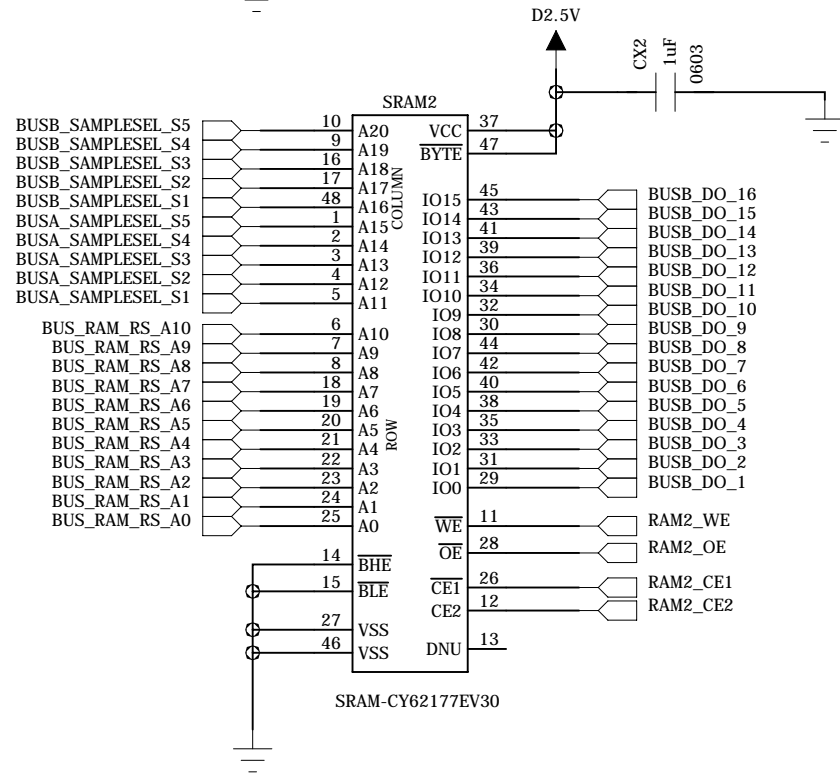
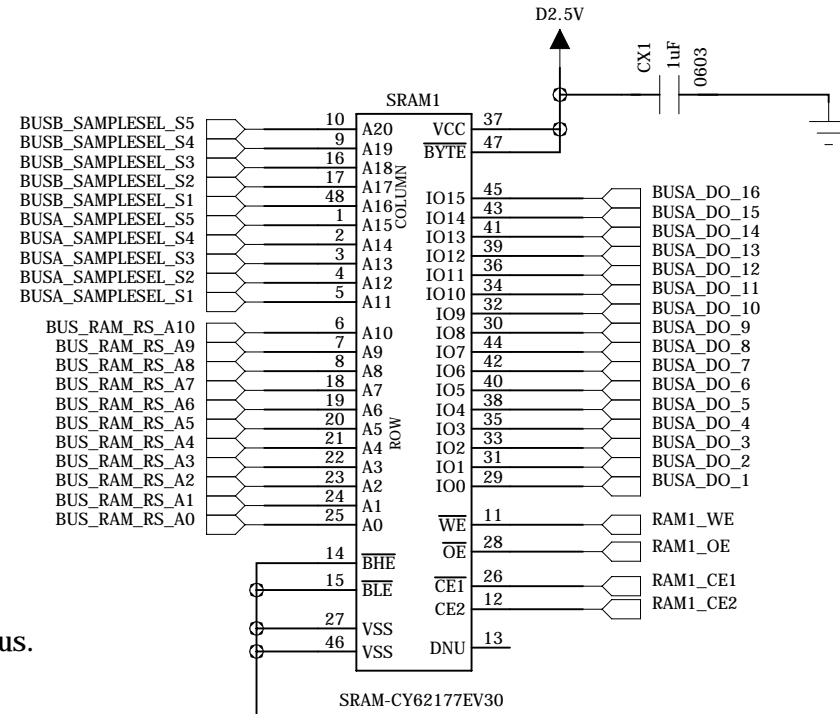
B

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A

Typical active current: 4.5 mA at f = 1 MHz

RAM1 and RAM2 share the address bus.



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