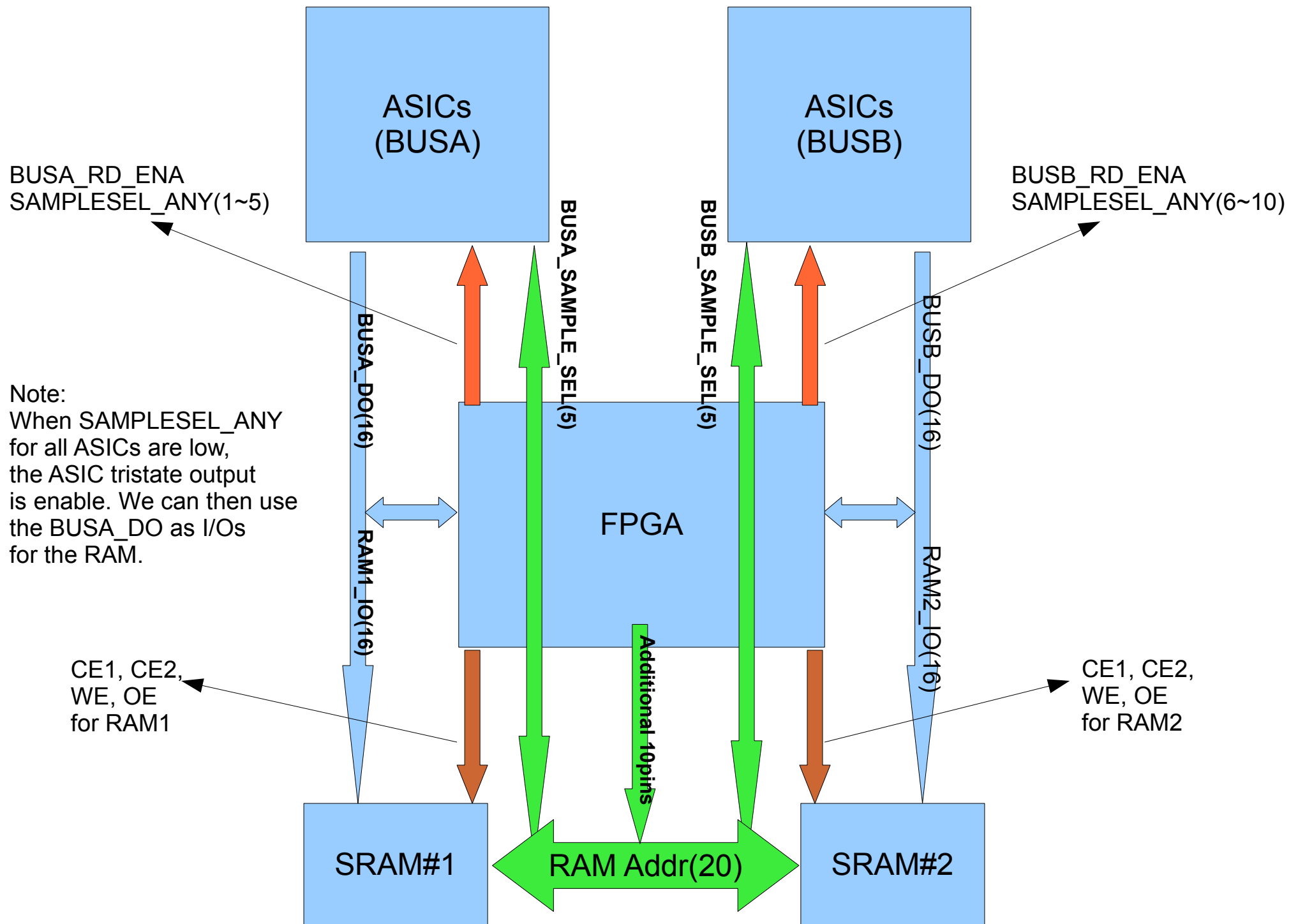




KLM M.B.TARGET X Pedestal Diagram
April 3rd, 2014

Truth table for the CY62177EV30 (in 2M x 16 configuration)

Datasheet page 11

$\overline{\text{CE1}}$	CE2	$\overline{\text{WE}}$	$\overline{\text{OE}}$	I/O	Mode	Power
H	X	X	X	High Z	Power down	standby
X	L	X	X	High Z	Power down	standby
L	H	H	L	Data out I/O(0~15)	Read	Active
L	H	L	X	Data in I/O(0~15)	Write	Active
L	H	H	H	High Z	Output Disabled	Active

Notes:

- Tie $\overline{\text{BYTE}}$ to VCC to use it as a 2M x 16 SRAM.
- Tie $\overline{\text{BHE}}$ and $\overline{\text{BLE}}$ to GND