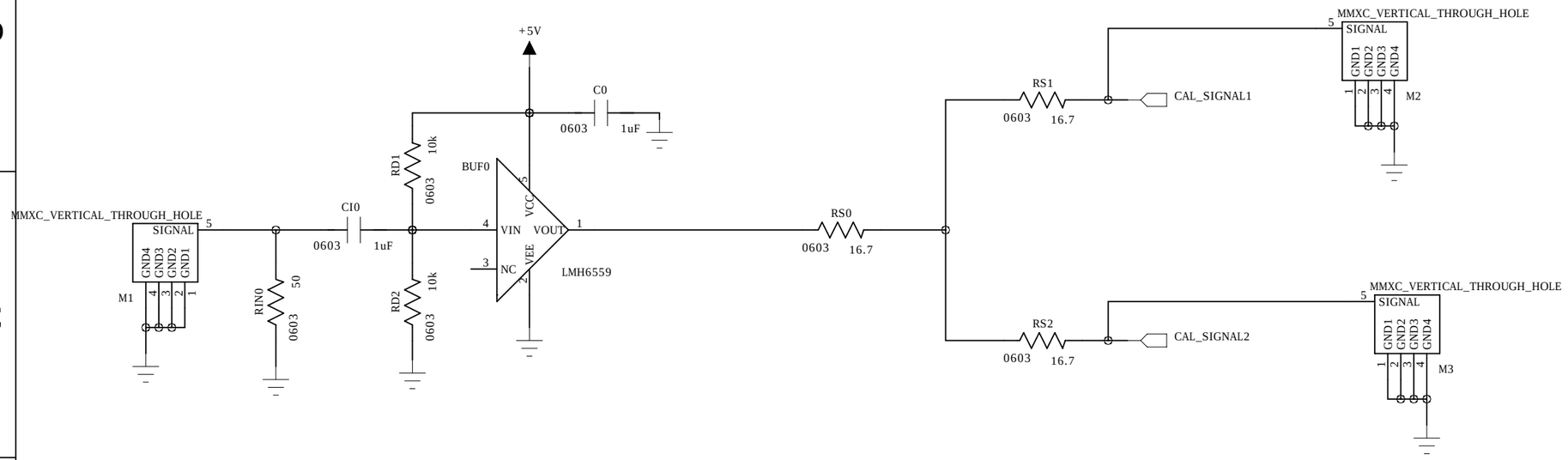
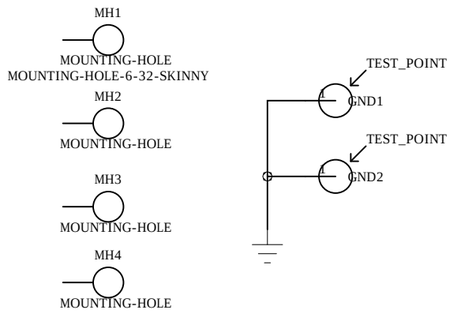
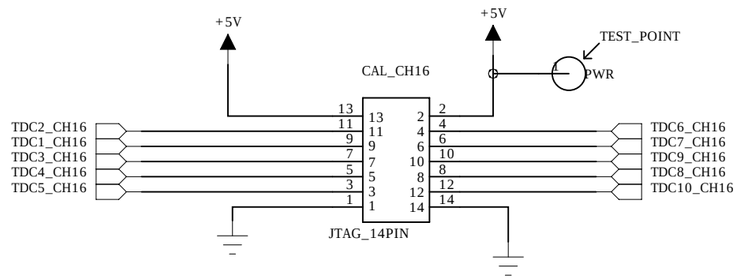


For KLM, the input is a DC bias sine wave.
CI0=0 ohm and do not install RD1 and RD2





institution:	University of Hawaii at Manoa High Energy Physics Lab Intrumentation Development Lab
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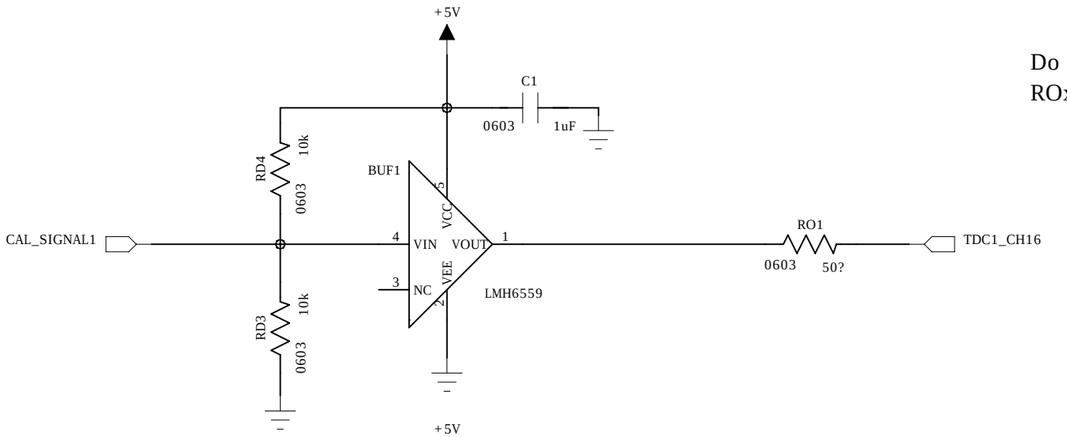
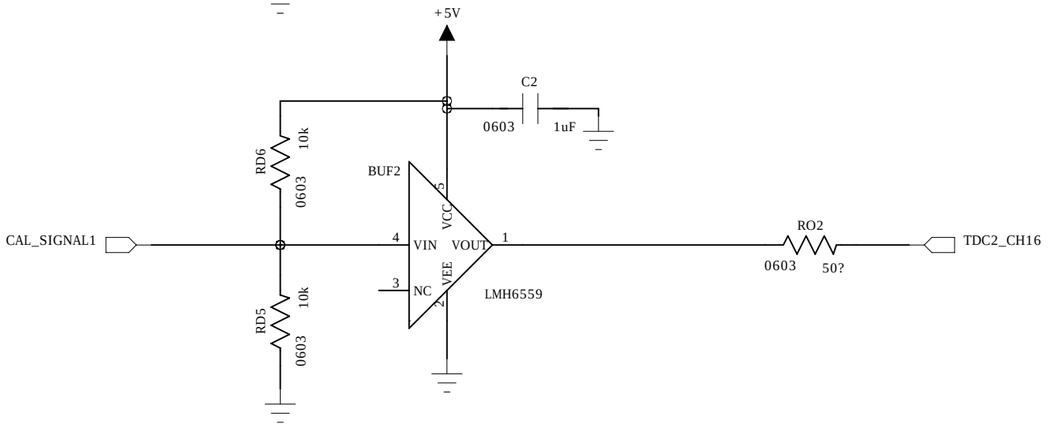
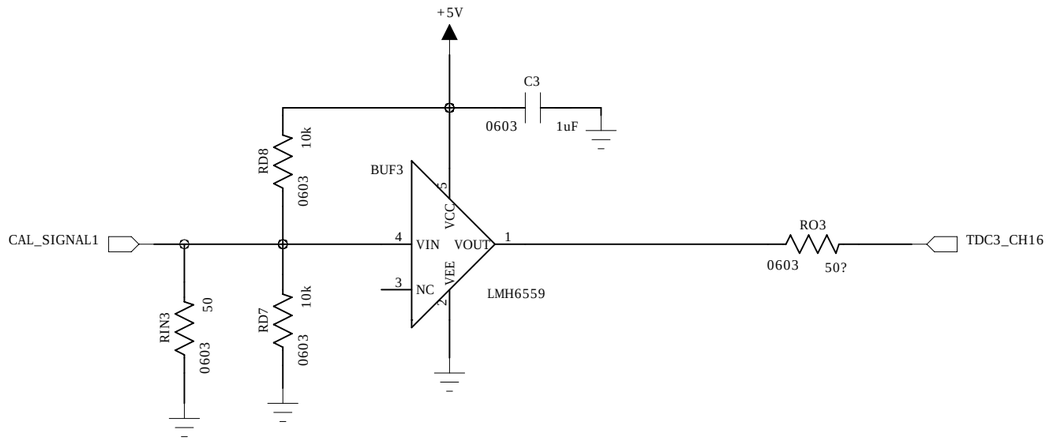
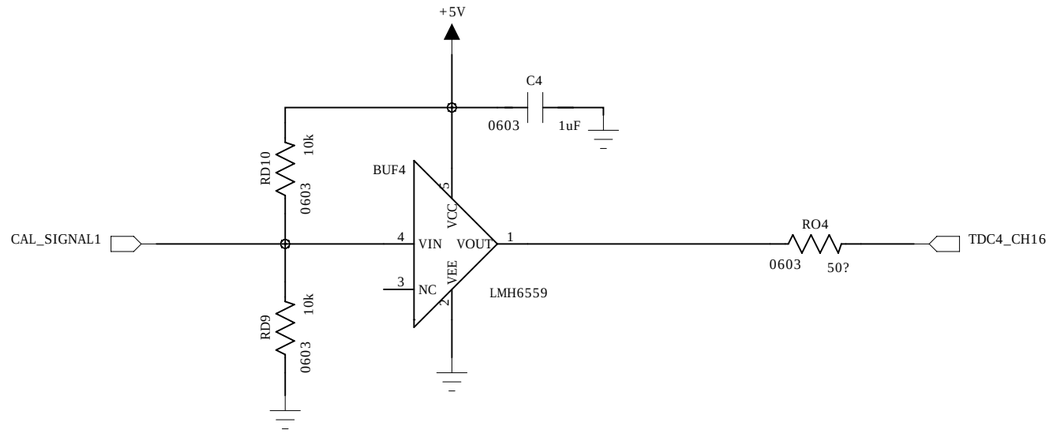
revision:	A
IDLAB design #:	IDL_15_016
circuit design:	IDLAB
PCB design:	XS
sheet #:	1 of 3
sheet description:	.
date last modified:	2015-05-18

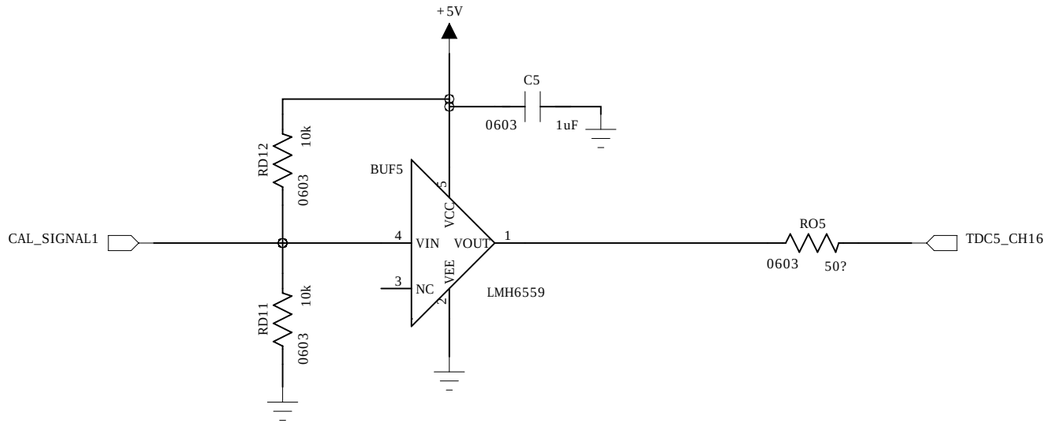
Do not install DC bias resistors RDx.
ROx = 0 ohm by default.











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sheet description:	.
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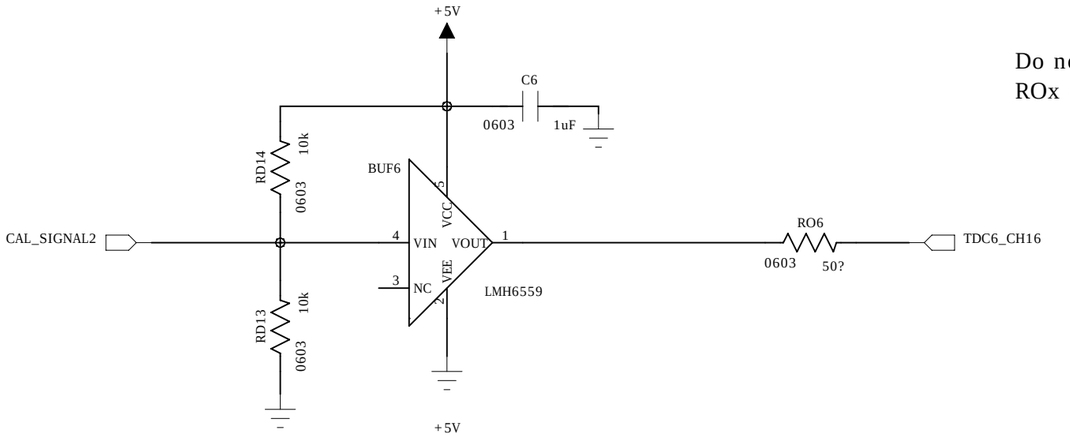
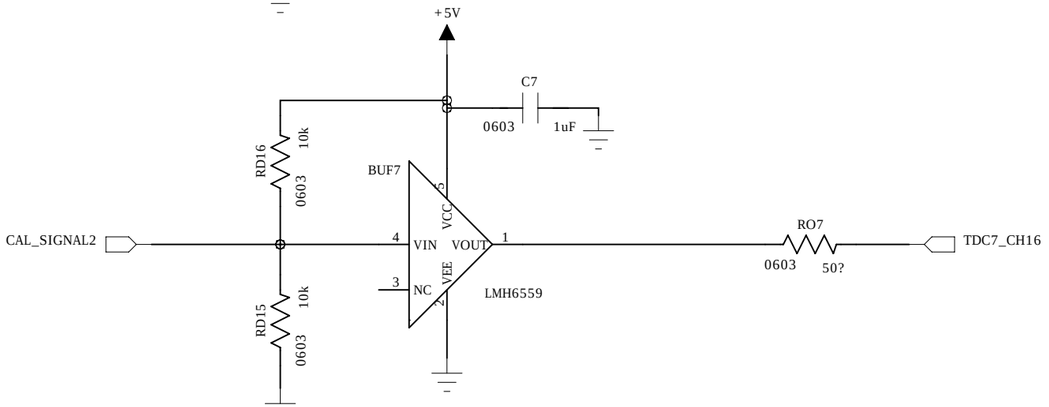
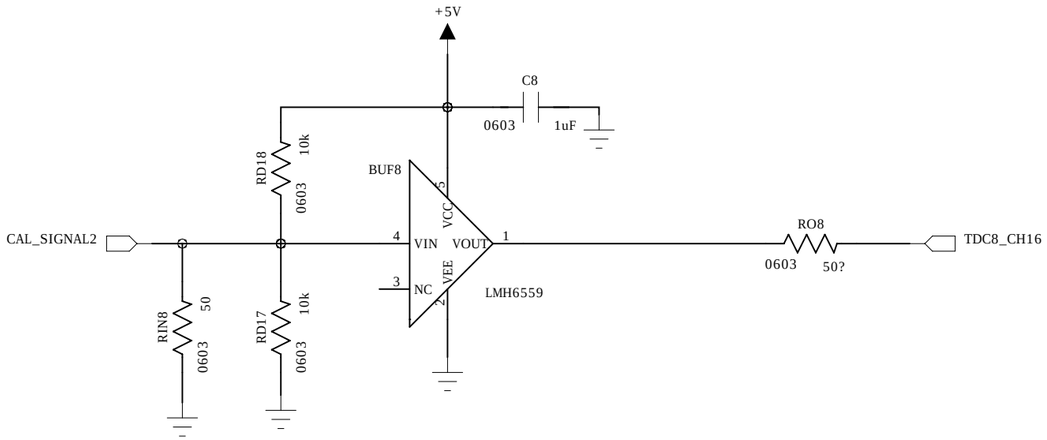
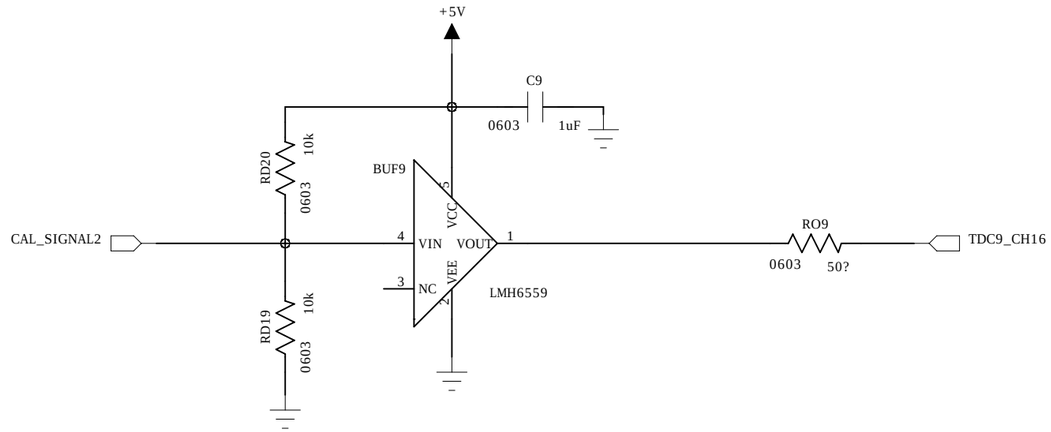
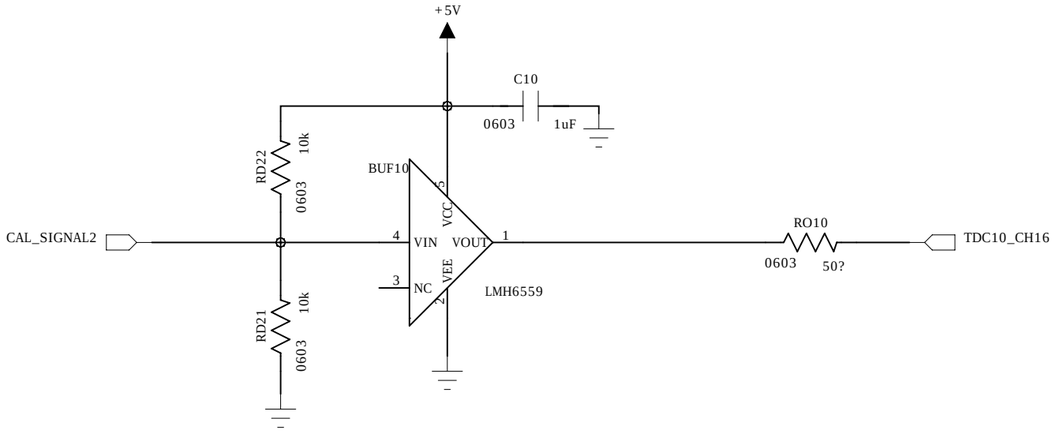
Do not install DC bias resistors RDx.
ROx = 0 ohm by default.











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