

institution: University of Hawaii at Manoa
 High Energy Physics Lab
 Instrumentation Development Lab
 title: IRSX Reference Schematic
 revision: A
 IDLAB design #: IDL_14_011
 circuit design: JLB, GSV
 PCB design: JLB
 sheet #: 1 of 3
 sheet description: ASIC & POWER
 date last modified: MAY 6, 2014

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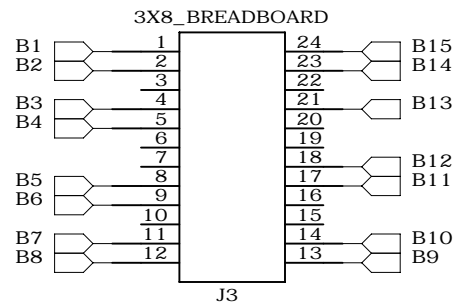
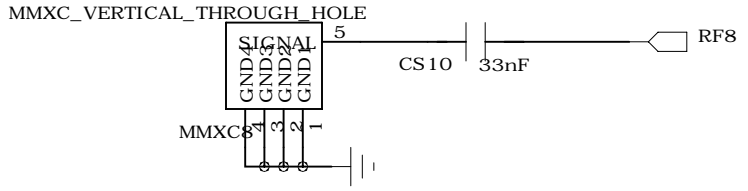
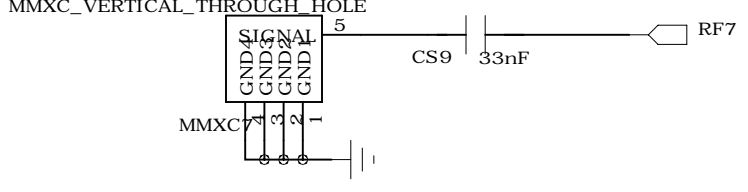
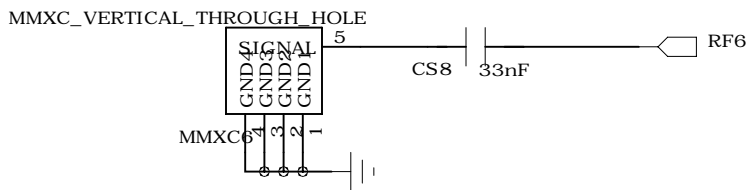
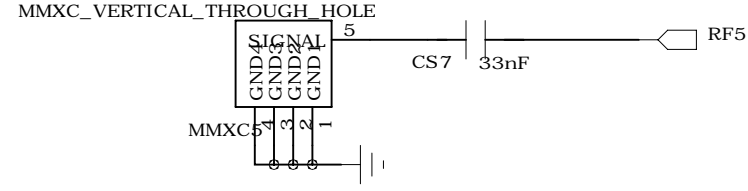
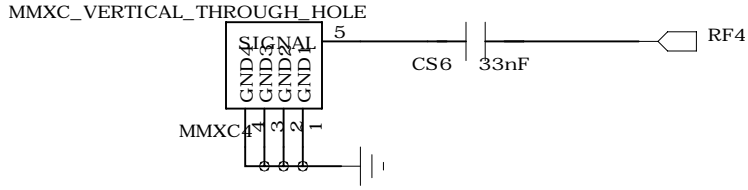
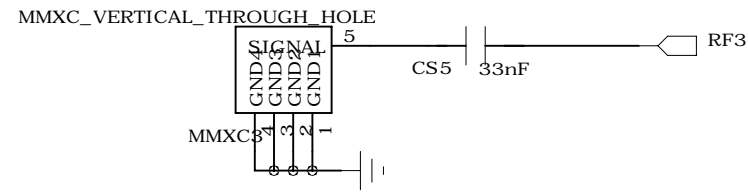
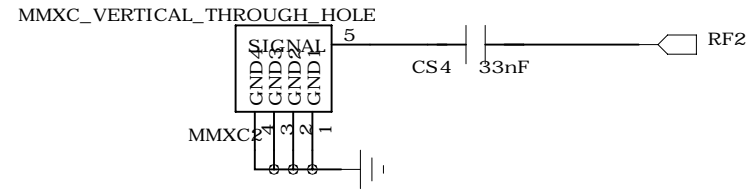
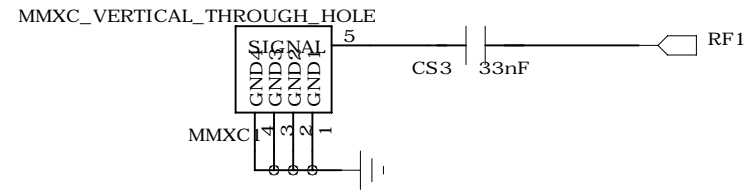
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A



institution:	University of Hawaii at Manoa High Energy Physics Lab Instrumentation Development Lab
title:	IRSX Reference Schematic
revision:	A
IDLAB design #:	IDL_14_011
circuit design:	JLB, GSV
PCB design:	JLB
sheet #:	2 of 3
sheet description:	RF Inputs
date last modified:	MAY 6, 2014

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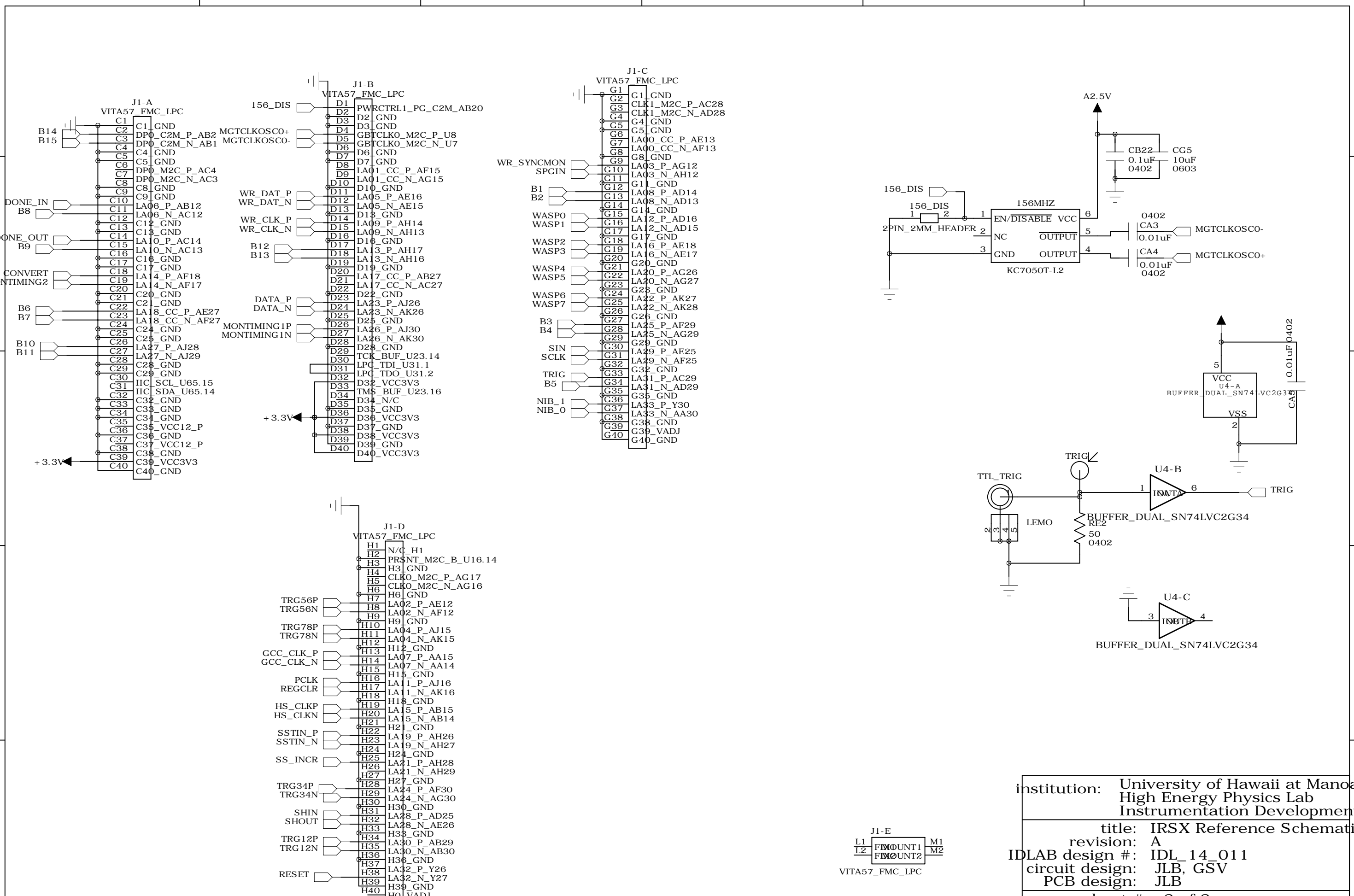
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A



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 High Energy Physics Lab
 Instrumentation Development Lab

title: IRSX Reference Schematic

revision: A

IDLAB design #: IDL_14_011

circuit design: JLB, GSV

PCB design: JLB

sheet #: 3 of 3

sheet description: FMC LPC Connector

date last modified: MAY 6, 2014