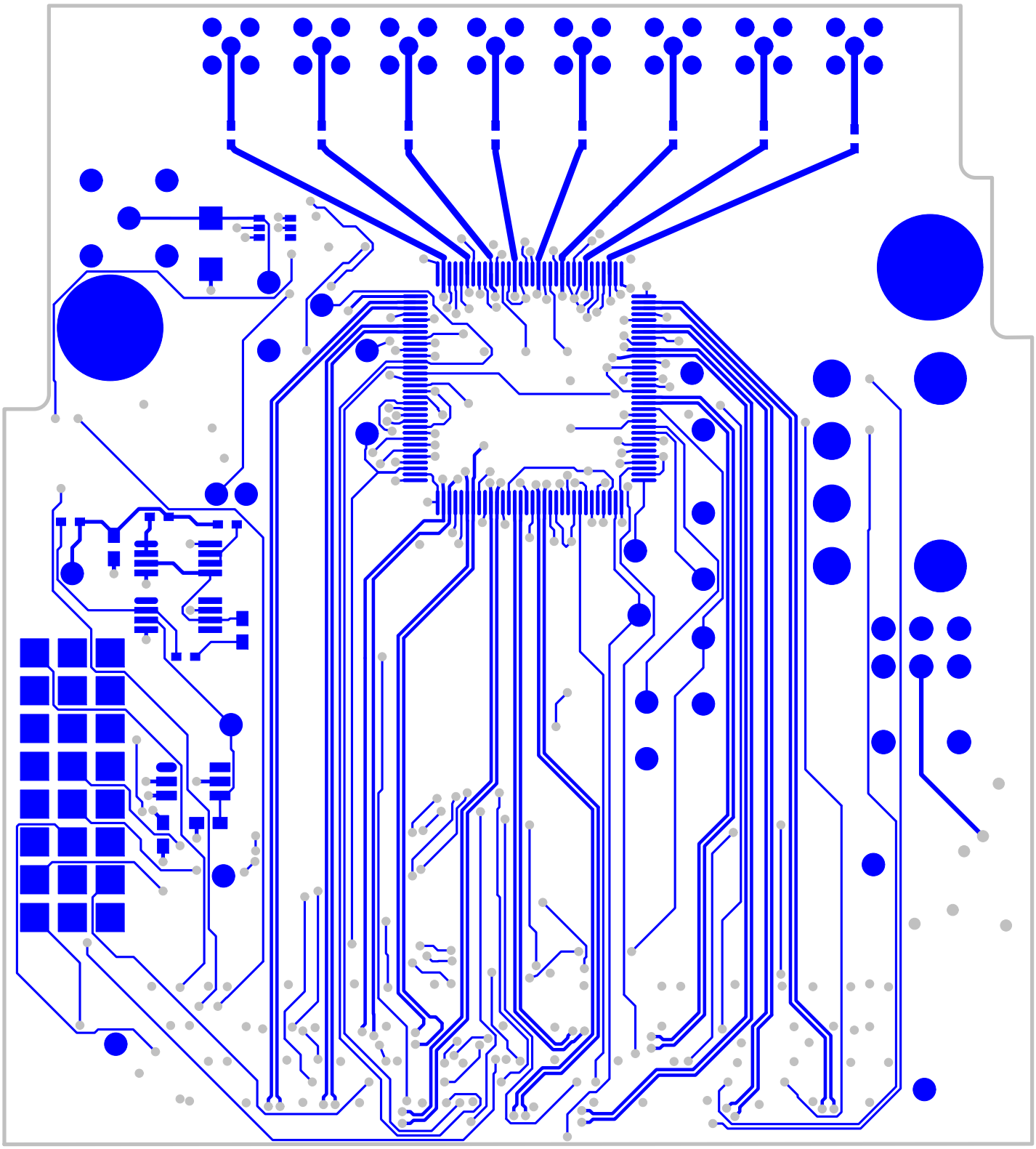

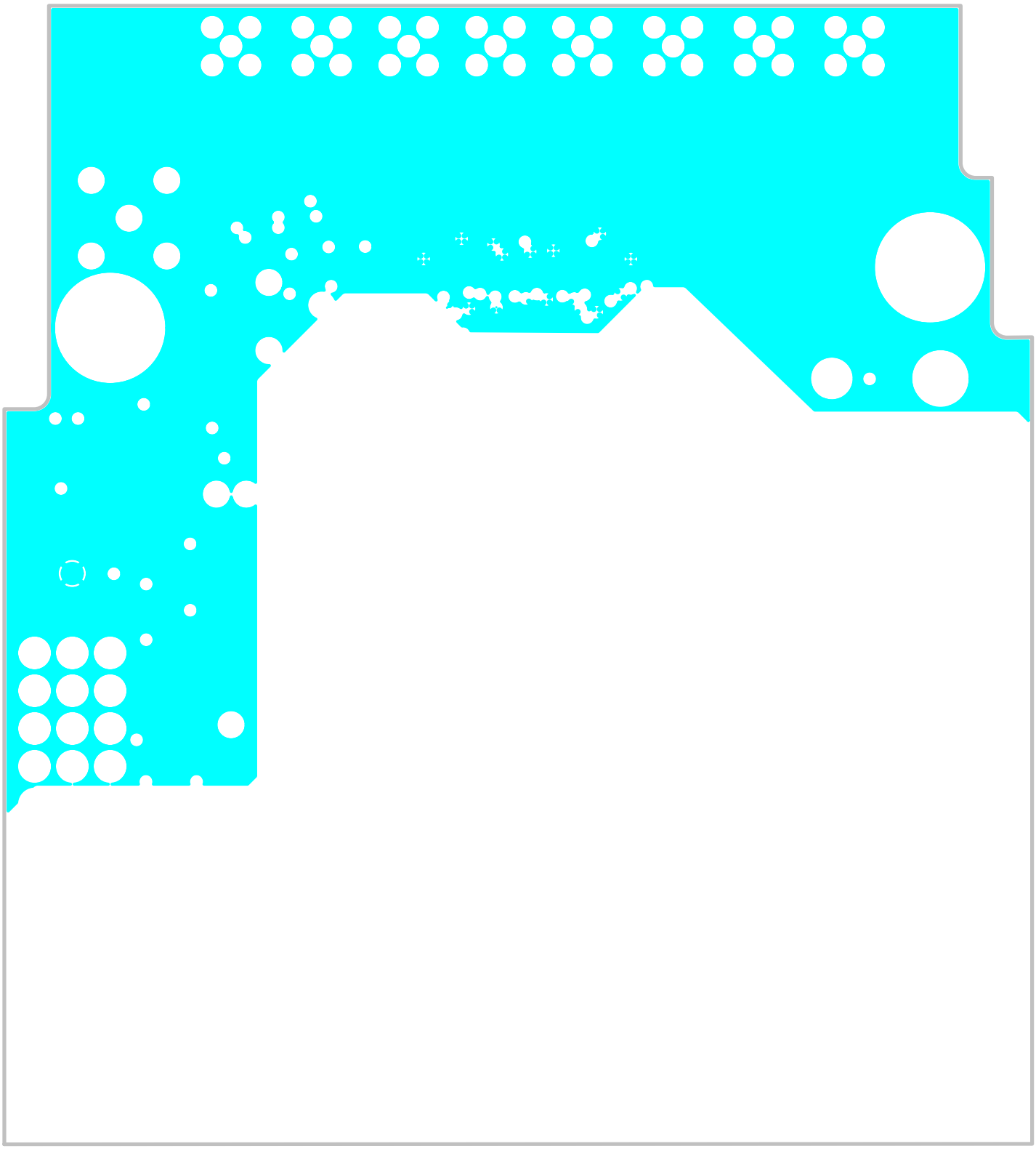

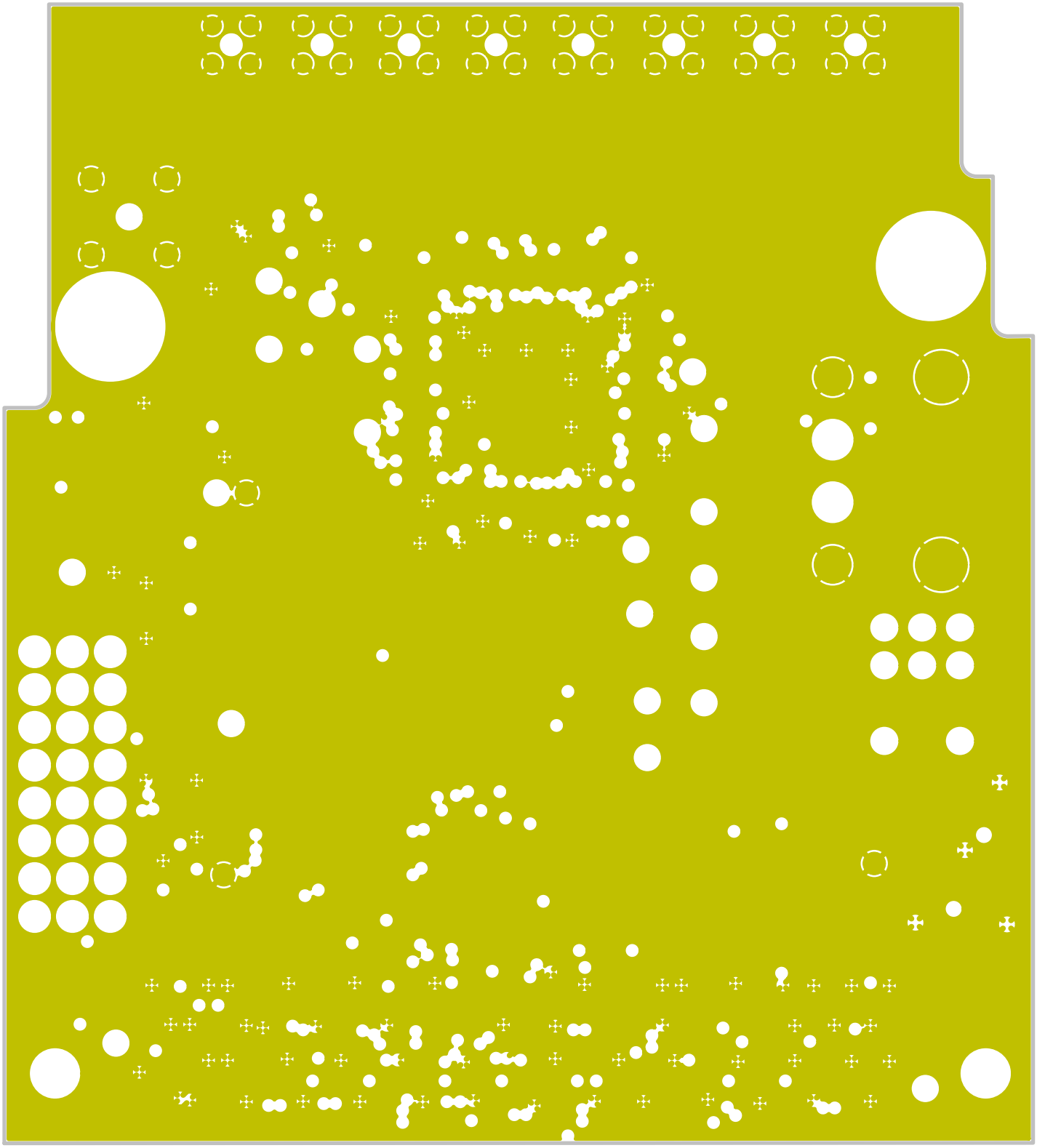

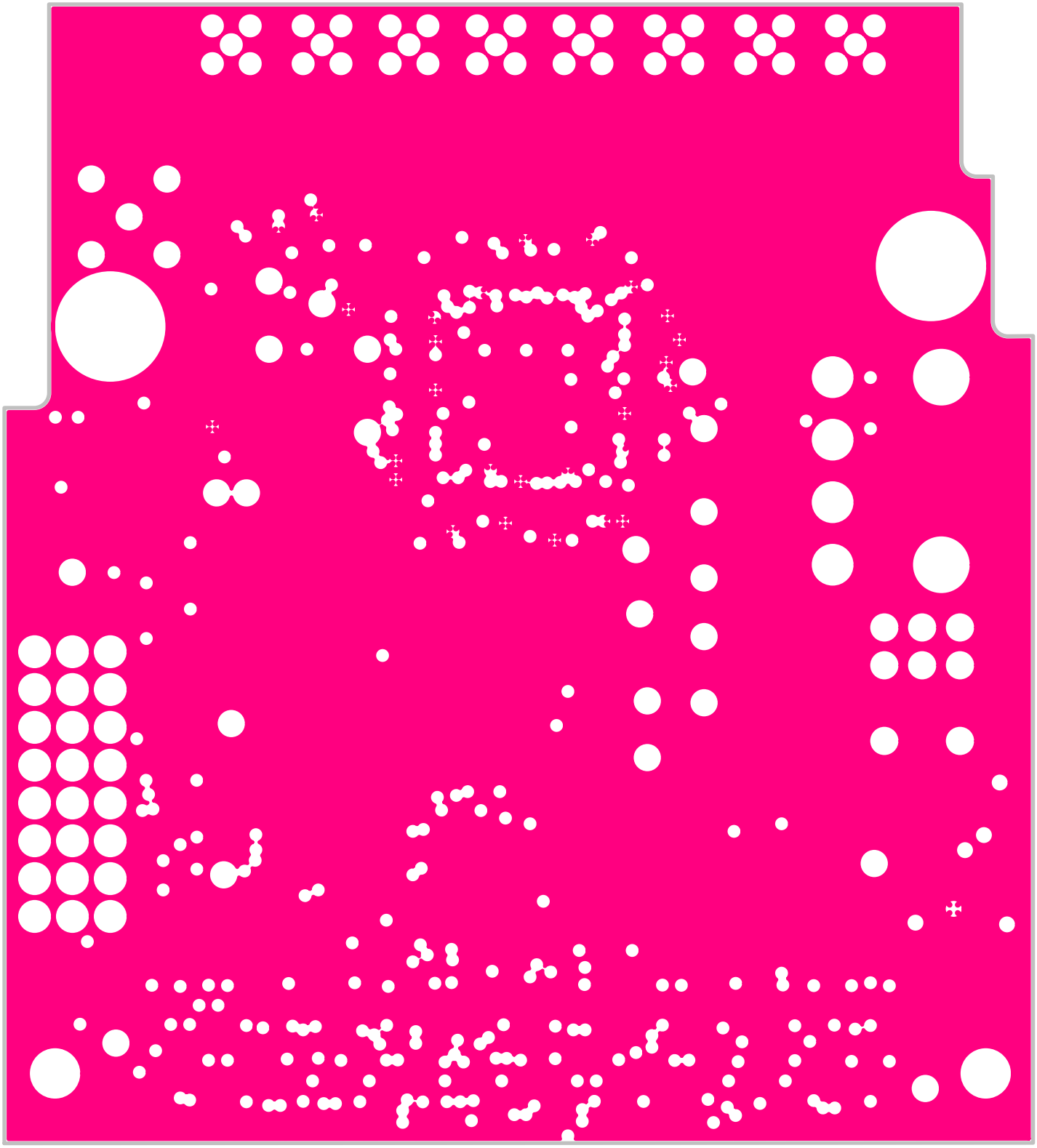

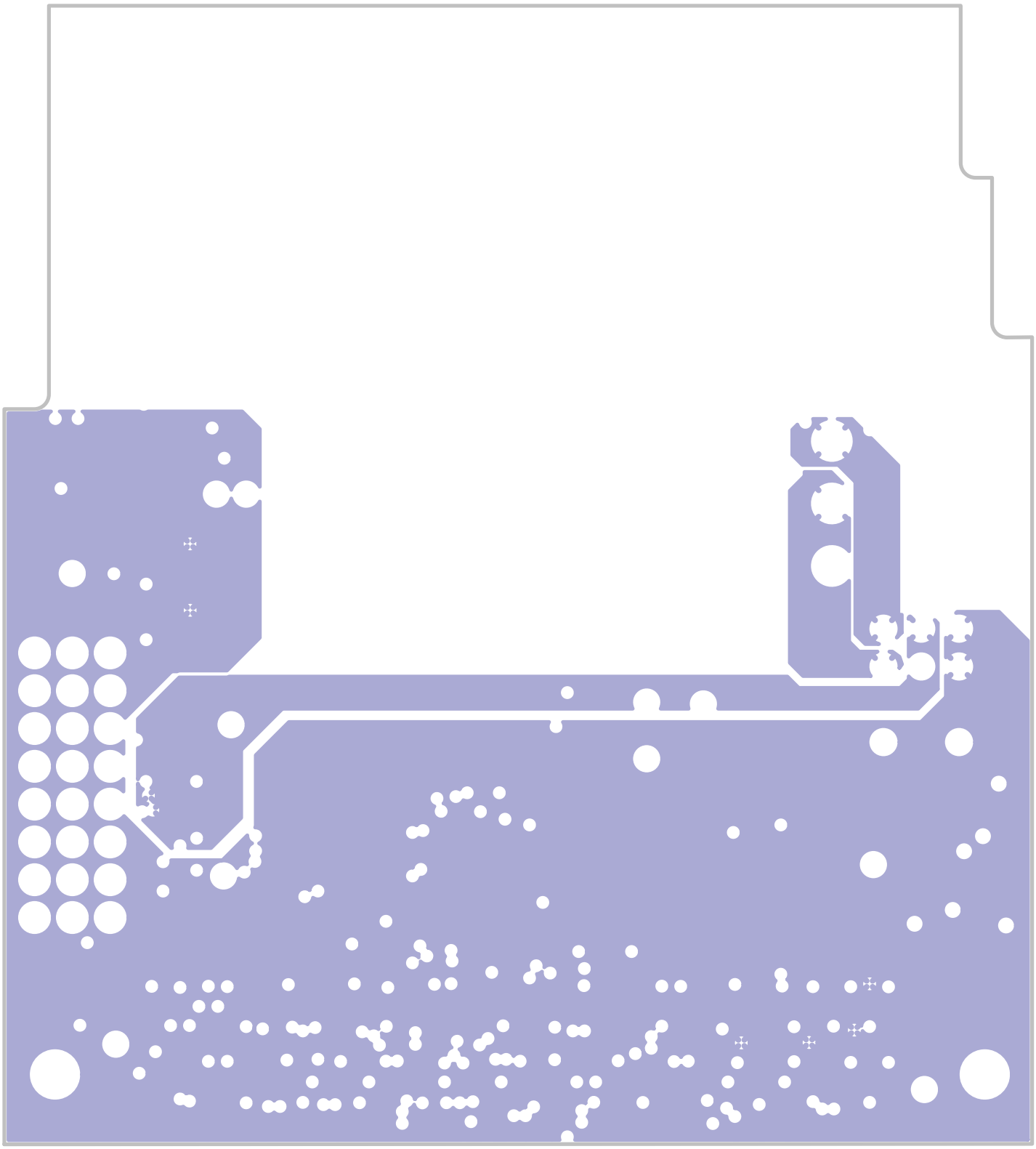

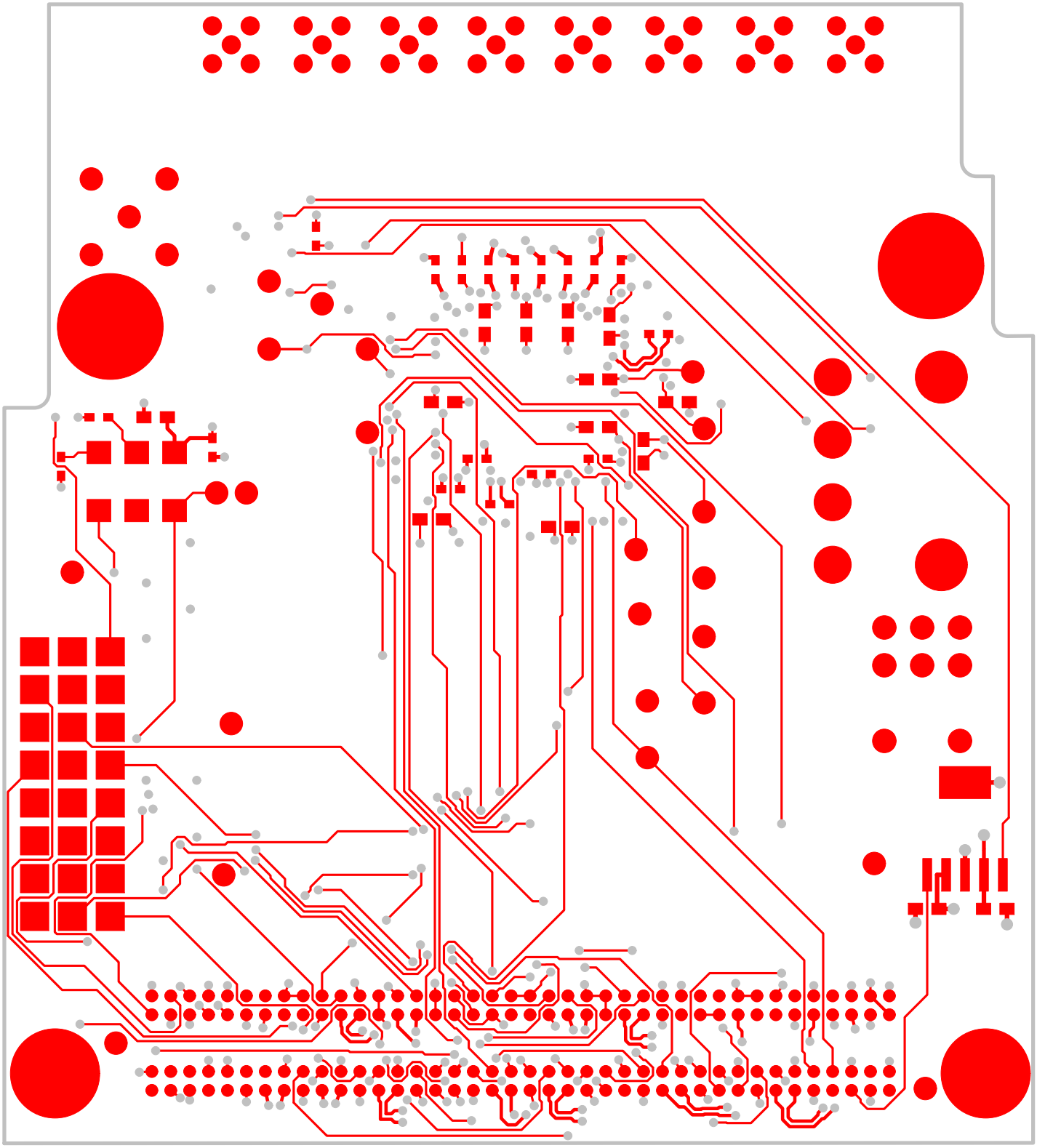

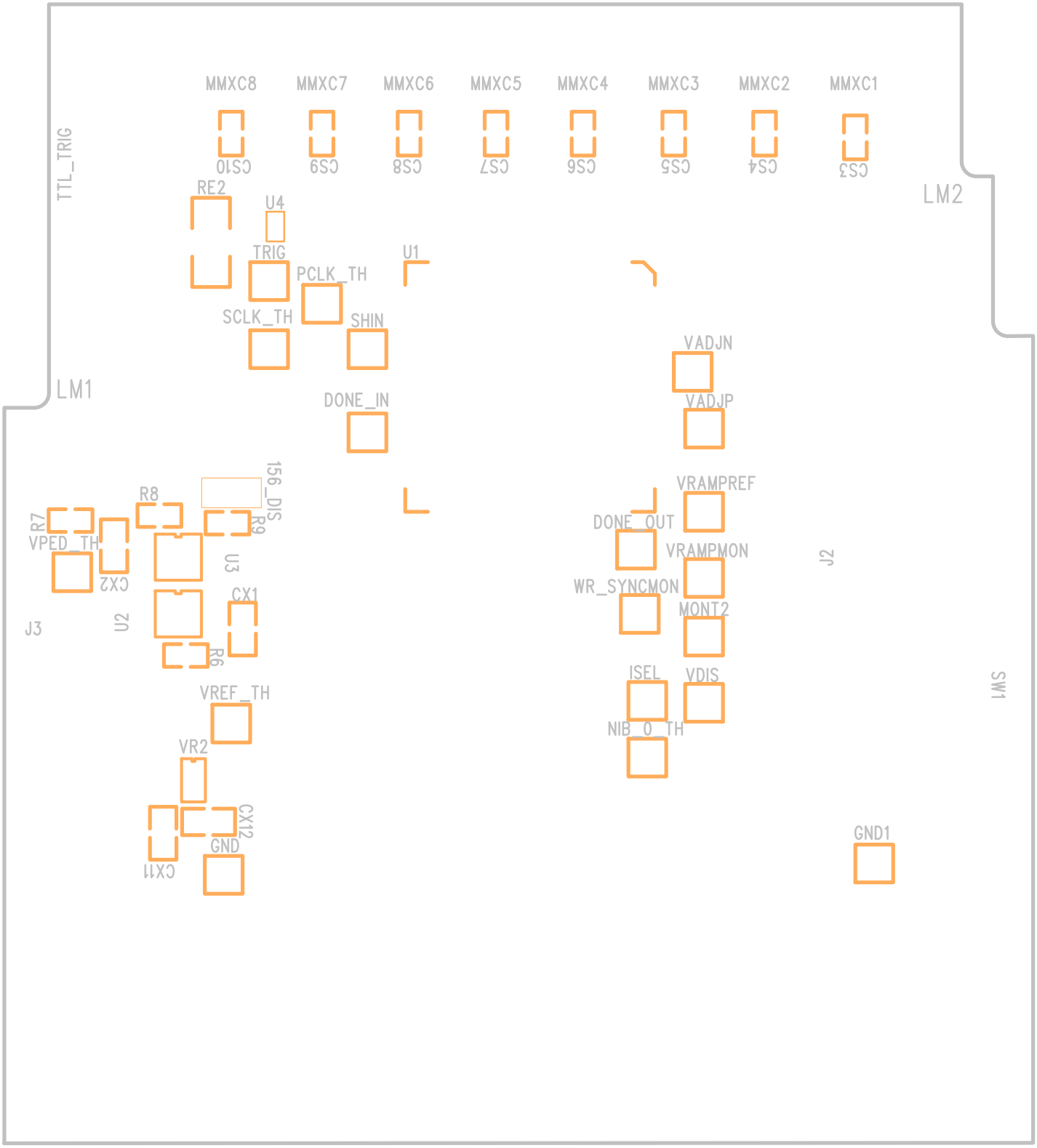
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CS8
CS7
CS6
CS5
CS4
CS3
TTL_TRIG
LM2
RE2
U4
TRIG
PCLK_TH
U1
SCLK_TH
SHIN
VADJN
LM1
DONE_IN
VADJP
156_DIS
R8
R9
R7
VPED_TH
CX2
U3
VRAMPREF
DONE_OUT
VRAMPMON
J2
WR_SYNCMON
CX1
MONT2
J3
U2
R6
ISEL
VDIS
SW1
VREF_TH
NIB_0_TH
VR2
CX12
GND
CX11
GND1

CA5
BTR8
BTR7
BTR6
BTR5
BTR4
BTR3
BTR2
BTR1
C16
R1
C10
C12
C14
C15
C22
C21
C20
CA3
CG5
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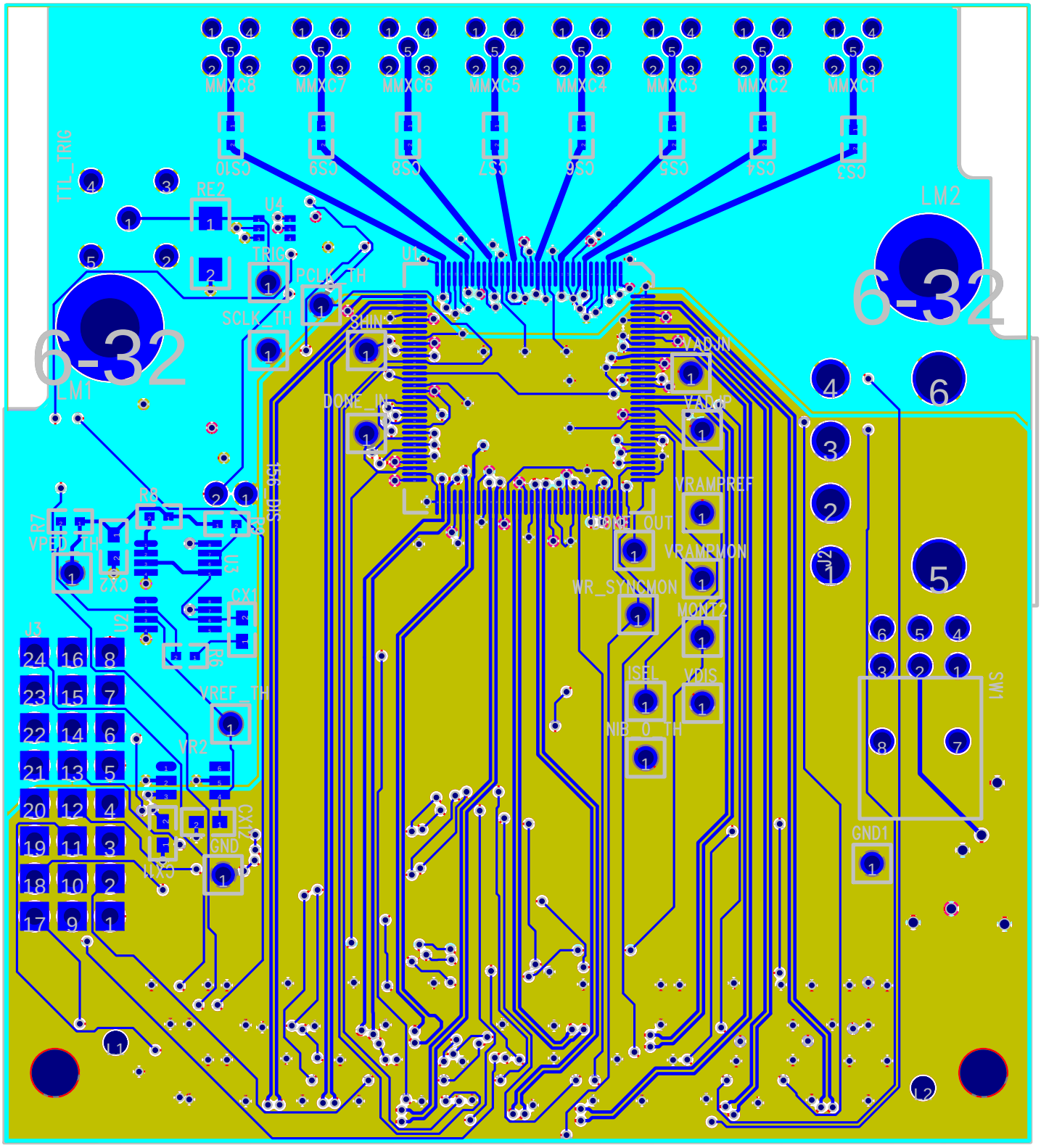
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CS3
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RE2
U4
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U1
LM2
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6-32
LM1
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U2
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CX6
SW1
GND1
L1
L2