

6

5

4

3

2

1

E

E

D

D

C

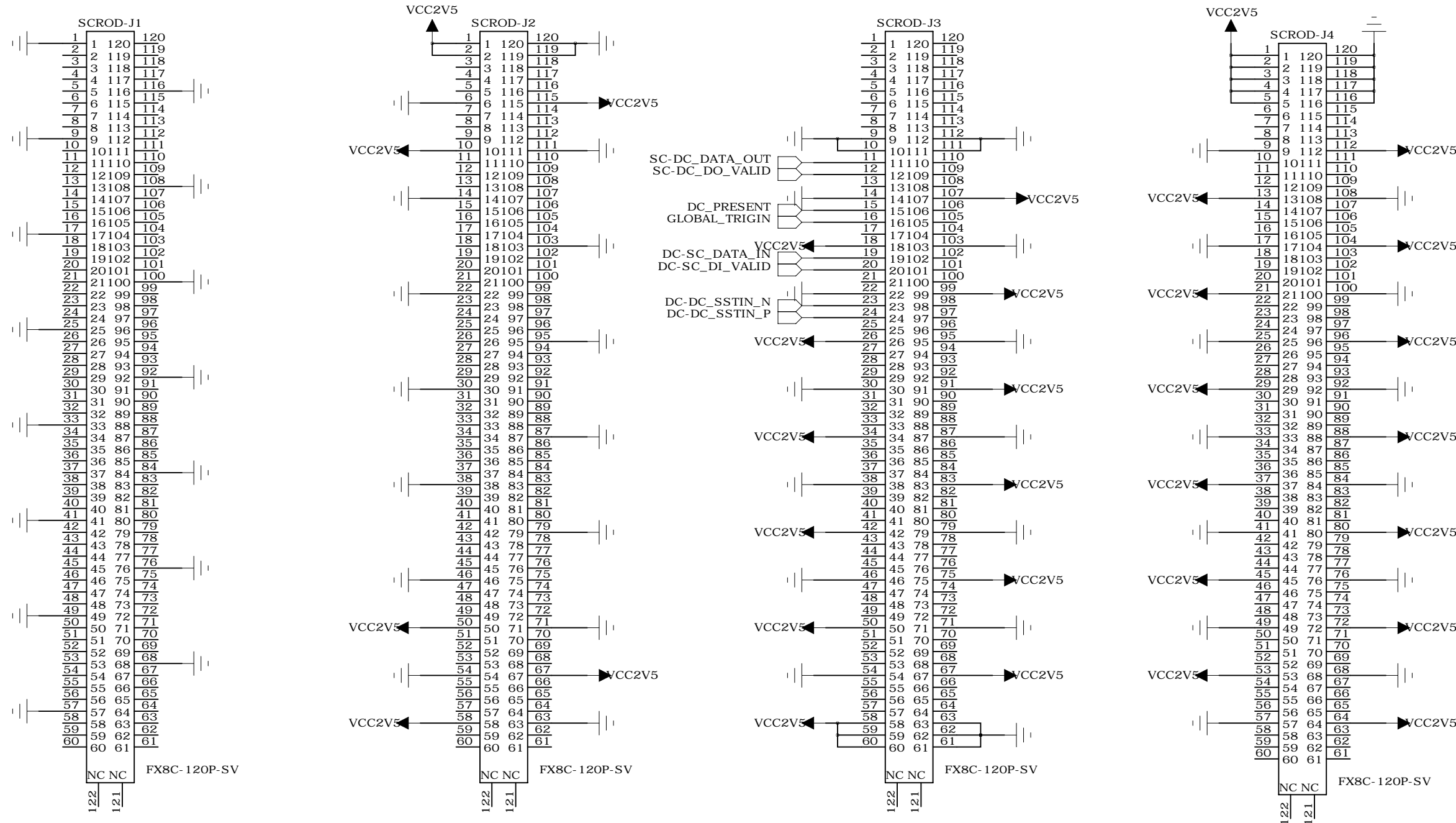
C

B

B

A

A



institution: University of Hawai'i at Manoa
 High Energy Physics Group
 Instrumentation Development Lab

title: BMD_Interface_Proto
 revision: A
 IDLAB design #: IDL_16_11
 circuit design: JLB
 PCB design: JLB

sheet #: 1 of 3
 sheet description: SCROD_Connector
 date last modified: 2016-08-31

6

5

4

3

2

1

E

E

D

D

C

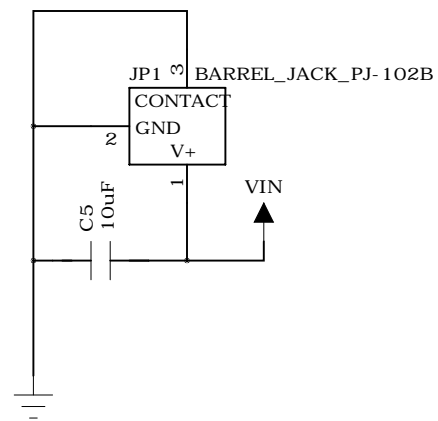
C

B

B

A

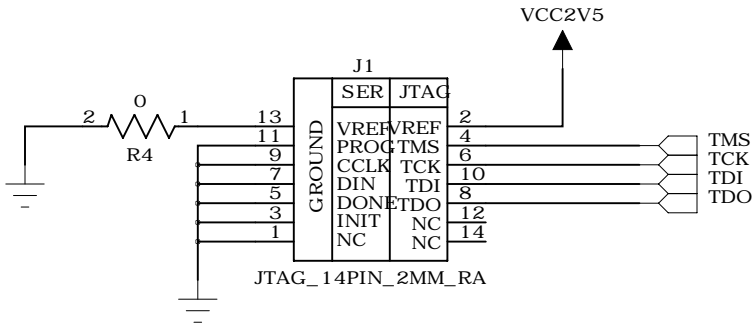
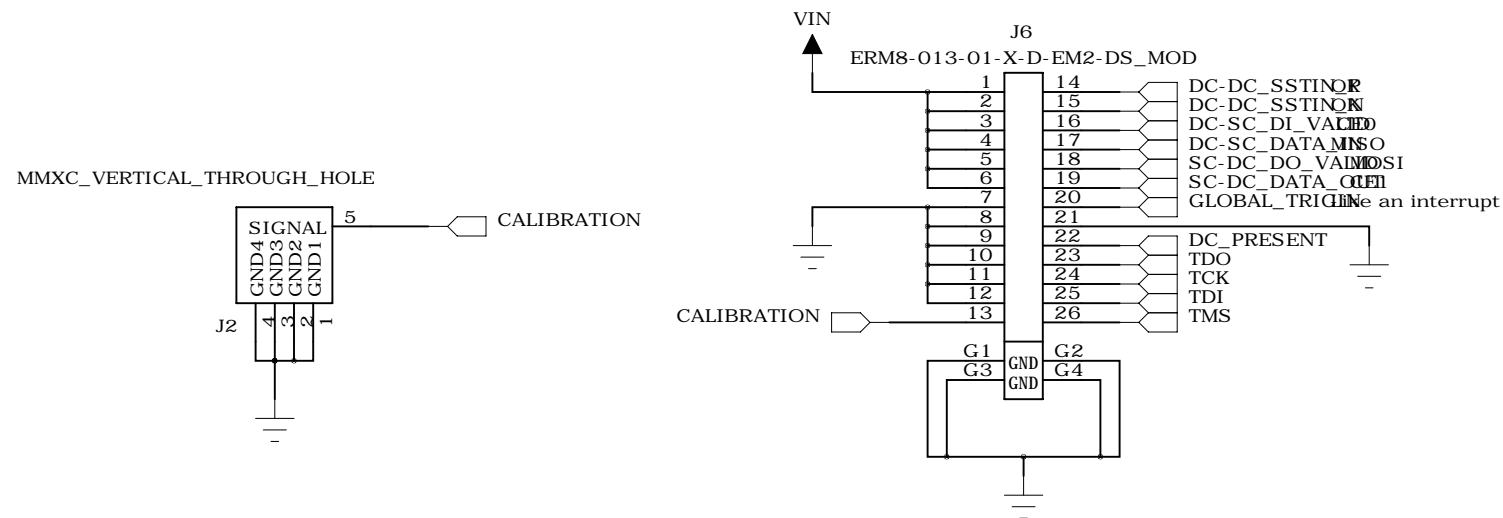
A



institution:	University of Hawai'i at Manoa High Energy Physics Group Instrumentation Development Lab
title:	BMD_Interface_Proto
revision:	A
IDLAB design #:	IDL_16_11
circuit design:	JLB
PCB design:	JLB
sheet #:	2 of 3
sheet description:	Power_Page
date last modified:	2016-08-31

Have test points for HV in (using external supply)
 Need MMCX & BNC for calibration
 From Spartan 6 to TX 2V5
 From Spartan 6 to SCROD 2V5
 From SCROD to Spartan 6 2V5

Cross reference with PCB of DC board (different footprint)



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title:	BMD_Interface_Proto
revision:	A
IDLAB design #:	IDL_16_11
circuit design:	JLB
PCB design:	JLB
sheet #:	3 of 3
sheet description:	Logic_Interconnect
date last modified:	2016-08-31