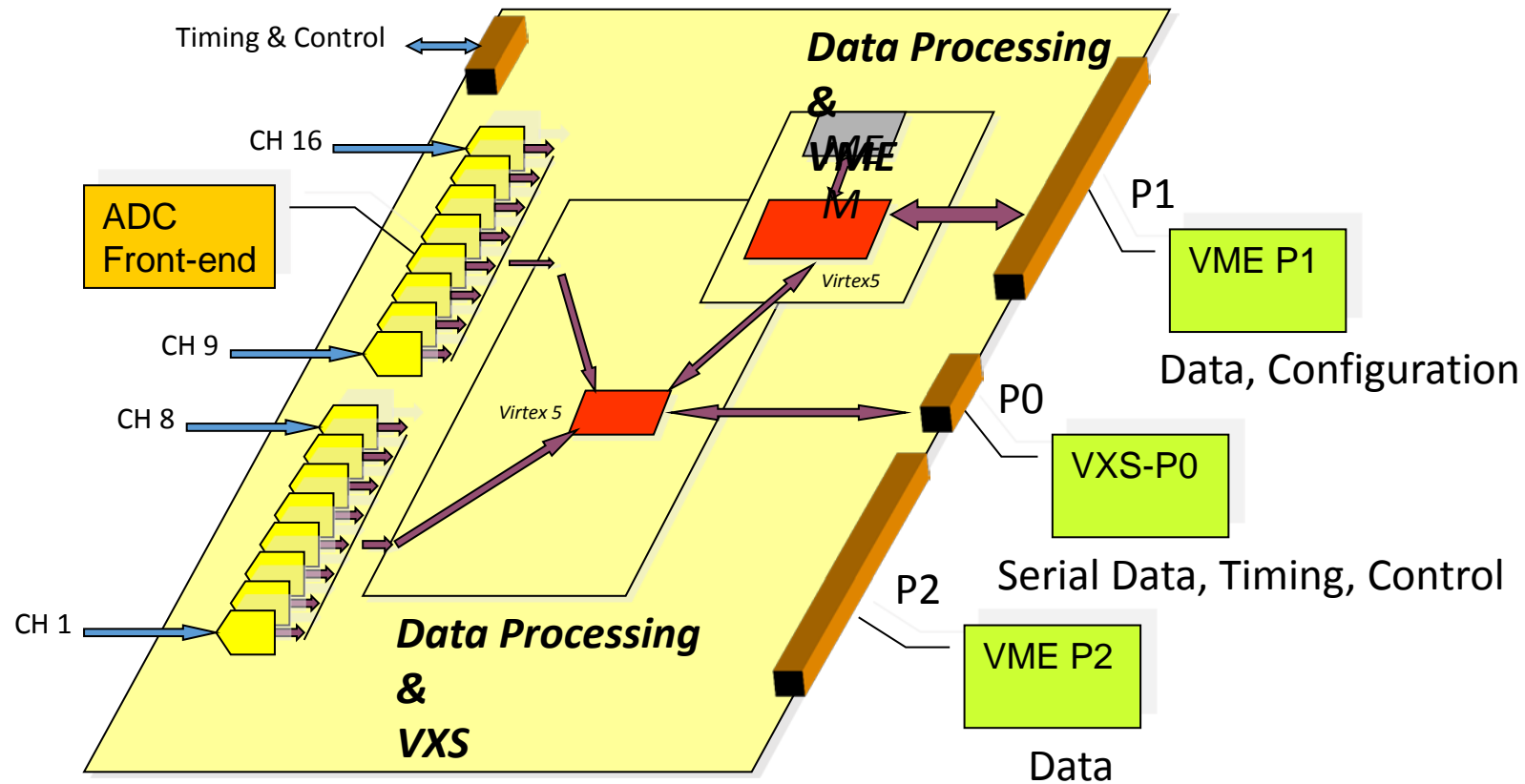


fADC250V2 – 250 MSPS Pipelined flash ADC



- Fewer Parts
- Lower Power (56 W)
- SPI for config/read-back
- Test Vector Generation
- Newer Gen FPGAs.

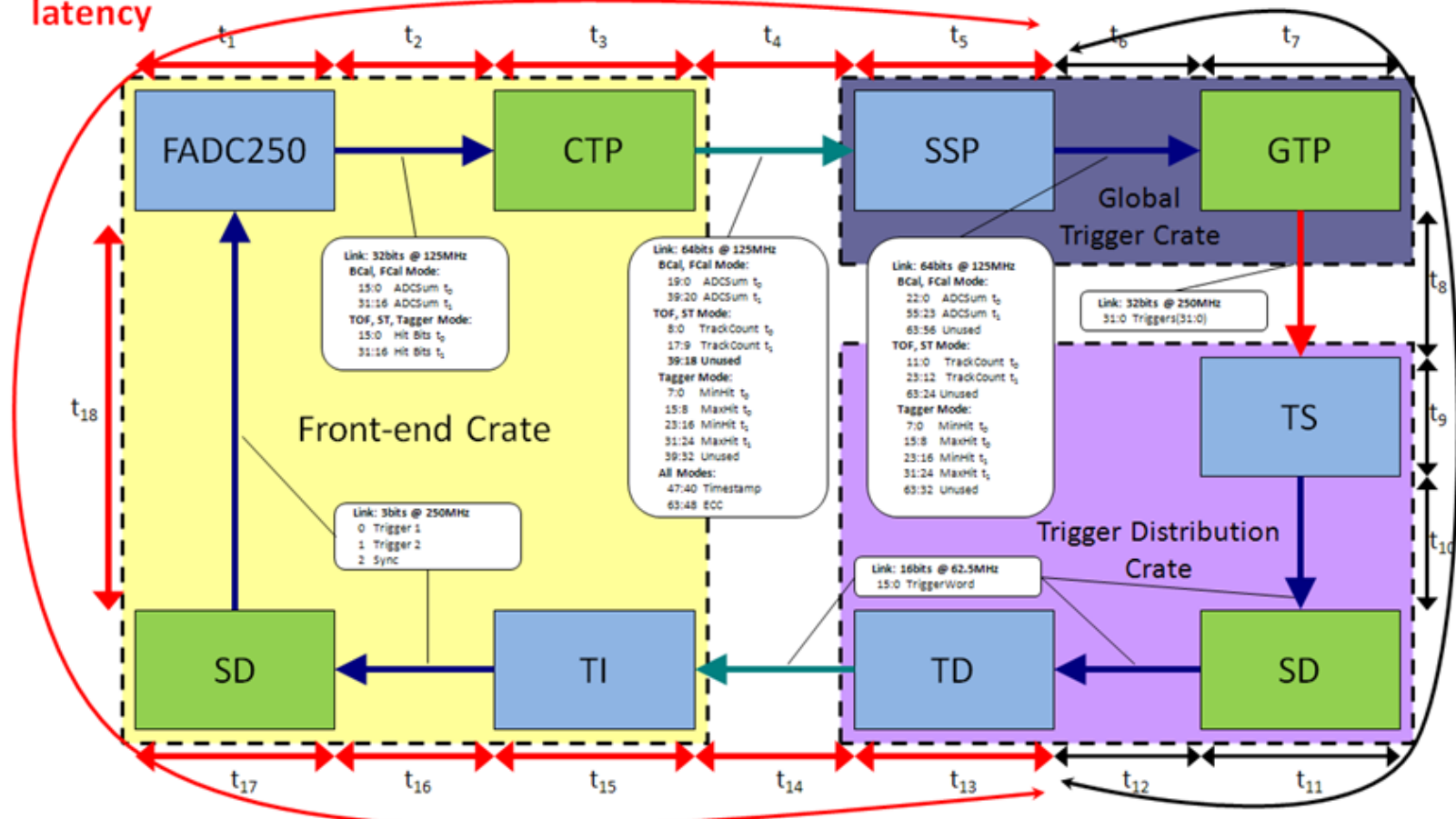
- Tests - 3rd Qtr 2010.
- Engineering Run – 1st Qtr 2011.
- Production – 1st Qtr 2012
(300 units for Hall D).

27 April 2010

GlueX Level 1 Timing

2.3 μ s measured latency

660ns estimated latency remaining



2.3 μ s (measured) + 660ns (estimated) < 3 μ s!

