	Oct 15 to 21	Oct 22 to 29	Oct 29 to Nov 4	Nov E to Nov 11	Nov 12 to Nov 18	Nov 19 to Nov 25	Nov 26 to Dec 2	Dec 3 to Dec 9	Dog 10 to Dog 16	Dec 17 to Dec 23	Dec 24 to Dec 30	Dec 31 to Jan 6
	Oct 15 to 21	Oct 22 to 28 PREAMP TEST SYSTEM DONE	OCI 29 IO NOV 4	SCIFI READOUT		100 19 to 100 25	NOV 20 to Dec 2	Dec 3 to Dec 9	Dec 10 to Dec 16 SCIFI SYSTEM DONE,	Dec 17 to Dec 23	Dec 24 to Dec 30	
Other Event		SYSTEM DONE			B2GM week				TESTED, SHIPPED			KEK CLOSED
	D			BASIC TEST DONE								
	Preamplifier Test System	T										
	MPPC/LED interface			Construction End Nov 53								
	Preamp/Test interface			Eleleb ber New 70								
	Digitized sample readou	t		Finish by Nov. 7?								
	Define test protocol Readout software											
		(DONE)										
		GUI for readout										
		GOI IOI TEAGOUT										
	SciFi (using TARGET4)											
	Basic motherboard test											
			Daughter card									
			population									
			Ordering parts	Accepta								
				Assembly								
					Tastina							
					Testing Motherboard populated							
					inotherboard populated							
	Firmware development											
	SCROD version of											
	firmware (X) Merge command											
	interpreter (X)											
			Trigger bit testing									
				Trigger bit readout								
			Implement data format	control (X)								
			Implement data format	Simple format for iTOP								
				Simple format for iTOP tracking								
				Multi-ASIC readout (X)								
				Continuous sampling								
					Feature extraction							
						Optimize firmware						
			5									
			Data Acquisition Basic USB readout									
				Transition to PCI/?								
				Transition to PCI/?								
			Slow Control									
			Joiow Control									
			Calibration and offline	Wilkinson comparators,								
			analysis Define calibration	overvoltages								
			method									
				Test calibration								
				Implement offline analysis framerwork								
			İ	2aryolo namorwork								
		Hardware Development	(Mostly done already)									
		Frontend: Preamp-										
		TARGET interface, termination circuit etc	1									
		MPPC + Bar Interface										
		Power distribution										
			MPPC testing									
					Install MPPCs							
				Install readout elec.								
						System Test at UH						
						Assembly						
								Run full system				
									Packup			
				(VERY preliminary sched								
			Daughter card fabrication		Cubmit	Assamble						
					Submit	Assemble				-		
			1	Firmware development		Toot cotup						
		1	1		I	Test setup			1	I.		