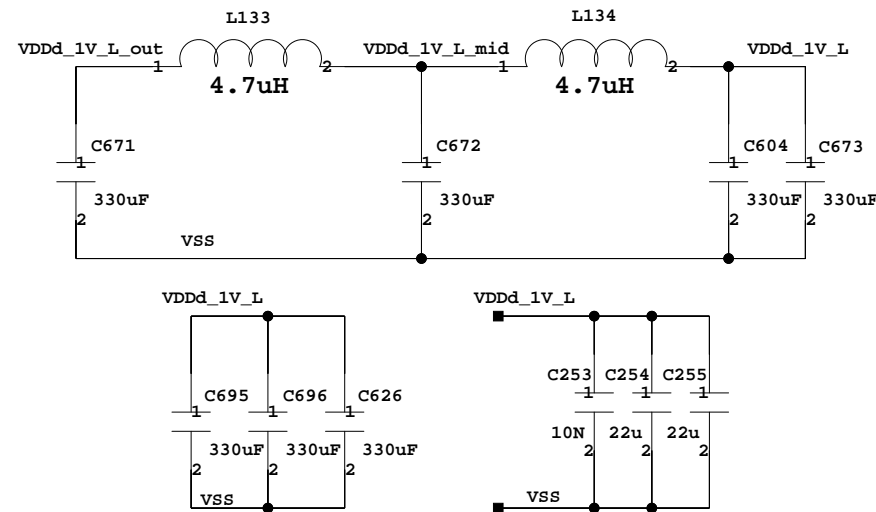
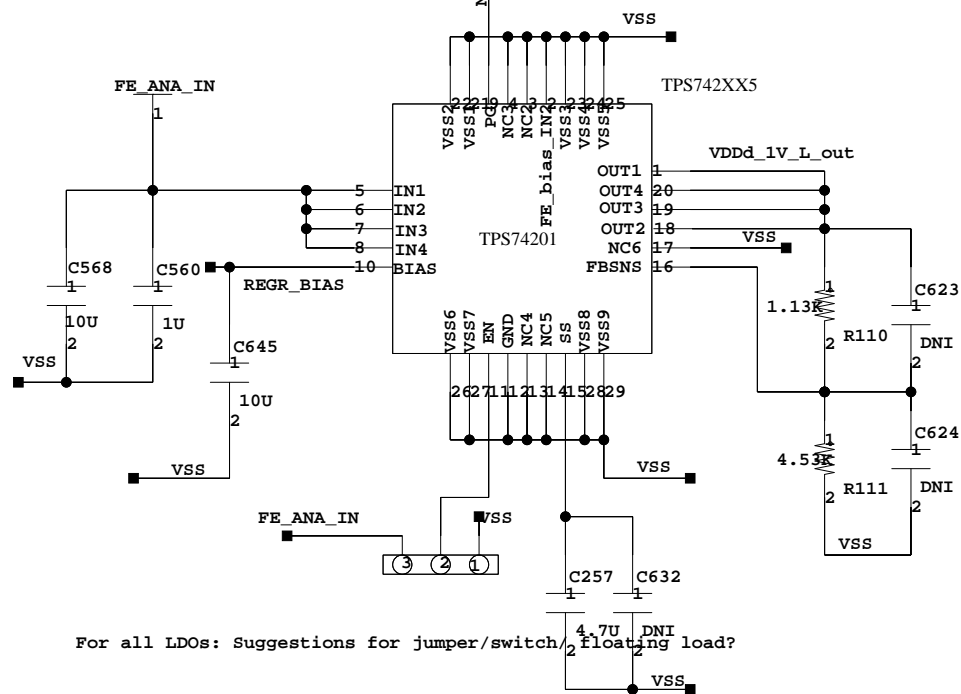
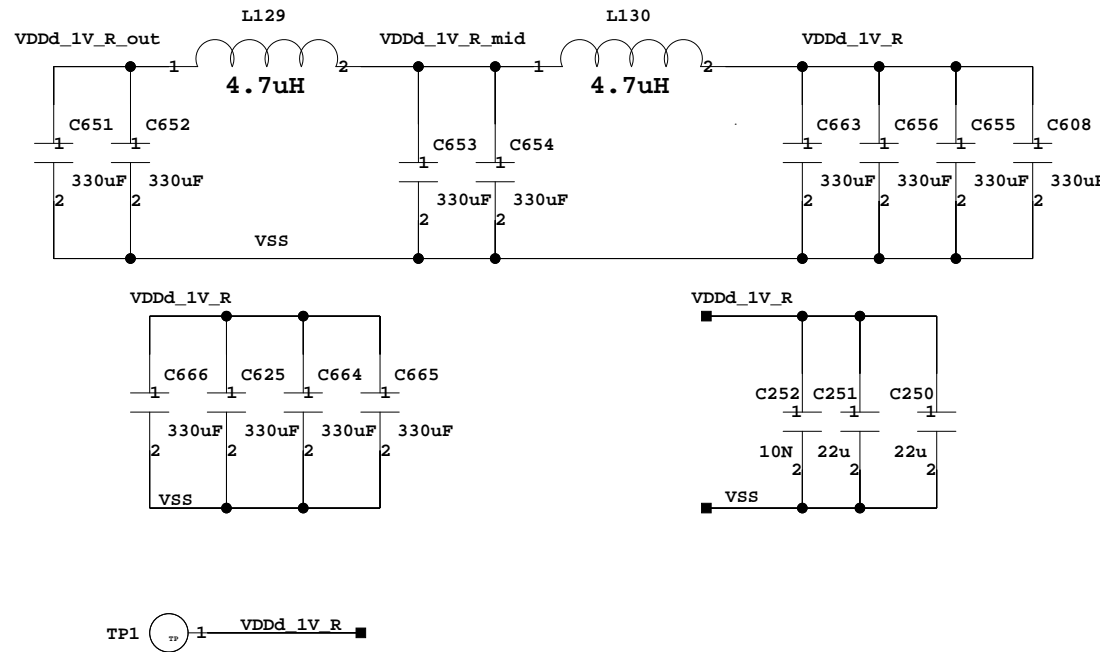
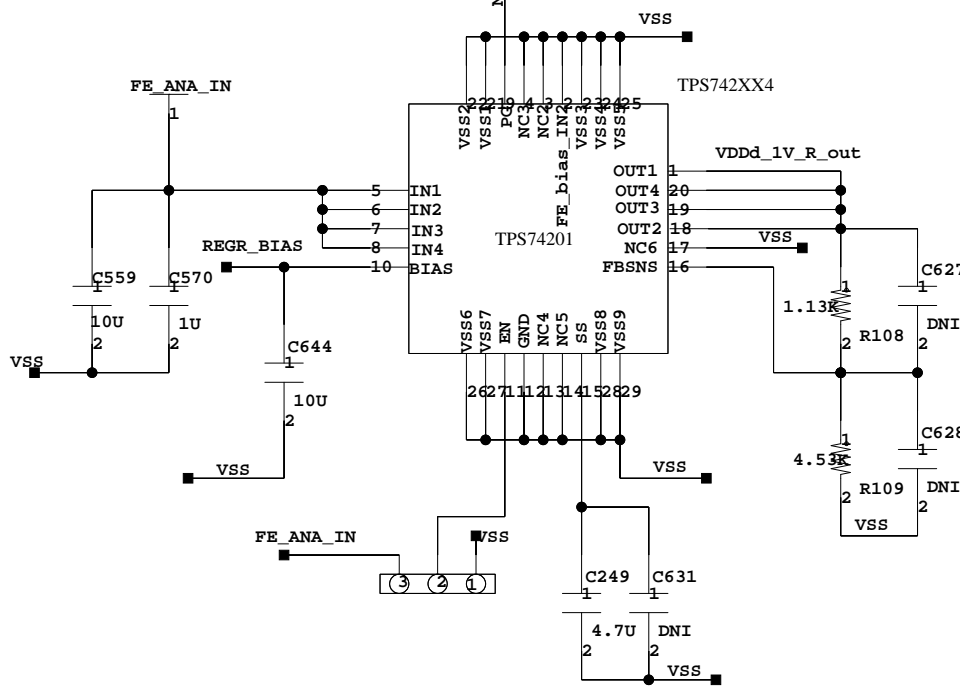


Left Digital 1V

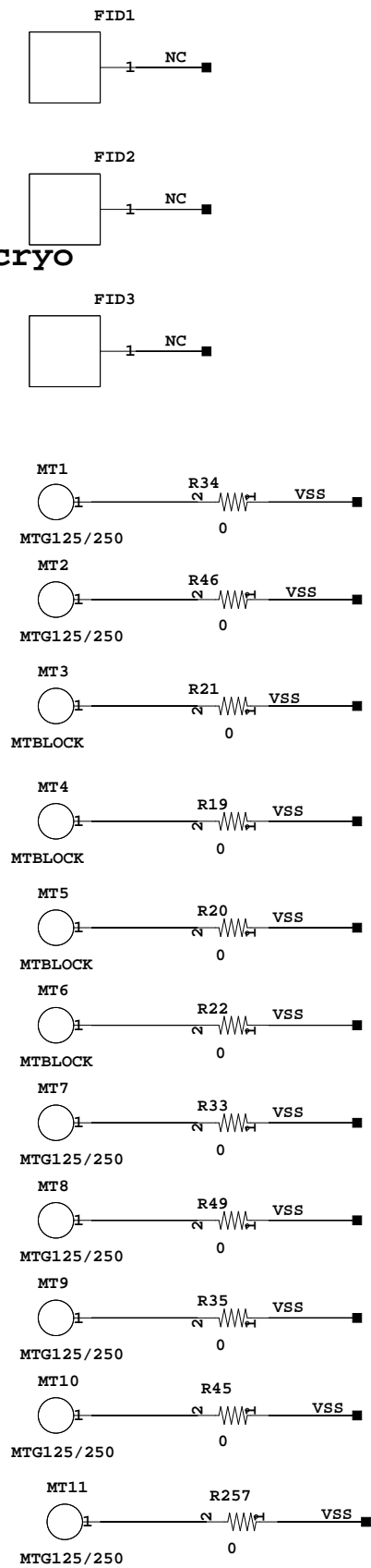


Right Digital 1V



sense lines. Keep resistors close to asic. in between both

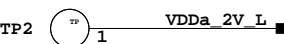
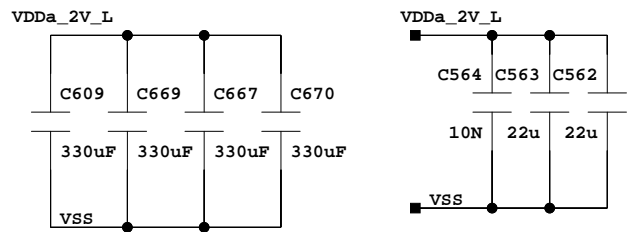
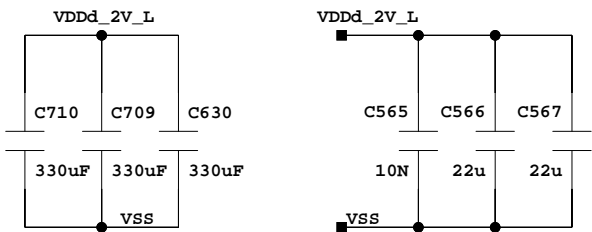
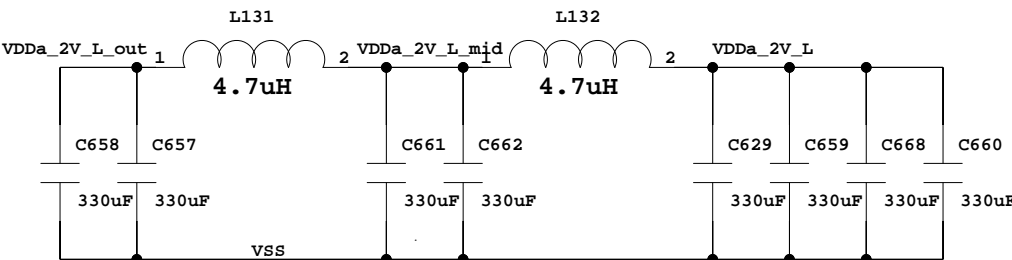
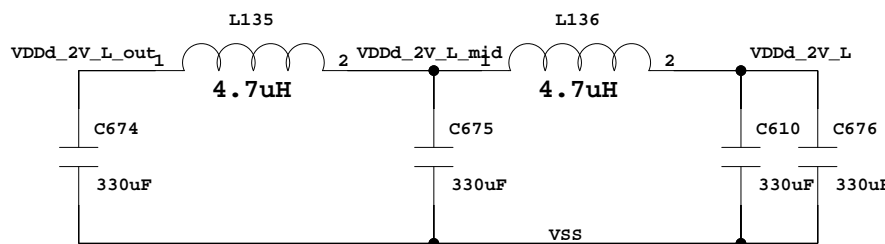
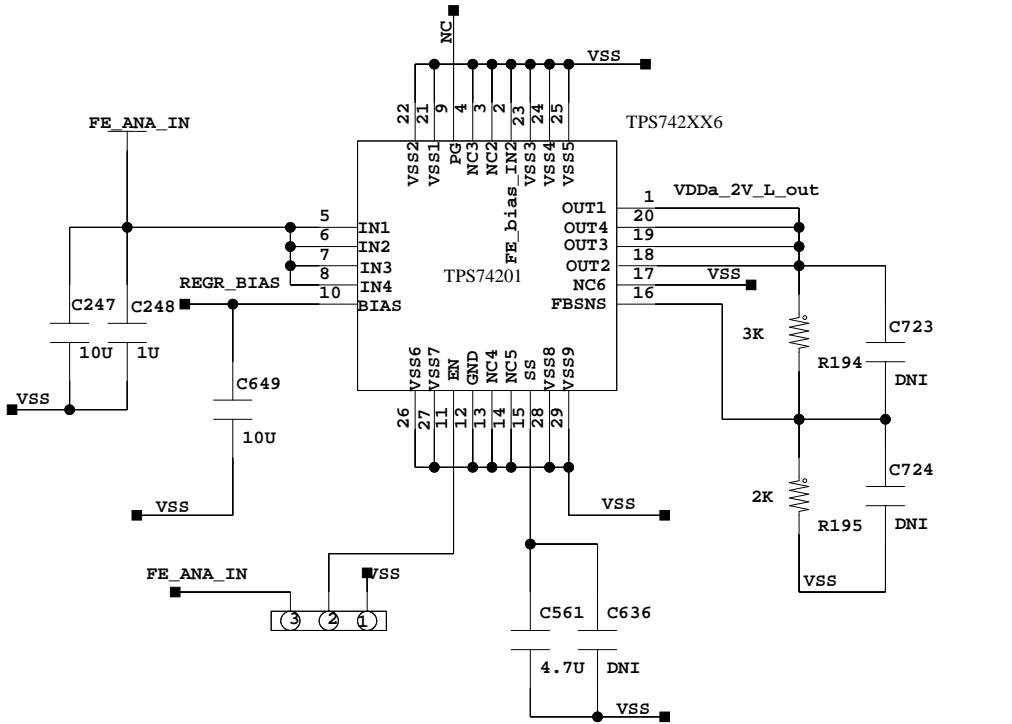
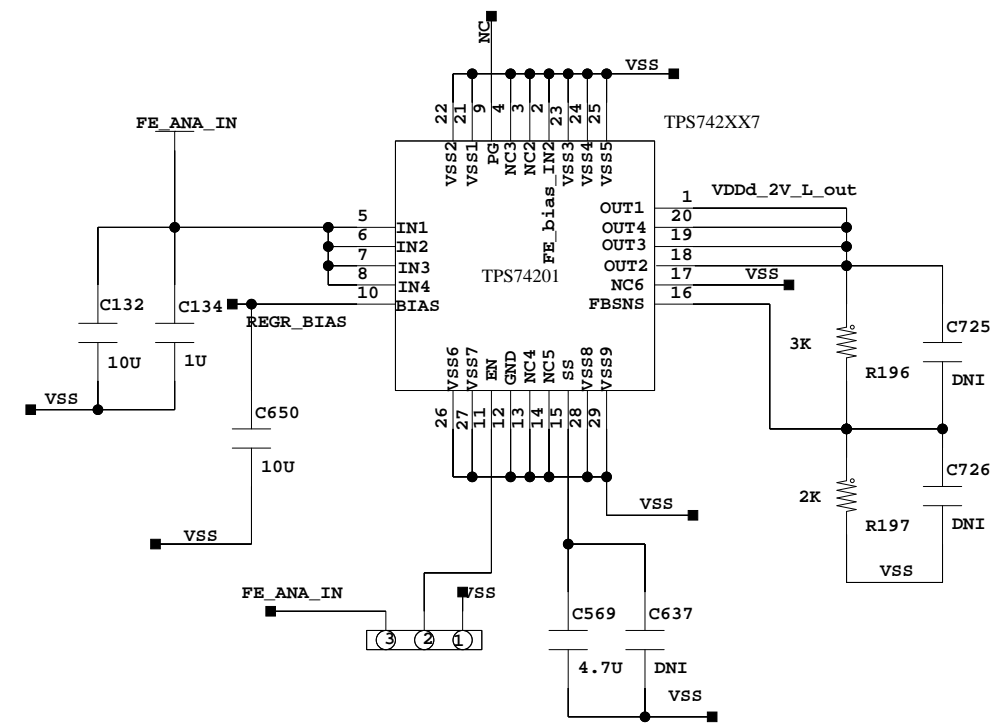
Ground? but keep close to cryo



TITLE				
DUNE FRONT END MOTHER BOARD				
TEAM				
UNIVERSITY OF HAWAII MANOA INSTRUMENT DEVELOPMENT LAB				
SIZE	REV	SHEET	2	OF 13
DRAWN	JEFF KLEYNER	DATE	08/10/2019:17:32	

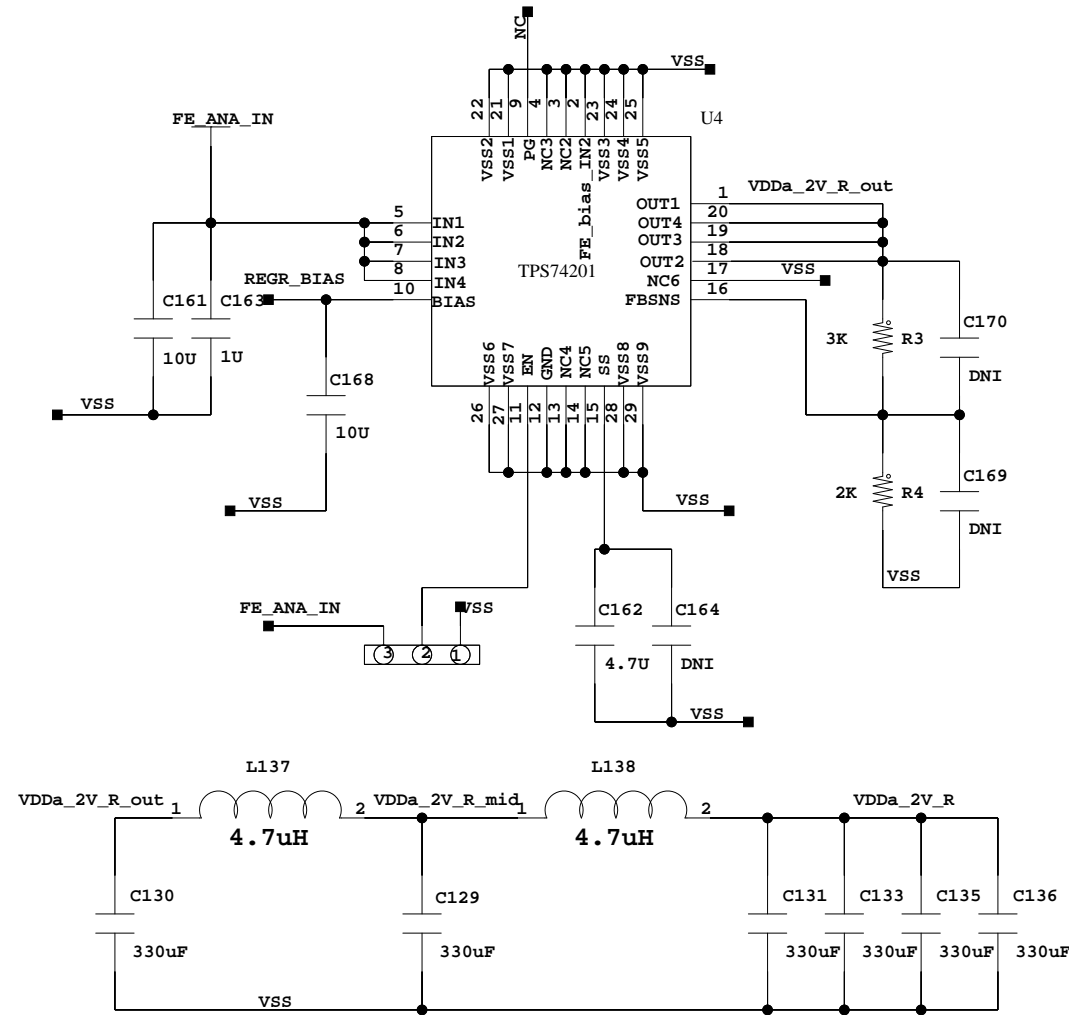
Left Digital 2V

Left Analog 2V

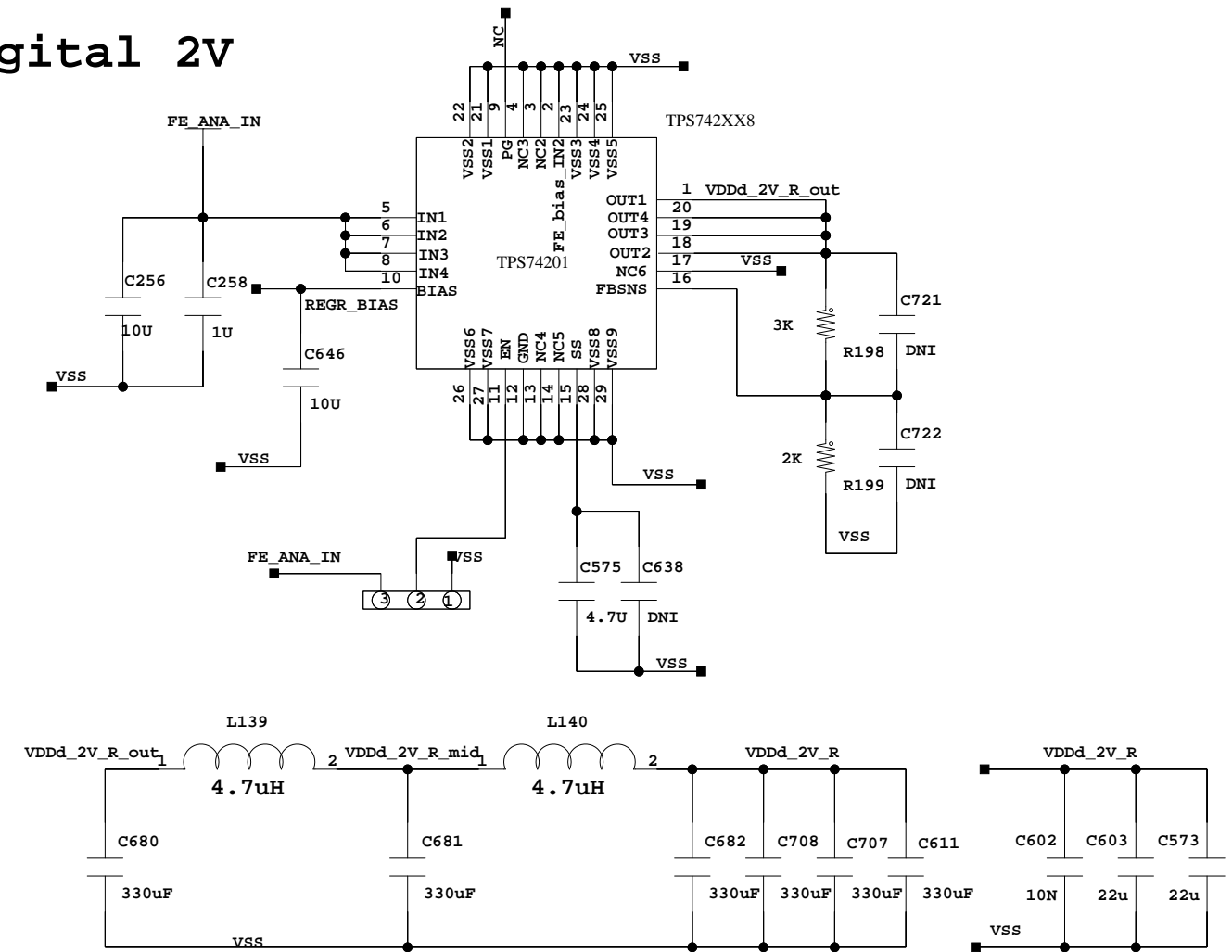


TITLE				
DUNE FRONT END MOTHER BOARD				
TEAM				
UNIVERSITY OF HAWAII MANOA INSTRUMENT DEVELOPMENT LAB				
SIZE	REV	SHEET	3	OF 13
DRAWN	JEFF KLEYNER	DATE	08/10/2019:17:33	

Right Analog 2V

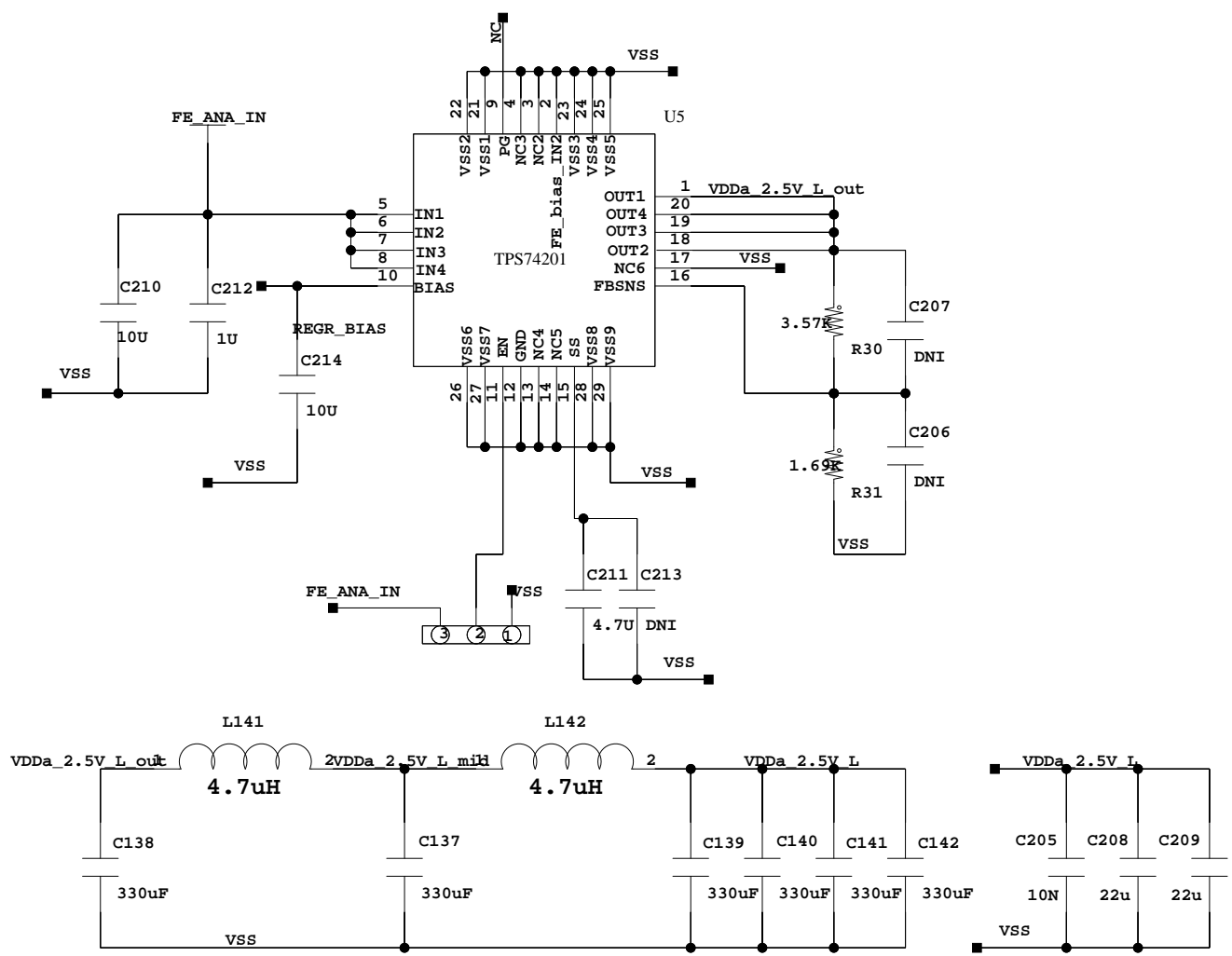


Right Digital 2V

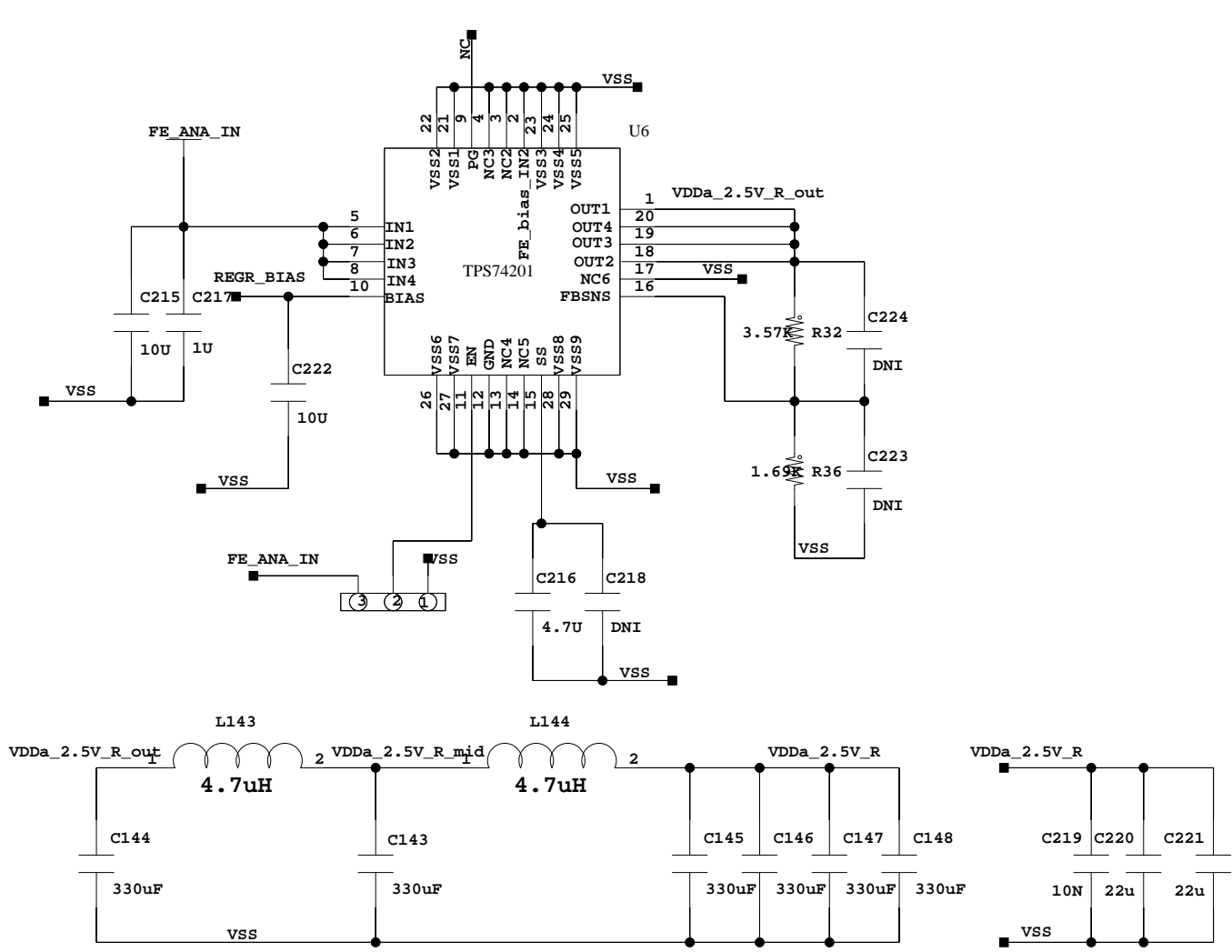


TITLE				
DUNE FRONT END MOTHER BOARD				
TEAM				
UNIVERSITY OF HAWAII MANOA INSTRUMENT DEVELOPMENT LAB				
SIZE	REV	SHEET	4	OF 13
DRAWN	JEFF KLEYNER	DATE	08/10/2019:17:34	

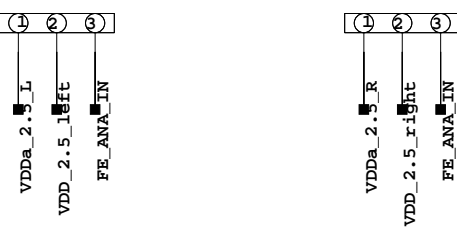
Left Analog 2.5V



Right Analog 2.5V



Select raw power in or ldo 2.5 out



TITLE				
DUNE FRONT END MOTHER BOARD				
TEAM				
UNIVERSITY OF HAWAII MANOA INSTRUMENT DEVELOPMENT LAB				
SIZE	REV	SHEET	5	OF 13
DRAWN	JEFF KLEYNER	DATE	08/10/2019:17:34	

4



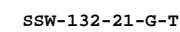
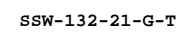
2

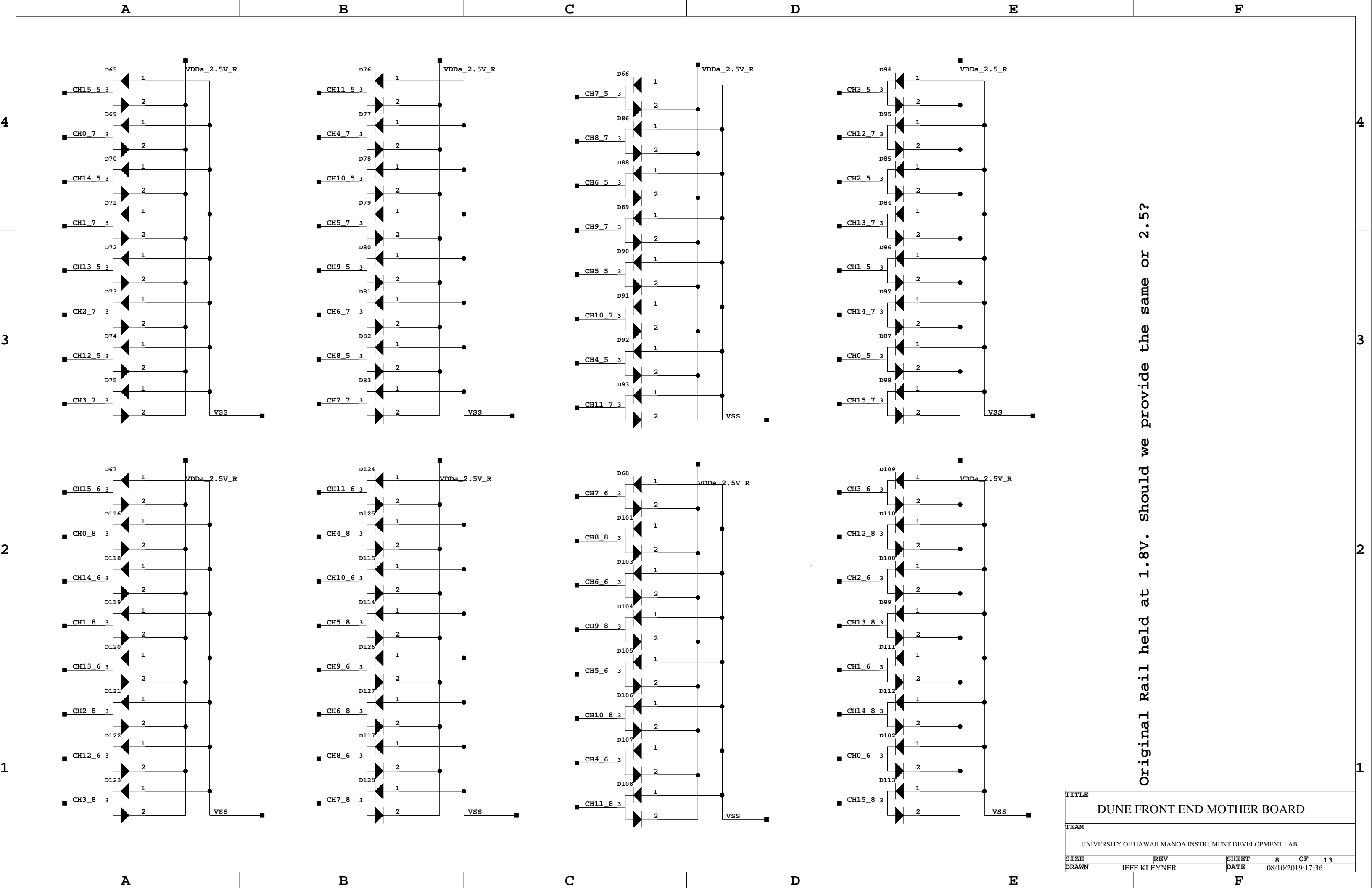


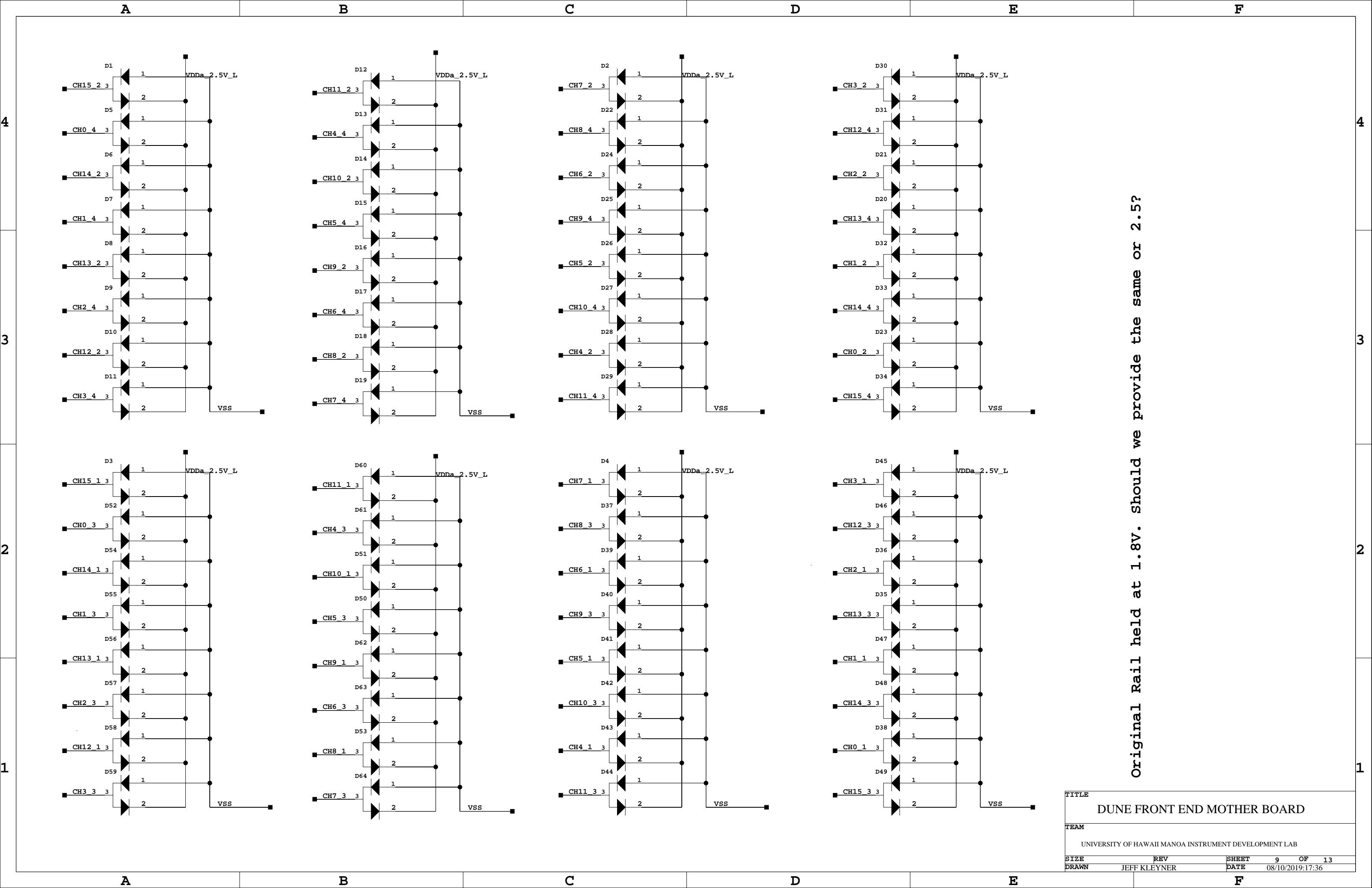
4

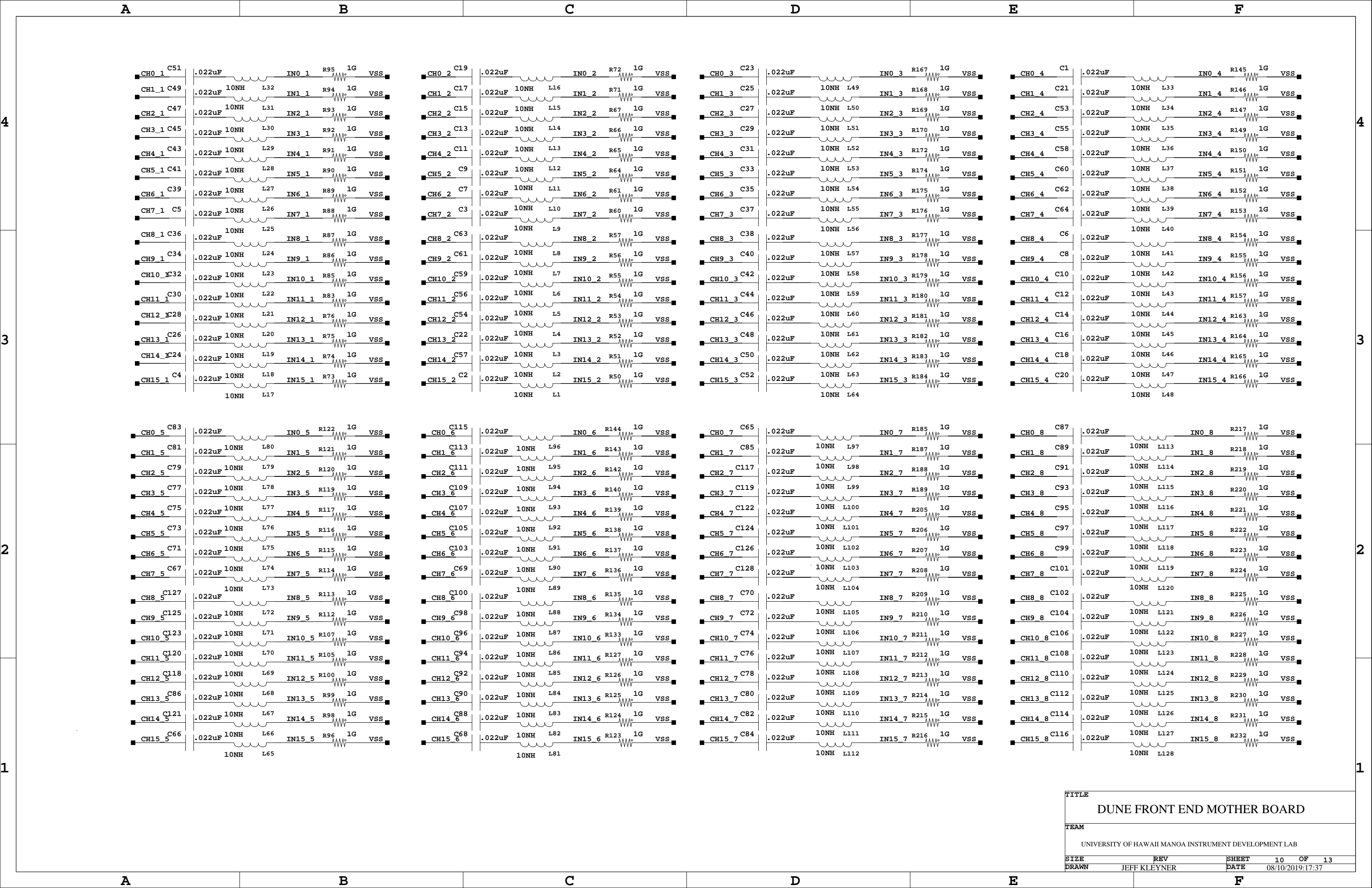


1





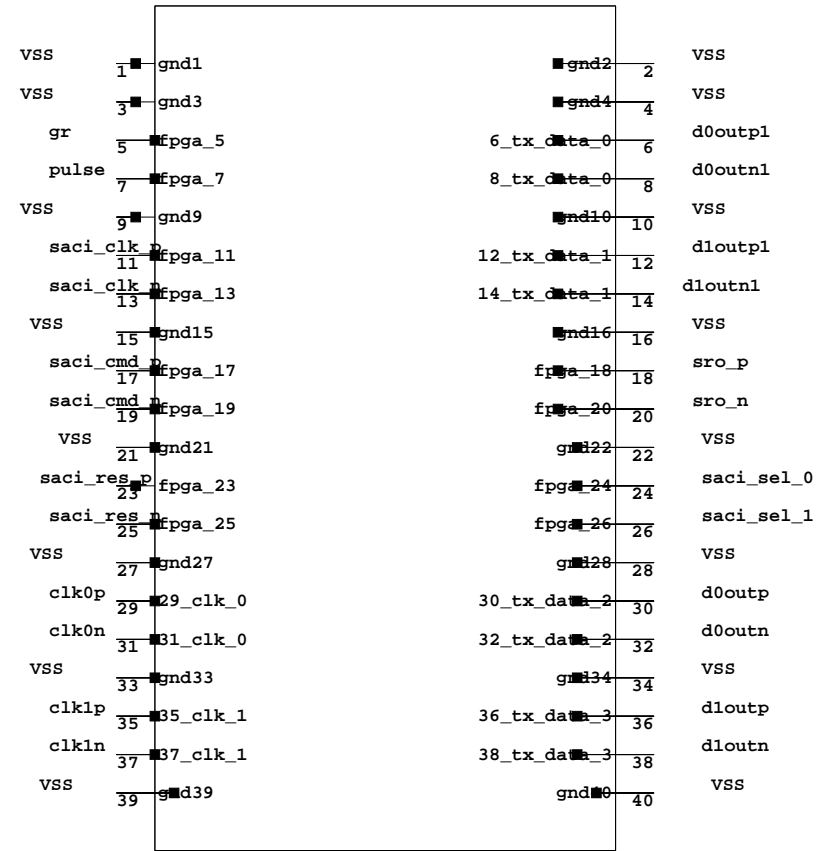








WIB IN



Ext. Monitor

