DUNE FRONT END MOTHERBOARD
University of Hawaii at Manoa IDL
Adapted from Brookhaven Design

 Default Op Conditions

(jumpers)

128 data channels

(walls)

WIB DATA out to WIB
WIB clk to CRYO
WIB SACI to CRYO

(all LDOs and power options have enable pins and jumpers to load as needed)
sense lines. Keep resistors close to ASIC in between both

Ground? but keep close to cryo

Left Digital 1V

Right Digital 1V
Left Analog 2.5V

Right Analog 2.5V

Select raw power in or ldo 2.5 out
**Left Digital 2.5V**

**Right Digital 2.5V**

Select raw power in or ldo 2.5 out