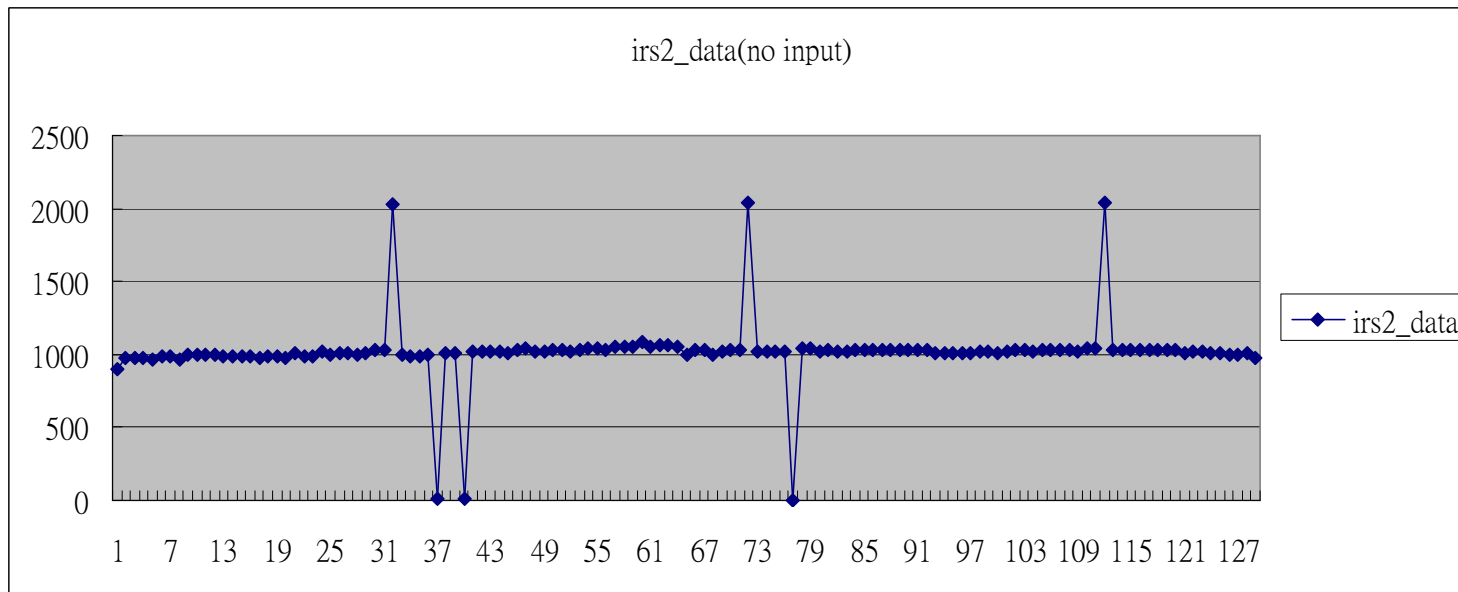


# IRS2 Testing Update (20110511)

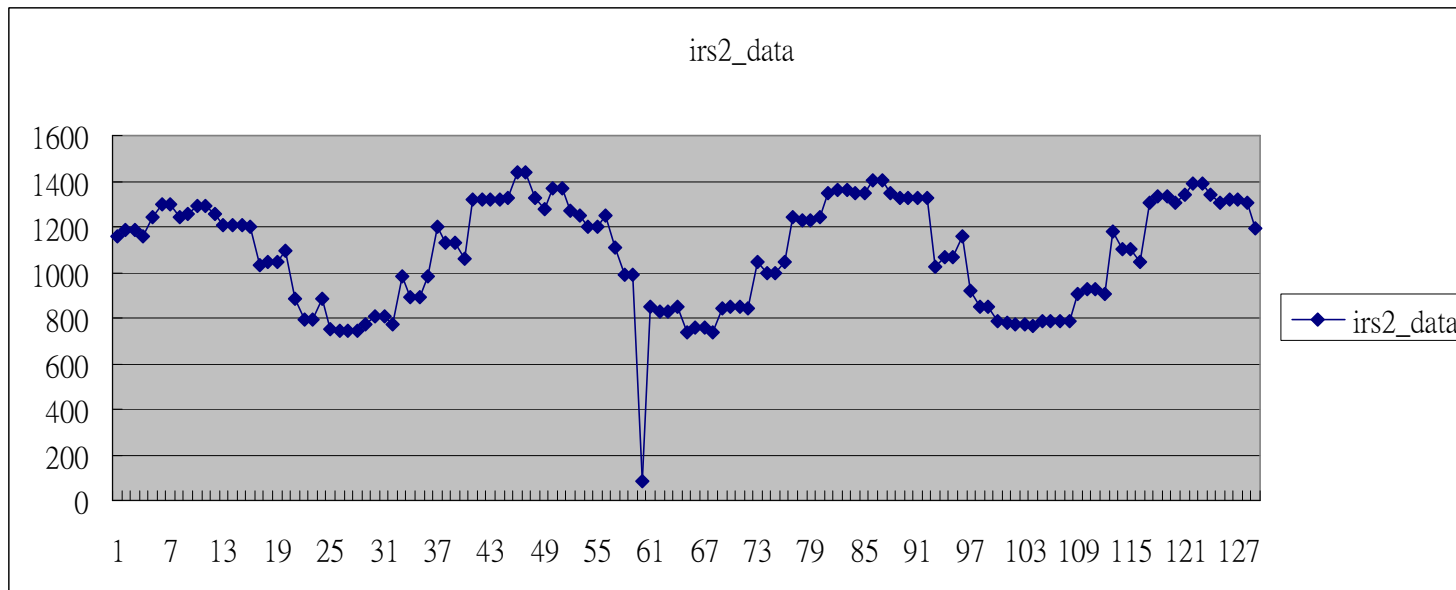
- Wilkinson module and read module are complete.
- Export ADC counts (raw data) from Chipscope (displayed on Microsoft Excel)
- Further Test:
  - Memory module and Pedestal readout module will be added.

# ADC Counts (no Input)



# ADC Counts (Raw Data)

(Input 240MHz 1V<sub>p-p</sub> Sine Wave)



# Adjusted ADC Counts

(Raw Data<sub>240MHz, 1Vp-p</sub> – ADC Counts<sub>no input</sub>)

