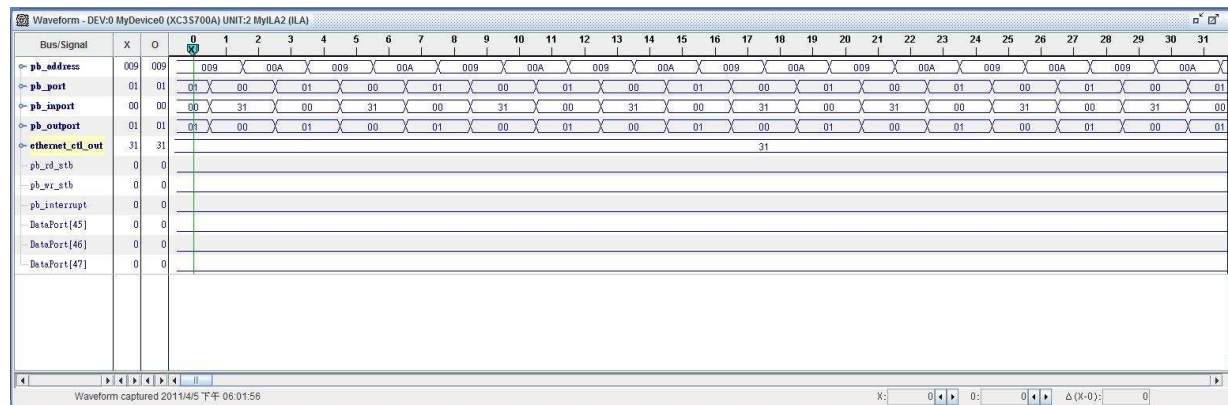


# DDA Testing Update

- Solve 2 problems
  - 25 MHz TSA clock is running.
  - Chipscope is available.



- We are still studying firmware code and adding function to observe IRS2 digitized data using Chipscope.