Progress of IRS Testing

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Progress of Equalizer prototypes and IRS_eval Board

- We received the soldered PCB.
- Some components are waiting for import (from Digi-key)
Testing of IRS 32K Storage Address

• Sampling 4K points storage to 0-4K or 4K-8K or ...
  ... storage address.

• Transfer 4K CH8 IRS data to FPGA.
Access 0-4K and 4K-8K Sampling Point Address by 3MHz Sine Wave 200mVpp
Access 8-12K and 12-16K Sampling Point Address by 3MHz Sine Wave 200mVpp

![Graph](image1.png)

![Graph](image2.png)
Access 16-18K and 20-24K Sampling Point Address by 3MHz Sine Wave 200mVpp
Access 24-28K and 28-32K Sampling Point Address by 3MHz Sine Wave 200mVpp
Some Data Fail...

two trigger events at same 24K-28K address