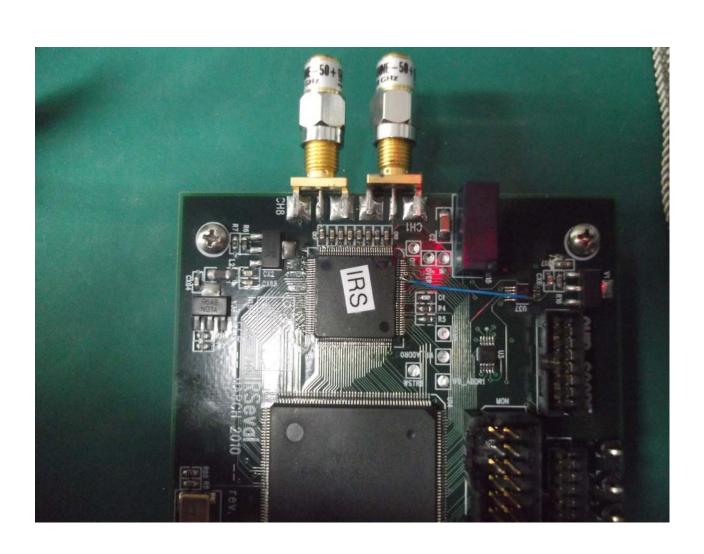
# IRS Chip Testing Report 09/09/2010

Chih-Ching Chen and Chiu-Chuan Ming-Yuan

# status of the tests we discussed in Hawaii

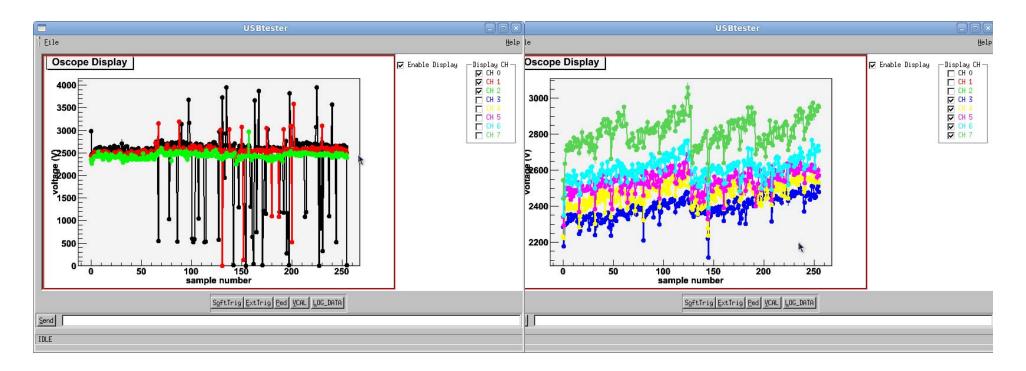
- Find a data value set for testing
- Replace a new IRS chip
- Voltage cal.
- Storage access testing for all 32K (ongoing)
  Will do:
- Sample timing calibration for one channel
- Sampling timing calibration by Avtech pulse
- Change CMPbias resister

## Replace a new IRS



#### testing details and difficulties

 The CHO-CH2 signal are bad after 64 sampling points in new IRS chip. New TSA value?



### questions

The IRS testing board layout file