Univeristy of Hawaii, HEPG, IDL

TITLE: Level setting amp for 1st ADC/LVDS

DRAWN: 2-14-05
CHECKED: 2-15-05
QUALITY CONTROL: 2-16-05
RELEASED: 2-17-05

Design: JWK

DRAWING NO: A
REV: A

SHEET: 3 OF 8
Please note: our labeling is different from convention of chip.

Stage one LVDS conversion sheet
Univeristy of Hawaii, HEPG, IDL

Title: Level setting amp for 2nd ADC/LVDS

Drawn: 2-14-05

Checked: Dated:

Quality Control: Dated:

Released: Dated:

Design: JWK

Size: DRAWING NO:

Sheet: 5 of 8
Please note: Our labeling convention is different from the chip convention.