Course Syllabus: Physics 476 Section 1, Spring Semester 2023

Instructor: Dr. Gary Varner

week date Lecture topics Laboratory topics 1 10-Jan electronics packaging soldering and hand wiring assembly techniques prototyping and wire-bonding 12-Jan 2 17-Jan Intro to PCB technology Ex. 0: CAD/CAM 19-Jan Schematic capture (I) Ex.1: symbols and hierarchy 3 24-Jan Schematic capture (II) Ex. 2: wiring, busses, netlists Schematic capture (III) Ex. 3: parts/footprints 26-Jan 4 31-Jan Design management Ex. 4: Libraries and part creation 2-Feb [Electronic standards] Ex. 5: Placement 5 7-Feb Lavout (I) Ex. 6: Routing 9-Feb Layout (II) Ex. 7: Power and area fills 6 14-Feb Layout (III) Ex. 8: Artwork generation Design review/submission 16-Feb Ex. 9: da BOM 7 21-Feb Intro to Programmable Logic Ex. 10: CAD tools 23-Feb FGPAs (I) Ex. 11: design entry FGPAs (II) 8 28-Feb Ex. 12: user constraints VHDL language 2-Mar Ex. 13: VHDL Verilog language Ex. 14: Verilog 9 7-Mar Logical simulation Ex. 15: State table simulation 9-Mar 10 14-Mar SPRING SPRING 16-Mar BREAK BREAK 11 21-Mar Intro to analog simulation Ex. 16: models SPICE simulation 23-Mar Ex. 17: accuracy, convergence **Conceptual Design Preparation** parts specification 12 28-Mar 30-Mar Intro to VLSI design **Conceptual Design Review** 13 4-Apr Bipolar vs. CMOS Ex. 18: NAND and NOR **Technical Design Preparation** 6-Apr Ex. 19: Amplifiers, parasitics Large system design 14 11-Apr **Technical Design Review** 13-Apr MOSIS and Fab. Processes Ex. 20: DRC and submission 15 18-Apr Project Theory (I) Project work 20-Apr **Critical Design Preparation** Project work 16 25-Apr Project Theory (II) **Critical Design Review** 27-Apr **Deadline Management** Project work 17 2-May Final Design Review Prep. **Final Design Review** Project work 4-May The Success of Failures 18 TBD Tentative: Final Presentations -- 9:45 - 11:45 am

Lecture Hours: T Th 10:30- 11:30am, Lab T Th 13:00 - 15:00 Watanabe 214

Prerequisite: Physics 475, equivalent or premission from instructor

Texts: Horowitz & Hill: The Art of Electronics Johnson & Graham: High-Speed Digital Design: A Handbook of Black Magic

Office hours: WAT333 M 10-11, any afternoon in WAT214 by appointment email: <u>varner@phys.hawaii.edu</u>

Exercises: Mandatory -- must be completed prior to next lab session

Grading: 20% Exercises

40% Final project

20% Final presenation

20% Final report